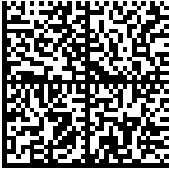
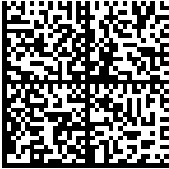


-1-

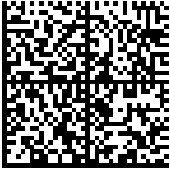
AMD.FC762



AMD.FC762

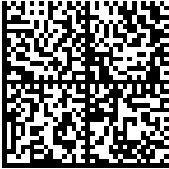


AMD.FC762

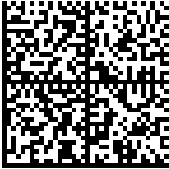


-4-

AMD.FC762

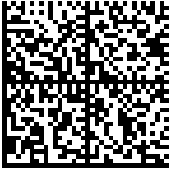


AMD.FC762



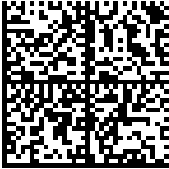
-6-

AMD.FC762



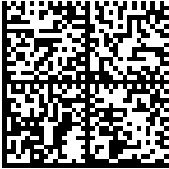
AMD.FC762

-8-

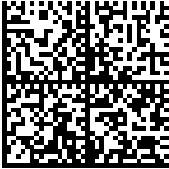


AMD.FC762

-9-

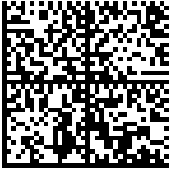


AMD.FC762

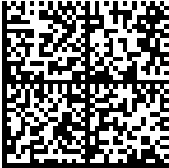


AMD.FC762

-10-

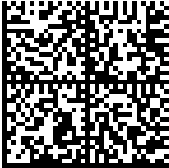


AMD.FC762

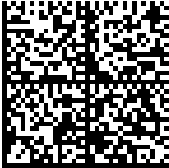


AMD.FC762

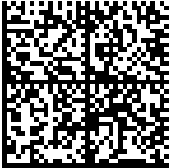
-12-



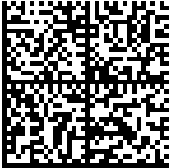
AMD.FC762



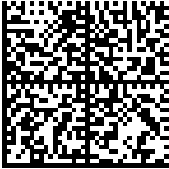
AMD.FC762



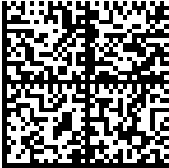
AMD.FC762



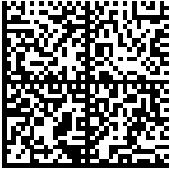
AMD.FC762



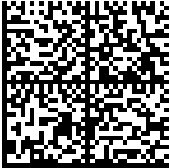
AMD.FC762



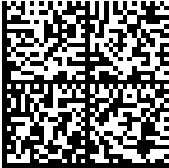
AMD.FC762



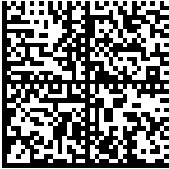
AMD.FC762



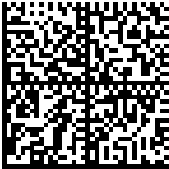
AMD.FC762



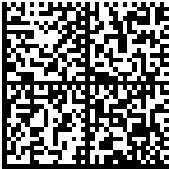
AMD.FC762



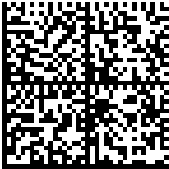
AMD.FC762



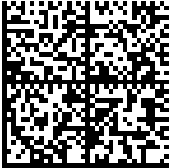
AMD.FC762



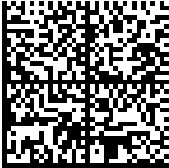
AMD.FC762



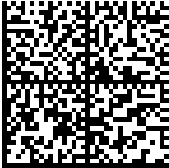
AMD.FC762



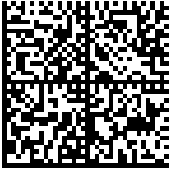
AMD.FC762



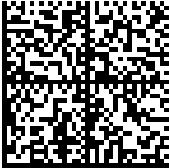
AMD.FC762



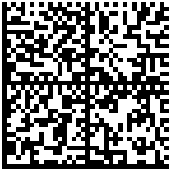
AMD.FC762



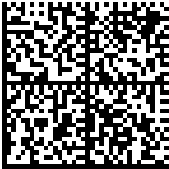
AMD.FC762



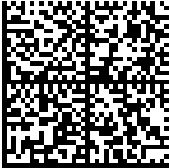
AMD.FC762



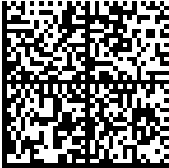
AMD.FC762



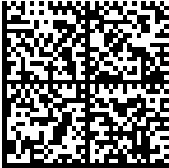
AMD.FC762



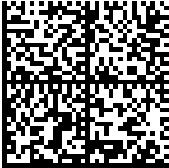
AMD.FC762



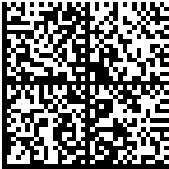
AMD.FC762



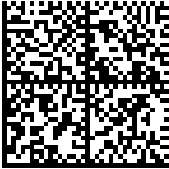
AMD.FC762



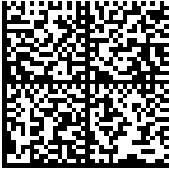
AMD.FC762



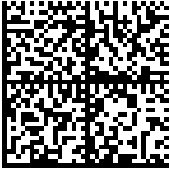
AMD.FC762



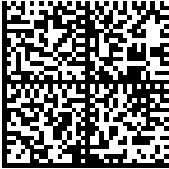
AMD.FC762



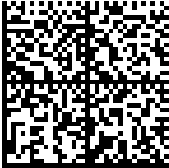
AMD.FC762



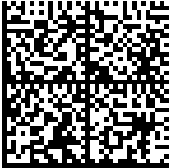
AMD.FC762



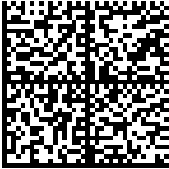
AMD.FC762



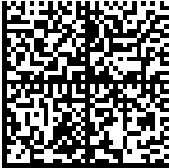
AMD.FC762



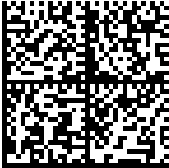
AMD.FC762



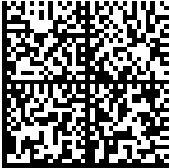
AMD.FC762



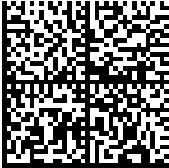
AMD.FC762



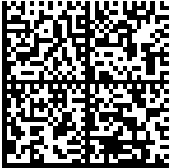
AMD.FC762



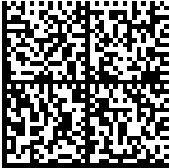
AMD.FC762



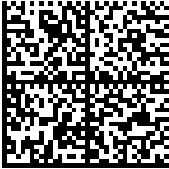
AMD.FC762



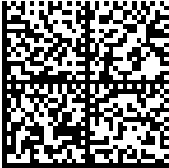
AMD.FC762



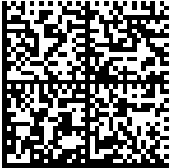
AMD.FC762



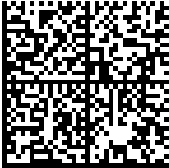
AMD.FC762



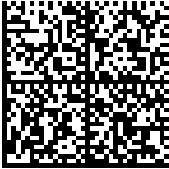
AMD.FC762



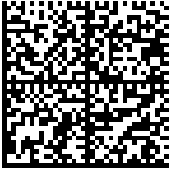
AMD.FC762



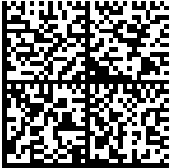
AMD.FC762



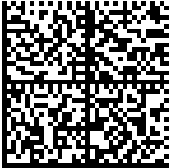
AMD.FC762



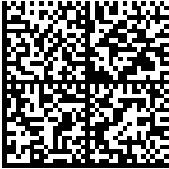
AMD.FC762



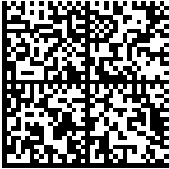
AMD.FC762



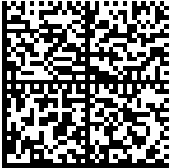
AMD.FC762



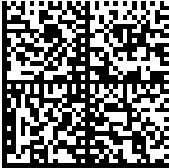
AMD.FC762



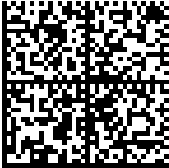
AMD.FC762



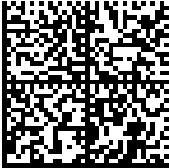
AMD.FC762



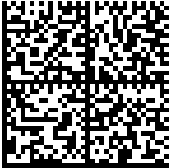
AMD.FC762



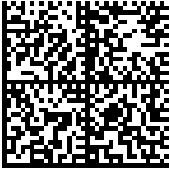
AMD.FC762



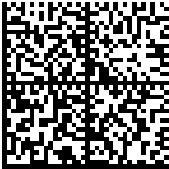
AMD.FC762



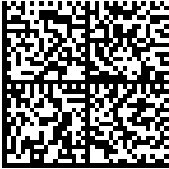
AMD.FC762



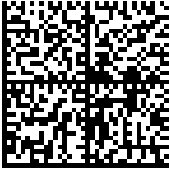
AMD.FC762



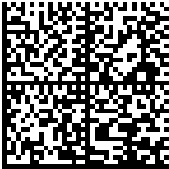
AMD.FC762



AMD.FC762

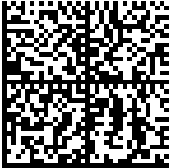


AMD.FC762

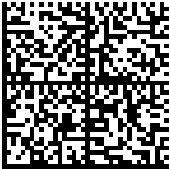


AMD.FC762

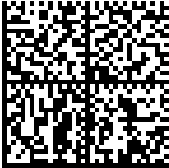
-70-



AMD.FC762

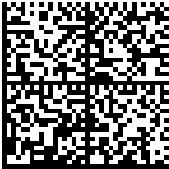


AMD.FC762

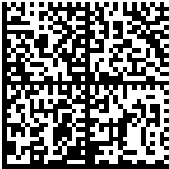


AMD.FC762

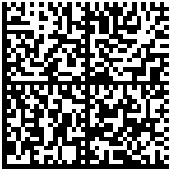
-73-



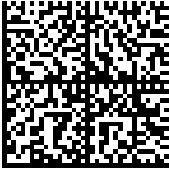
AMD.FC762



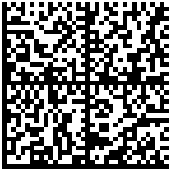
AMD.FC762



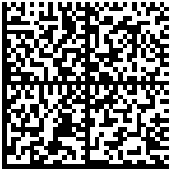
AMD.FC762



AMD.FC762

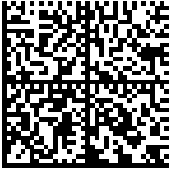


AMD.FC762

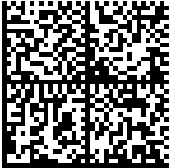


AMD.FC762

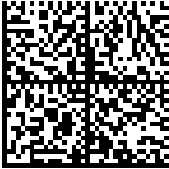
-79-



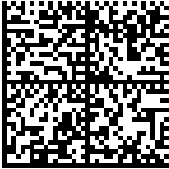
AMD.FC762



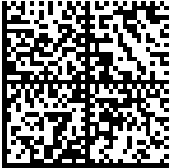
AMD.FC762



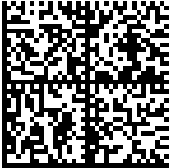
AMD.FC762



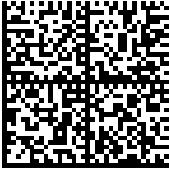
AMD.FC762



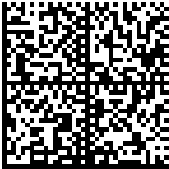
AMD.FC762



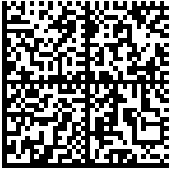
AMD.FC762



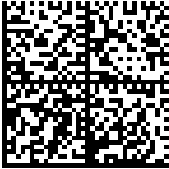
AMD.FC762



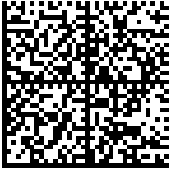
AMD.FC762



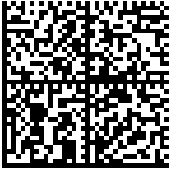
AMD.FC762



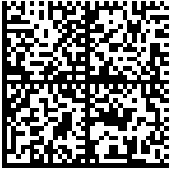
AMD.FC762



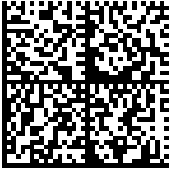
AMD.FC762



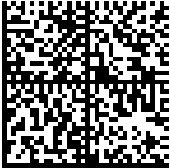
AMD.FC762



AMD.FC762



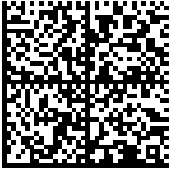
AMD.FC762



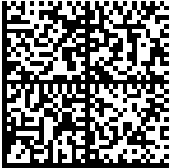
AMD.FC762



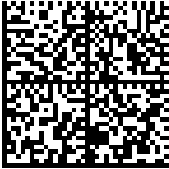
AMD.FC762



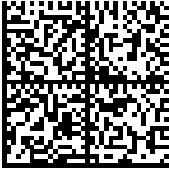
AMD.FC762



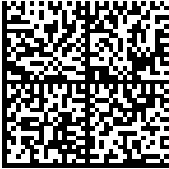
AMD.FC762



AMD.FC762

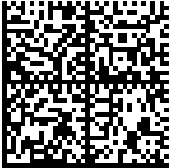


AMD.FC762



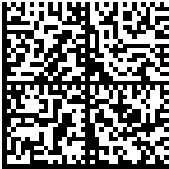
AMD.FC762

-100-



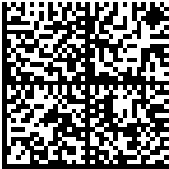
AMD.FC762

-101-



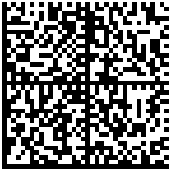
AMD.FC762

-102-



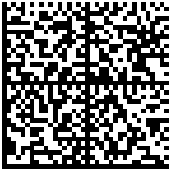
AMD.FC762

-103-

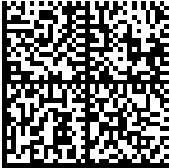


AMD.FC762

-104-

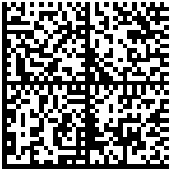


AMD.FC762

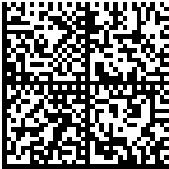


AMD.FC762

-106-

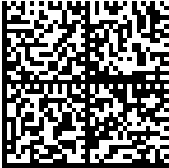


AMD.FC762



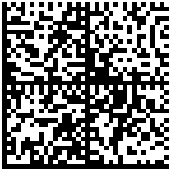
AMD.FC762

-108-



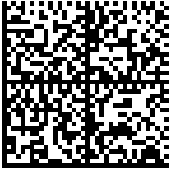
AMD.FC762

-109-



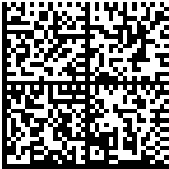
AMD.FC762

-110-



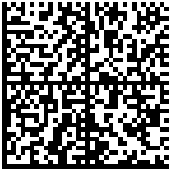
AMD.FC762

-111-



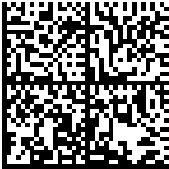
AMD.FC762

-112-



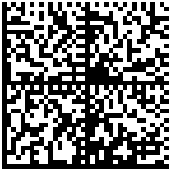
AMD.FC762

-113-



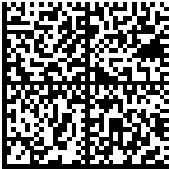
AMD.FC762

-114-



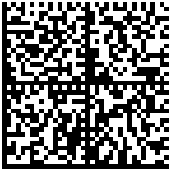
AMD.FC762

-115-



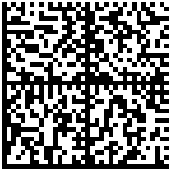
AMD.FC762

-116-



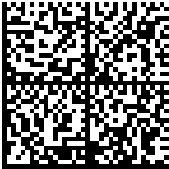
AMD.FC762

-117-



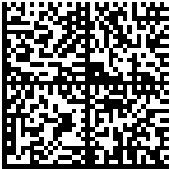
AMD.FC762

-118-



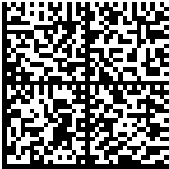
AMD.FC762

-119-



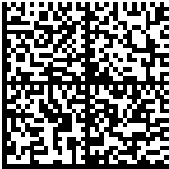
AMD.FC762

-120-



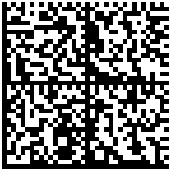
AMD.FC762

-121-



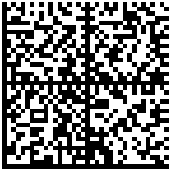
AMD.FC762

-122-



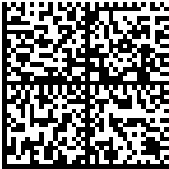
AMD.FC762

-123-



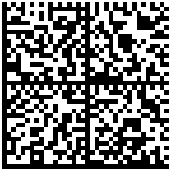
AMD.FC762

-124-

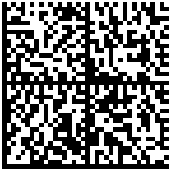


AMD.FC762

-125-

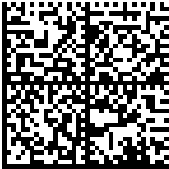


AMD.FC762



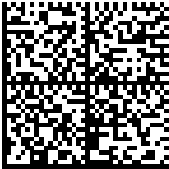
AMD.FC762

-127-



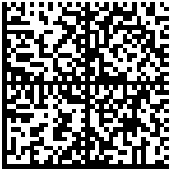
AMD.FC762

-128-



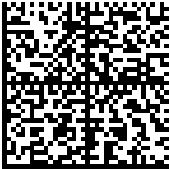
AMD.FC762

-129-



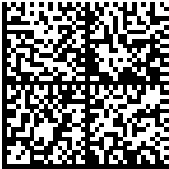
AMD.FC762

-130-



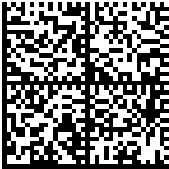
AMD.FC762

-131-



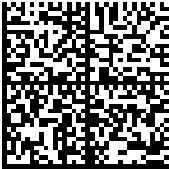
AMD.FC762

-132-



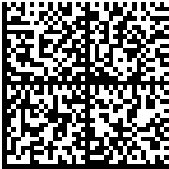
AMD.FC762

-133-



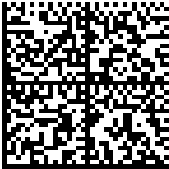
AMD.FC762

-134-



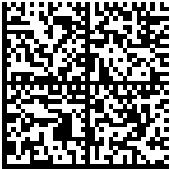
AMD.FC762

-135-



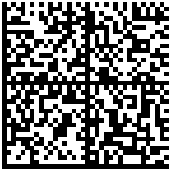
AMD.FC762

-136-



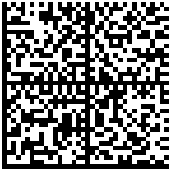
AMD.FC762

-137-



AMD.FC762

-138-

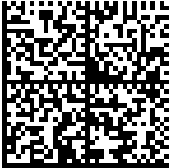


AMD.FC762

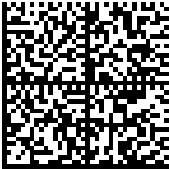
-139-



AMD.FC762

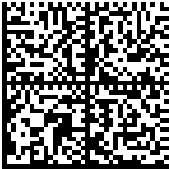


AMD.FC762



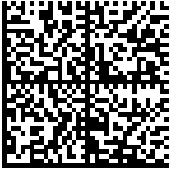
AMD.FC762

-142-

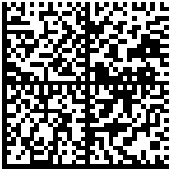


AMD.FC762

-143-

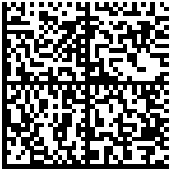


AMD.FC762



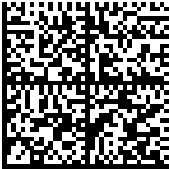
AMD.FC762

-145-



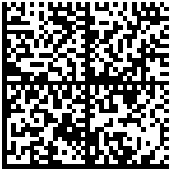
AMD.FC762

-146-



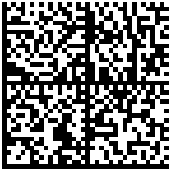
AMD.FC762

-147-

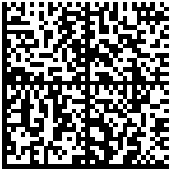


AMD.FC762

-148-

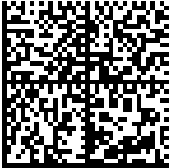


AMD.FC762



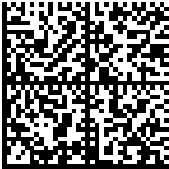
AMD.FC762

-150-



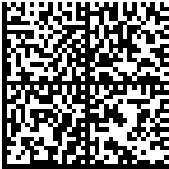
AMD.FC762

-151-



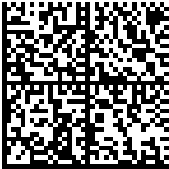
AMD.FC762

-152-



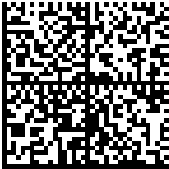
AMD.FC762

-153-



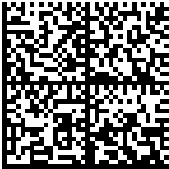
AMD.FC762

-154-



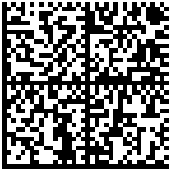
AMD.FC762

-155-



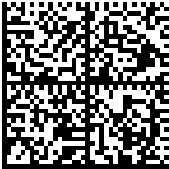
AMD.FC762

-156-



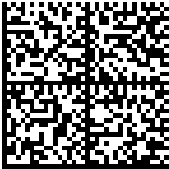
AMD.FC762

-157-



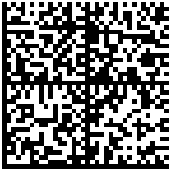
AMD.FC762

-158-



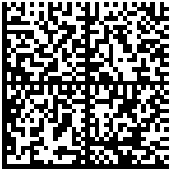
AMD.FC762

-159-

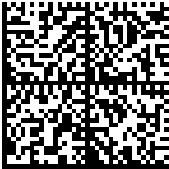


AMD.FC762

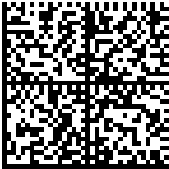
-160-



AMD.FC762

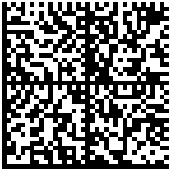


AMD.FC762



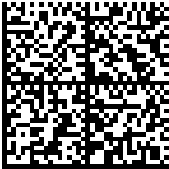
AMD.FC762

-163-



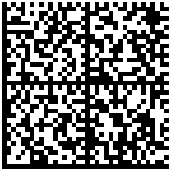
AMD.FC762

-164-



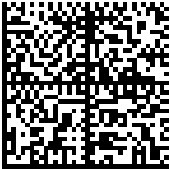
AMD.FC762

-165-



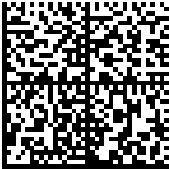
AMD.FC762

-166-



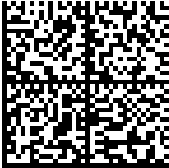
AMD.FC762

-167-



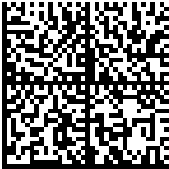
AMD.FC762

-168-



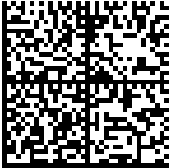
AMD.FC762

-169-



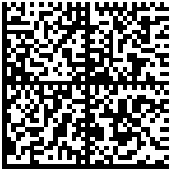
AMD.FC762

-170-



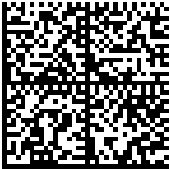
AMD.FC762

-171-



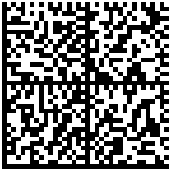
AMD.FC762

-172-



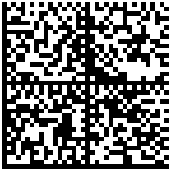
AMD.FC762

-173-



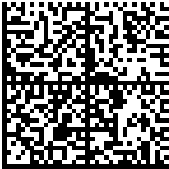
AMD.FC762

-174-



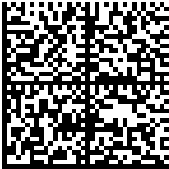
AMD.FC762

-175-



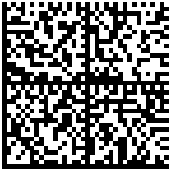
AMD.FC762

-176-



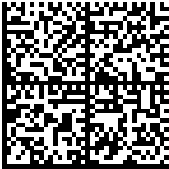
AMD.FC762

-177-

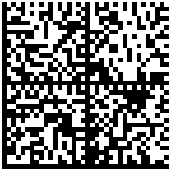


AMD.FC762

-178-

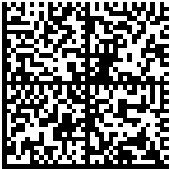


AMD.FC762



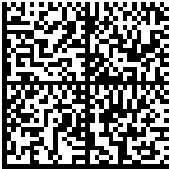
AMD.FC762

-180-



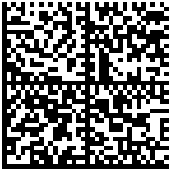
AMD.FC762

-181-



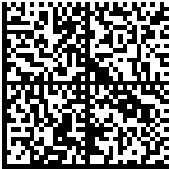
AMD.FC762

-182-



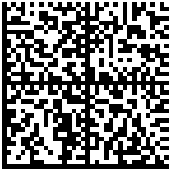
AMD.FC762

-183-



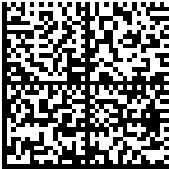
AMD.FC762

-184-



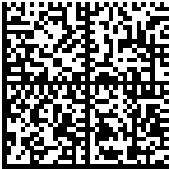
AMD.FC762

-185-



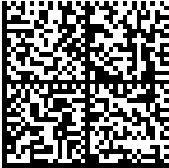
AMD.FC762

-186-



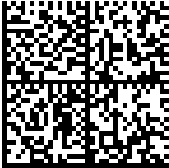
AMD.FC762

-187-



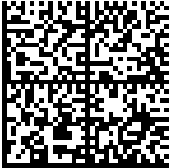
AMD.FC762

-188-



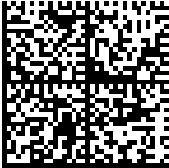
AMD.FC762

-189-



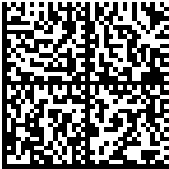
AMD.FC762

-190-



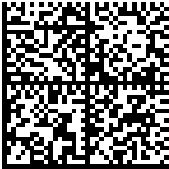
AMD.FC762

-191-

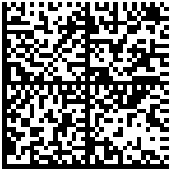


AMD.FC762

-192-

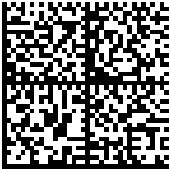


AMD.FC762



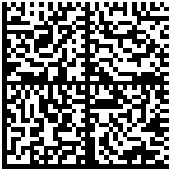
AMD.FC762

-194-



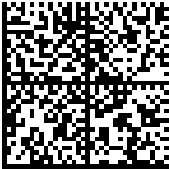
AMD.FC762

-195-



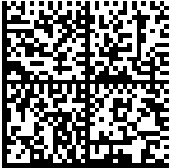
AMD.FC762

-196-

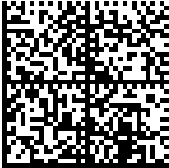


AMD.FC762

-197-

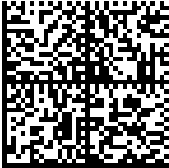


AMD.FC762

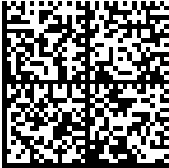


AMD.FC762

-199-

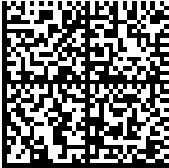


AMD.FC762



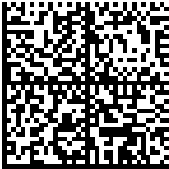
AMD.FC762

-201-



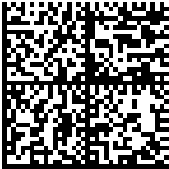
AMD.FC762

-202-



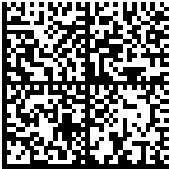
AMD.FC762

-203-



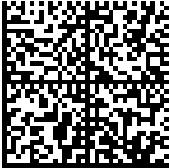
AMD.FC762

-204-

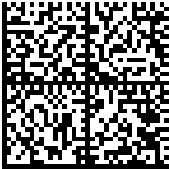


AMD.FC762

-205-

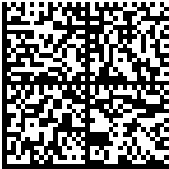


AMD.FC762



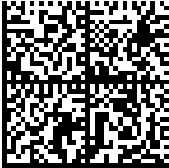
AMD.FC762

-207-

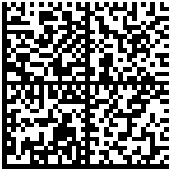


AMD.FC762

-208-

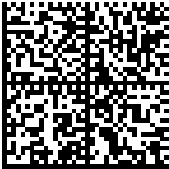


AMD.FC762



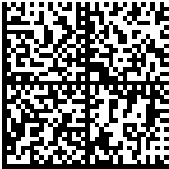
AMD.FC762

-210-



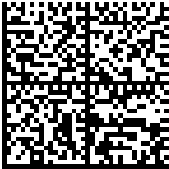
AMD.FC762

-211-



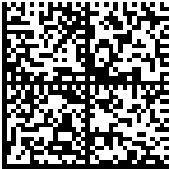
AMD.FC762

-212-



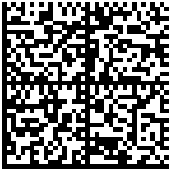
AMD.FC762

-213-



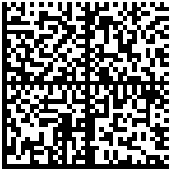
AMD.FC762

-214-



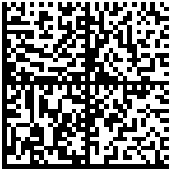
AMD.FC762

-215-



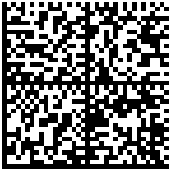
AMD.FC762

-216-



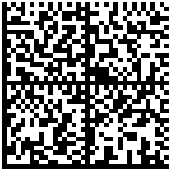
AMD.FC762

-217-



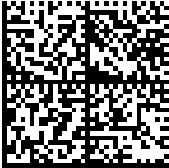
AMD.FC762

-218-



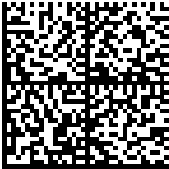
AMD.FC762

-219-



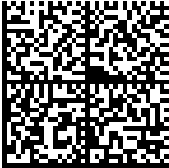
AMD.FC762

-220-



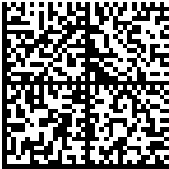
AMD.FC762

-221-



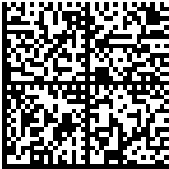
AMD.FC762

-222-



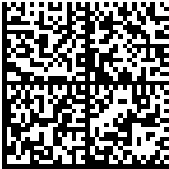
AMD.FC762

-223-



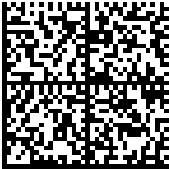
AMD.FC762

-224-

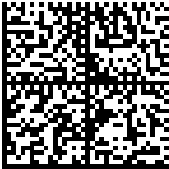


AMD.FC762

-225-

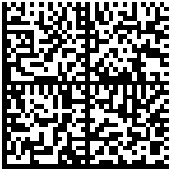


AMD.FC762

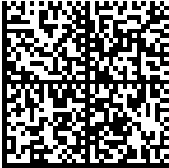


AMD.FC762

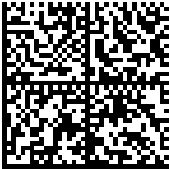
-227-



AMD.FC762

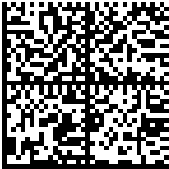


AMD.FC762



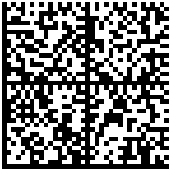
AMD.FC762

-230-



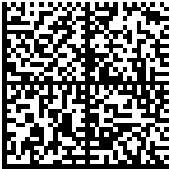
AMD.FC762

-231-



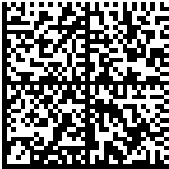
AMD.FC762

-232-



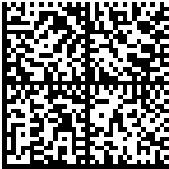
AMD.FC762

-233-

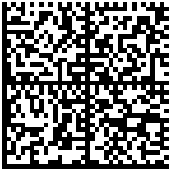


AMD.FC762

-234-

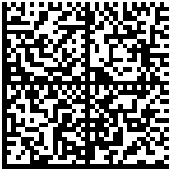


AMD.FC762



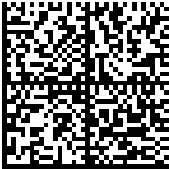
AMD.FC762

-236-



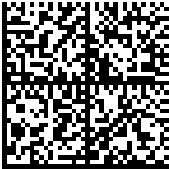
AMD.FC762

-237-



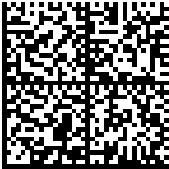
AMD.FC762

-238-



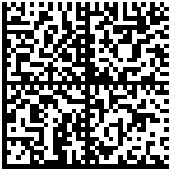
AMD.FC762

-239-

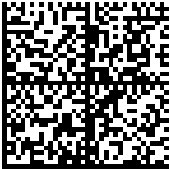


AMD.FC762

-240-

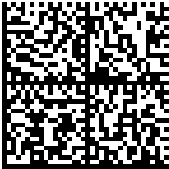


AMD.FC762



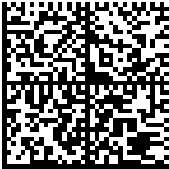
AMD.FC762

-242-



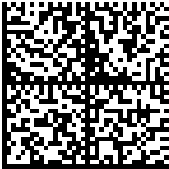
AMD.FC762

-243-



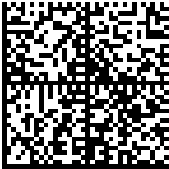
AMD.FC762

-244-



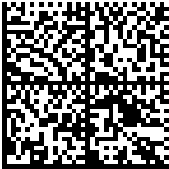
AMD.FC762

-245-



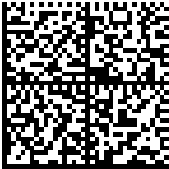
AMD.FC762

-246-



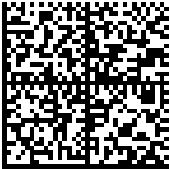
AMD.FC762

-247-



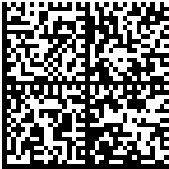
AMD.FC762

-248-



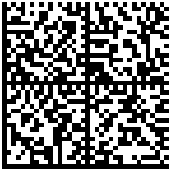
AMD.FC762

-249-



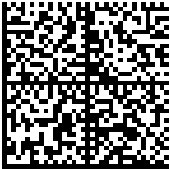
AMD.FC762

-250-



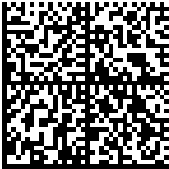
AMD.FC762

-251-



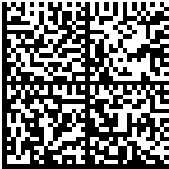
AMD.FC762

-252-



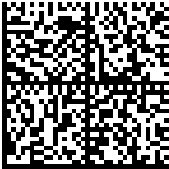
AMD.FC762

-253-



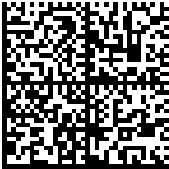
AMD.FC762

-254-



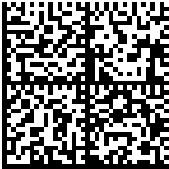
AMD.FC762

-255-



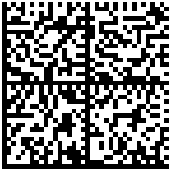
AMD.FC762

-256-



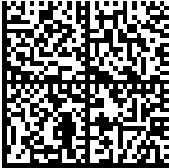
AMD.FC762

-257-



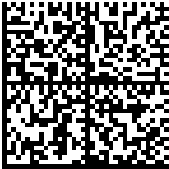
AMD.FC762

-258-



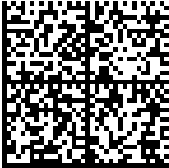
AMD.FC762

-259-

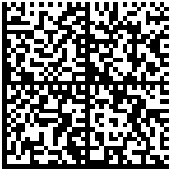


AMD.FC762

-260-

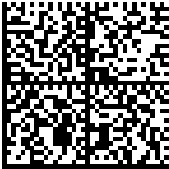


AMD.FC762



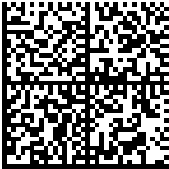
AMD.FC762

-262-



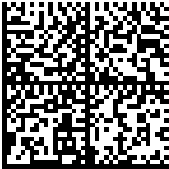
AMD.FC762

-263-

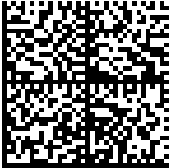


AMD.FC762

-264-

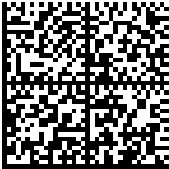


AMD.FC762

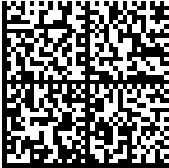


AMD.FC762

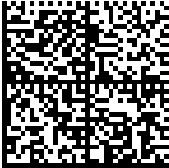
-266-



AMD.FC762

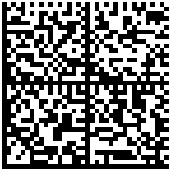


AMD.FC762



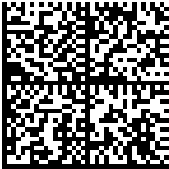
AMD.FC762

-269-



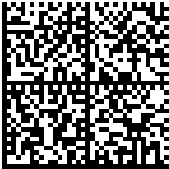
AMD.FC762

-270-



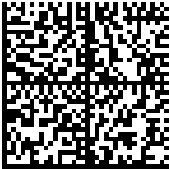
AMD.FC762

-271-



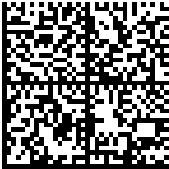
AMD.FC762

-272-

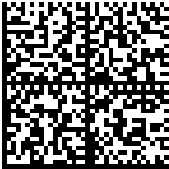


AMD.FC762

-273-

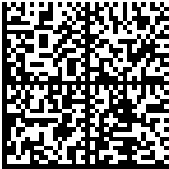


AMD.FC762



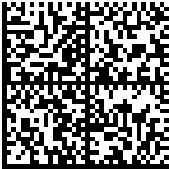
AMD.FC762

-275-



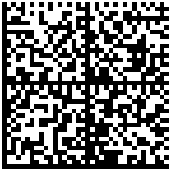
AMD.FC762

-276-



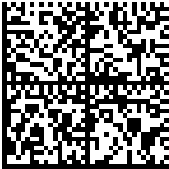
AMD.FC762

-277-



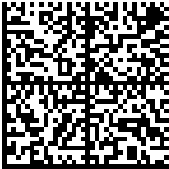
AMD.FC762

-278-



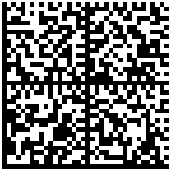
AMD.FC762

-279-

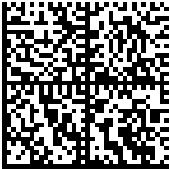


AMD.FC762

-280-

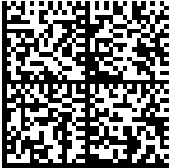


AMD.FC762



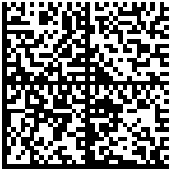
AMD.FC762

-282-

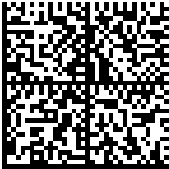


AMD.FC762

-283-

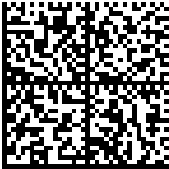


AMD.FC762



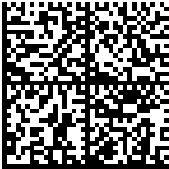
AMD.FC762

-285-



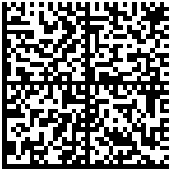
AMD.FC762

-286-



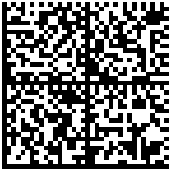
AMD.FC762

-287-

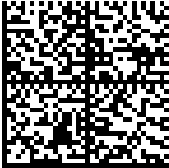


AMD.FC762

-288-

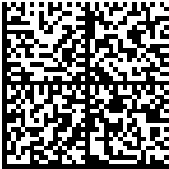


AMD.FC762



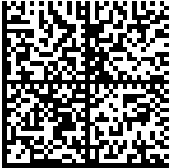
AMD.FC762

-290-



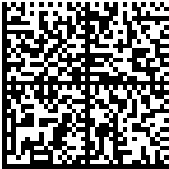
AMD.FC762

-291-



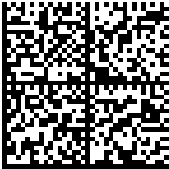
AMD.FC762

-292-

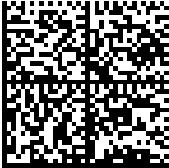


AMD.FC762

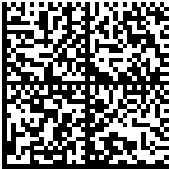
-293-



AMD.FC762

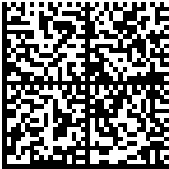


AMD.FC762



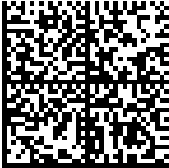
AMD.FC762

-296-



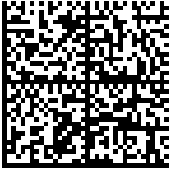
AMD.FC762

-297-



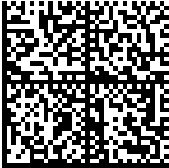
AMD.FC762

-298-



AMD.FC762

-299-



AMD.FC762

-300-