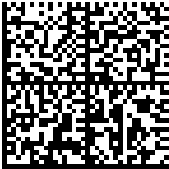


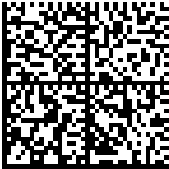
**AMD.FC996**

**(1) 21.08.2026**



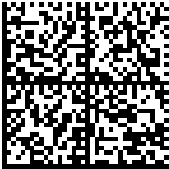
**AMD.FC996**

**(2) 21.08.2026**



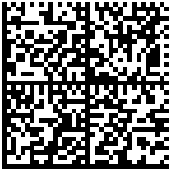
**AMD.FC996**

**(3) 21.08.2026**



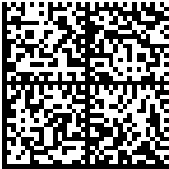
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**(4) 21.08.2026**



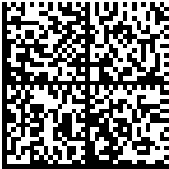
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**(5) 21.08.2026**



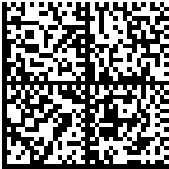
**AMD.FC996**

**(6) 21.08.2026**



**AMD.FC996**

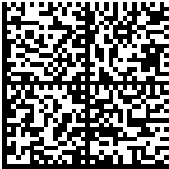
**(7) 21.08.2026**



**AMD.FC996**

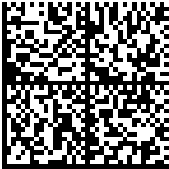
**(8) 21.08.2026**





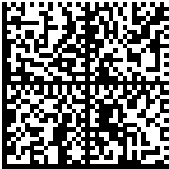
**AMD.FC996**

**(9) 21.08.2026**



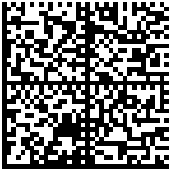
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**(10) 21.08.2026**



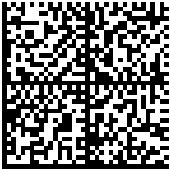
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**(11) 21.08.2026**



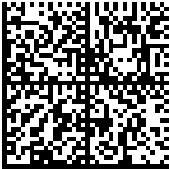
**AMD.FC996**

**(12) 21.08.2026**



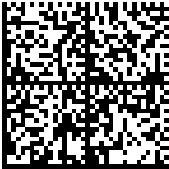
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**(13) 21.08.2026**



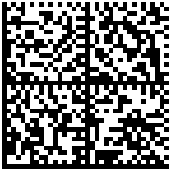
**AMD.FC996**

**(14) 21.08.2026**



**AMD.FC996**

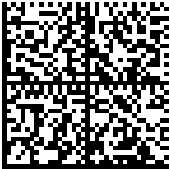
**(15) 21.08.2026**



**AMD.FC996**

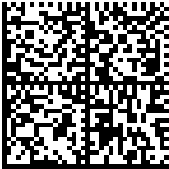
**(16) 21.08.2026**





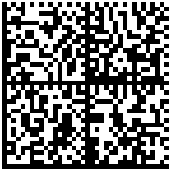
**AMD.FC996**

**(17) 21.08.2026**



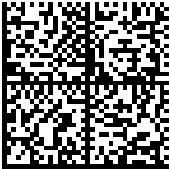
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**(18) 21.08.2026**



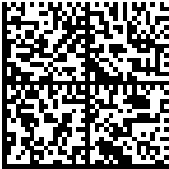
**AMD.FC996**

**(19) 21.08.2026**



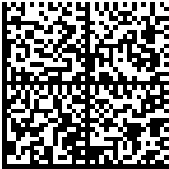
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**(20) 21.08.2026**



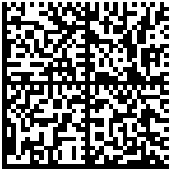
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**(21) 21.08.2026**



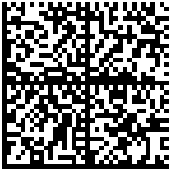
**AMD.FC996**

**(22) 21.08.2026**



**AMD.FC996**

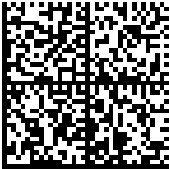
**(23) 21.08.2026**



**AMD.FC996**

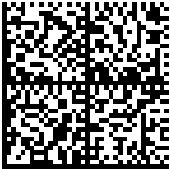
**(24) 21.08.2026**





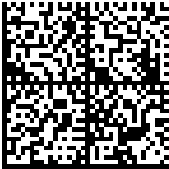
**AMD.FC996**

**(25) 21.08.2026**



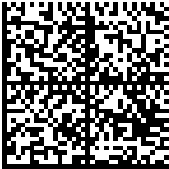
**AMD.FC996**

**(26) 21.08.2026**



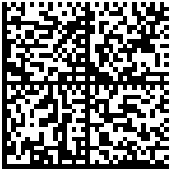
**AMD.FC996**

**(27) 21.08.2026**



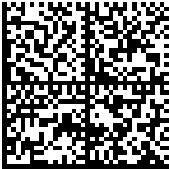
**AMD.FC996**

**(28) 21.08.2026**



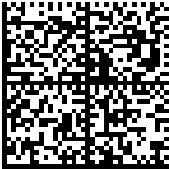
**AMD.FC996**

**(29) 21.08.2026**



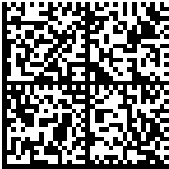
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**(30) 21.08.2026**



**AMD.FC996**

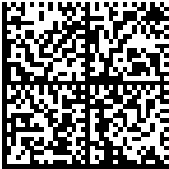
**(31) 21.08.2026**



**AMD.FC996**

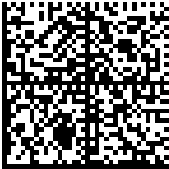
**(32) 21.08.2026**





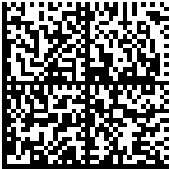
**AMD.FC996**

**(33) 21.08.2026**



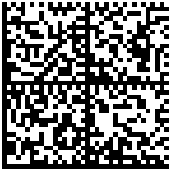
**AMD.FC996**

**(34) 21.08.2026**



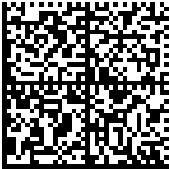
**AMD.FC996**

**(35) 21.08.2026**



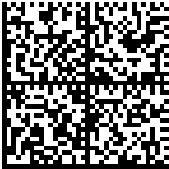
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**(36) 21.08.2026**



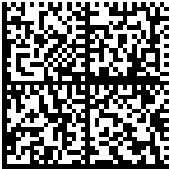
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**(37) 21.08.2026**



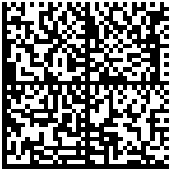
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**(38) 21.08.2026**



**AMD.FC996**

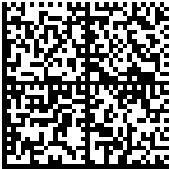
**(39) 21.08.2026**



**AMD.FC996**

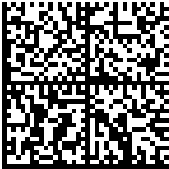
**(40) 21.08.2026**





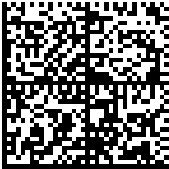
**AMD.FC996**

**(41) 21.08.2026**



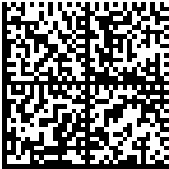
**AMD.FC996**

**(42) 21.08.2026**



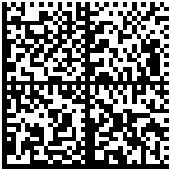
**AMD.FC996**

**(43) 21.08.2026**



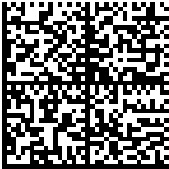
**AMD.FC996**

**(44) 21.08.2026**



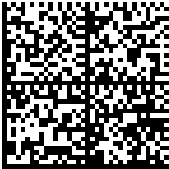
**AMD.FC996**

**(45) 21.08.2026**



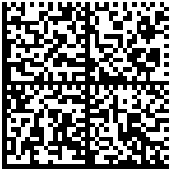
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**(46) 21.08.2026**



**AMD.FC996**

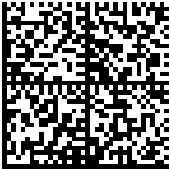
**(47) 21.08.2026**



**AMD.FC996**

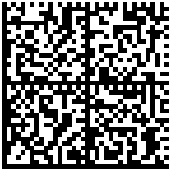
**(48) 21.08.2026**





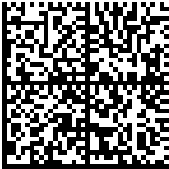
**AMD.FC996**

**(49) 21.08.2026**



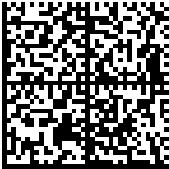
**AMD.FC996**

**(50) 21.08.2026**



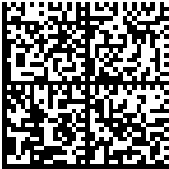
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**(51) 21.08.2026**



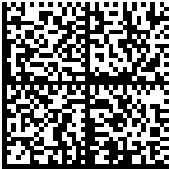
**AMD.FC996**

**(52) 21.08.2026**



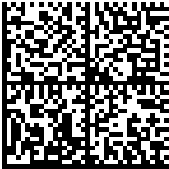
**AMD.FC996**

**(53) 21.08.2026**



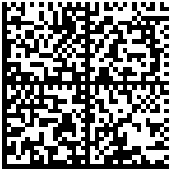
**AMD.FC996**

**(54) 21.08.2026**



**AMD.FC996**

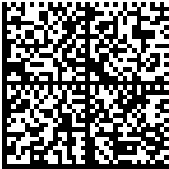
**(55) 21.08.2026**



**AMD.FC996**

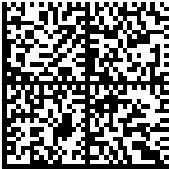
**(56) 21.08.2026**





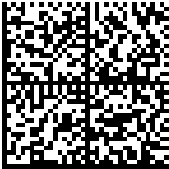
**AMD.FC996**

**(57) 21.08.2026**



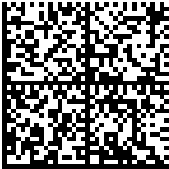
**AMD.FC996**

**(58) 21.08.2026**



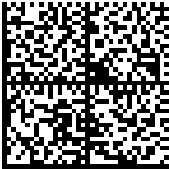
**AMD.FC996**

**(59) 21.08.2026**



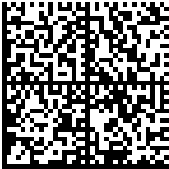
**AMD.FC996**

**(60) 21.08.2026**



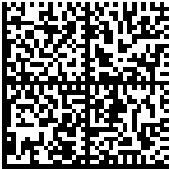
**AMD.FC996**

**(61) 21.08.2026**



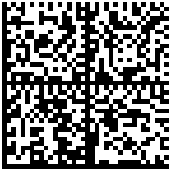
**AMD.FC996**

**(62) 21.08.2026**



**AMD.FC996**

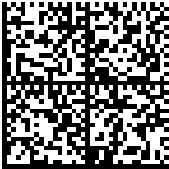
**(63) 21.08.2026**



**AMD.FC996**

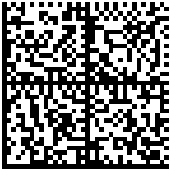
**(64) 21.08.2026**





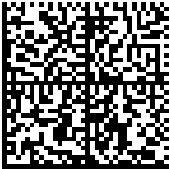
**AMD.FC996**

**(65) 21.08.2026**



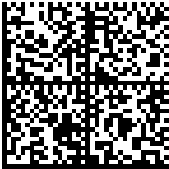
**AMD.FC996**

**(66) 21.08.2026**



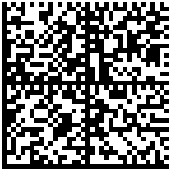
**AMD.FC996**

**(67) 21.08.2026**



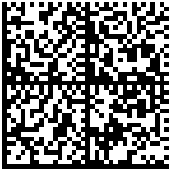
**AMD.FC996**

**(68) 21.08.2026**



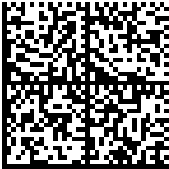
**AMD.FC996**

**(69) 21.08.2026**



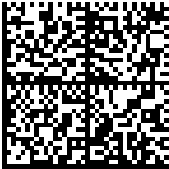
**AMD.FC996**

**(70) 21.08.2026**



**AMD.FC996**

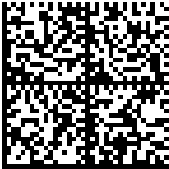
**(71) 21.08.2026**



**AMD.FC996**

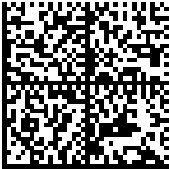
**(72) 21.08.2026**





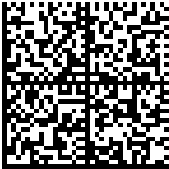
**AMD.FC996**

**(73) 21.08.2026**



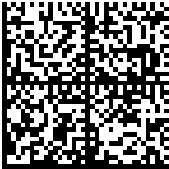
**AMD.FC996**

**(74) 21.08.2026**



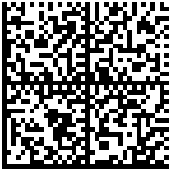
**AMD.FC996**

**(75) 21.08.2026**



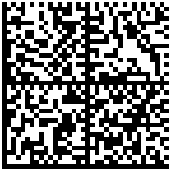
**AMD.FC996**

**(76) 21.08.2026**



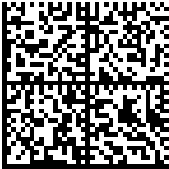
**AMD.FC996**

**(77) 21.08.2026**



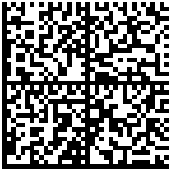
**AMD.FC996**

**(78) 21.08.2026**



**AMD.FC996**

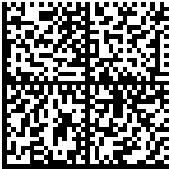
**(79) 21.08.2026**



**AMD.FC996**

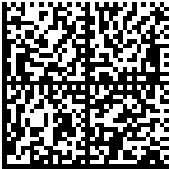
**(80) 21.08.2026**





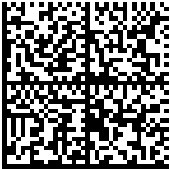
**AMD.FC996**

**(81) 21.08.2026**



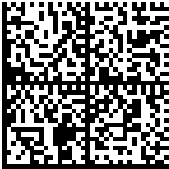
**AMD.FC996**

**(82) 21.08.2026**



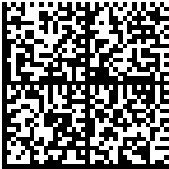
**AMD.FC996**

**(83) 21.08.2026**



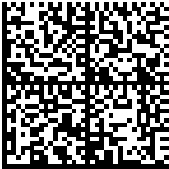
**AMD.FC996**

**(84) 21.08.2026**



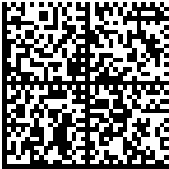
**AMD.FC996**

**(85) 21.08.2026**



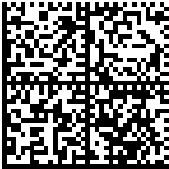
**AMD.FC996**

**(86) 21.08.2026**



**AMD.FC996**

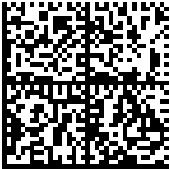
**(87) 21.08.2026**



**AMD.FC996**

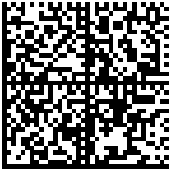
**(88) 21.08.2026**





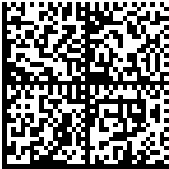
**AMD.FC996**

**(89) 21.08.2026**



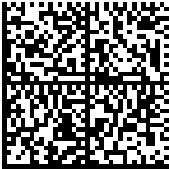
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**(90) 21.08.2026**



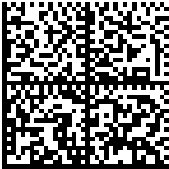
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**(91) 21.08.2026**



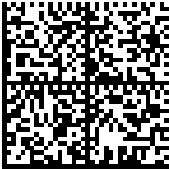
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**(92) 21.08.2026**



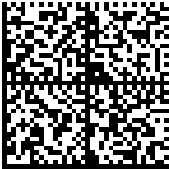
**AMD.FC996**

**(93) 21.08.2026**



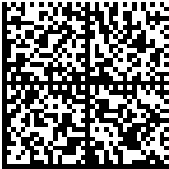
**AMD.FC996**

**(94) 21.08.2026**



**AMD.FC996**

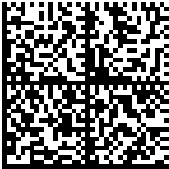
**(95) 21.08.2026**



**AMD.FC996**

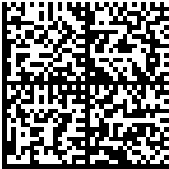
**(96) 21.08.2026**





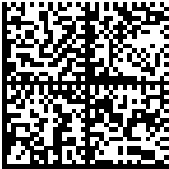
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**(97) 21.08.2026**



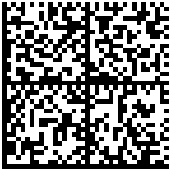
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**(98) 21.08.2026**



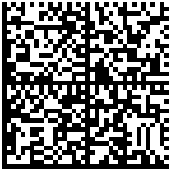
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**(99) 21.08.2026**



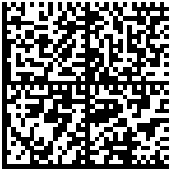
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**(100) 21.08.2026**



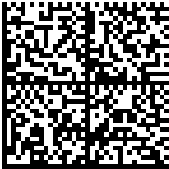
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**(101) 21.08.2026**



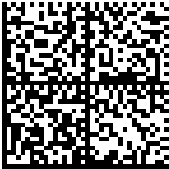
**AMD.FC996**

**(102) 21.08.2026**



**AMD.FC996**

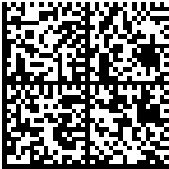
**(103) 21.08.2026**



**AMD.FC996**

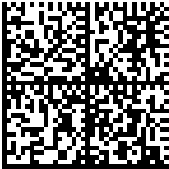
**(104) 21.08.2026**





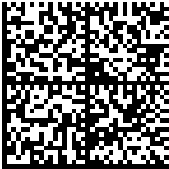
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**(105) 21.08.2026**



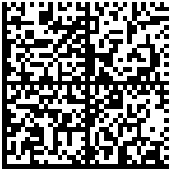
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**(106) 21.08.2026**



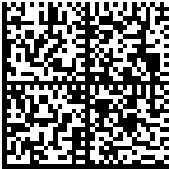
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**(107) 21.08.2026**



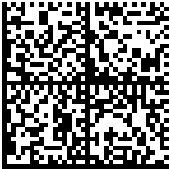
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**(108) 21.08.2026**



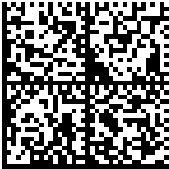
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**(109) 21.08.2026**



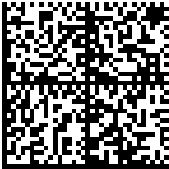
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**(110) 21.08.2026**



**AMD.FC996**

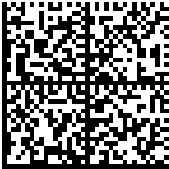
**(111) 21.08.2026**



**AMD.FC996**

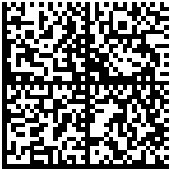
**(112) 21.08.2026**





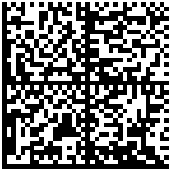
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**(113) 21.08.2026**



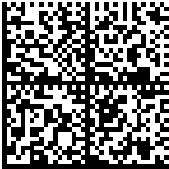
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**(114) 21.08.2026**



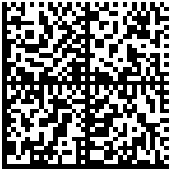
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**(115) 21.08.2026**



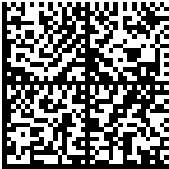
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**(116) 21.08.2026**



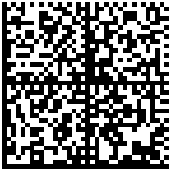
**AMD.FC996**

**(117) 21.08.2026**



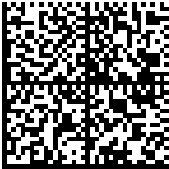
**AMD.FC996**

**(118) 21.08.2026**



**AMD.FC996**

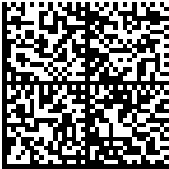
**(119) 21.08.2026**



**AMD.FC996**

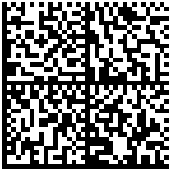
**(120) 21.08.2026**





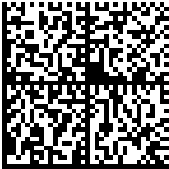
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**(121) 21.08.2026**



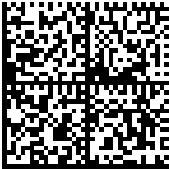
**AMD.FC996**

**(122) 21.08.2026**



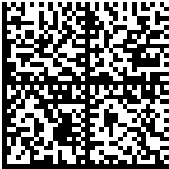
**AMD.FC996**

**(123) 21.08.2026**



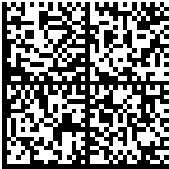
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**(124) 21.08.2026**



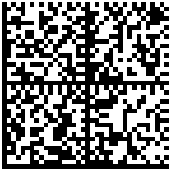
**AMD.FC996**

**(125) 21.08.2026**



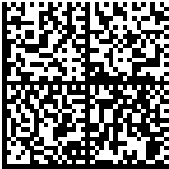
**AMD.FC996**

**(126) 21.08.2026**



**AMD.FC996**

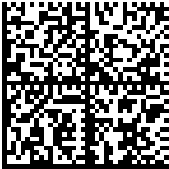
**(127) 21.08.2026**



**AMD.FC996**

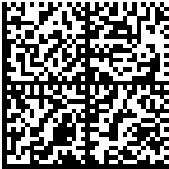
**(128) 21.08.2026**





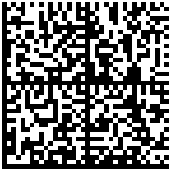
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**(129) 21.08.2026**



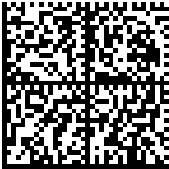
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**(130) 21.08.2026**



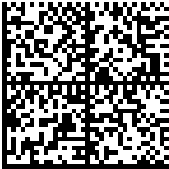
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**(131) 21.08.2026**



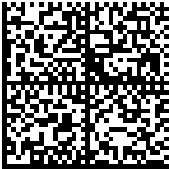
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**(132) 21.08.2026**



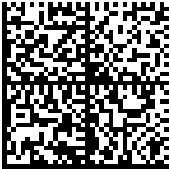
**AMD.FC996**

**(133) 21.08.2026**



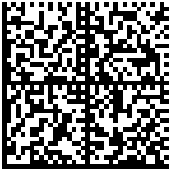
**AMD.FC996**

**(134) 21.08.2026**



**AMD.FC996**

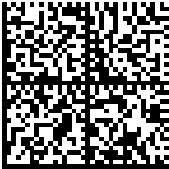
**(135) 21.08.2026**



**AMD.FC996**

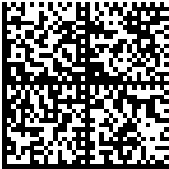
**(136) 21.08.2026**





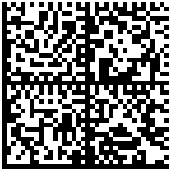
**AMD.FC996**

**(137) 21.08.2026**



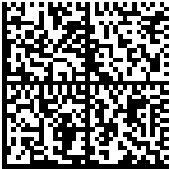
**AMD.FC996**

**(138) 21.08.2026**



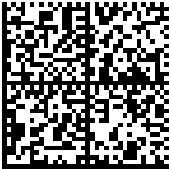
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**(139) 21.08.2026**



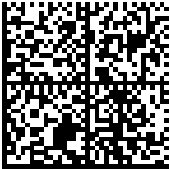
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**(140) 21.08.2026**



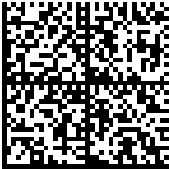
**AMD.FC996**

**(141) 21.08.2026**



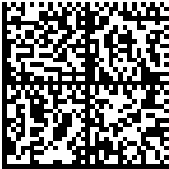
**AMD.FC996**

**(142) 21.08.2026**



**AMD.FC996**

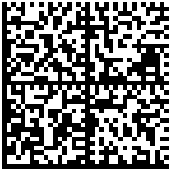
**(143) 21.08.2026**



**AMD.FC996**

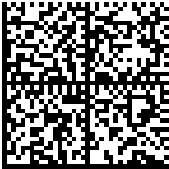
**(144) 21.08.2026**





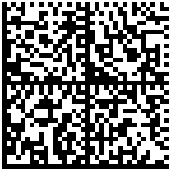
**AMD.FC996**

**(145) 21.08.2026**



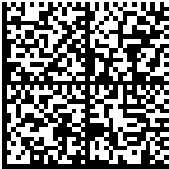
**AMD.FC996**

**(146) 21.08.2026**



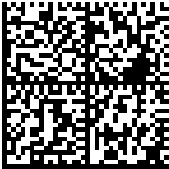
**AMD.FC996**

**(147) 21.08.2026**



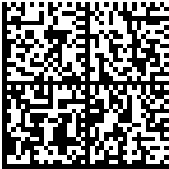
**AMD.FC996**

**(148) 21.08.2026**



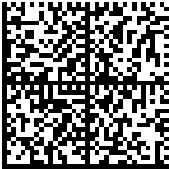
**AMD.FC996**

**(149) 21.08.2026**



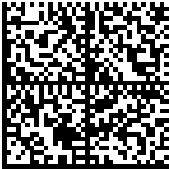
**AMD.FC996**

**(150) 21.08.2026**



**AMD.FC996**

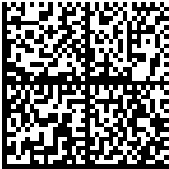
**(151) 21.08.2026**



**AMD.FC996**

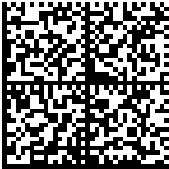
**(152) 21.08.2026**





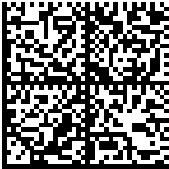
**AMD.FC996**

**(153) 21.08.2026**



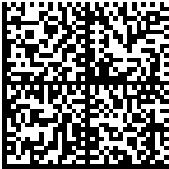
**AMD.FC996**

**(154) 21.08.2026**



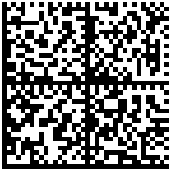
**AMD.FC996**

**(155) 21.08.2026**



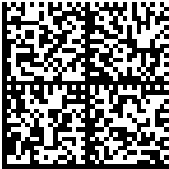
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**(156) 21.08.2026**



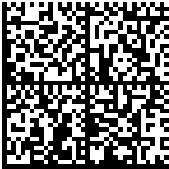
**AMD.FC996**

**(157) 21.08.2026**



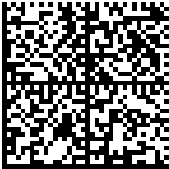
**AMD.FC996**

**(158) 21.08.2026**



**AMD.FC996**

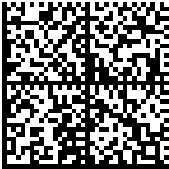
**(159) 21.08.2026**



**AMD.FC996**

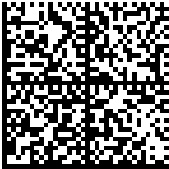
**(160) 21.08.2026**





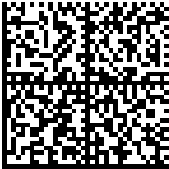
**AMD.FC996**

**(161) 21.08.2026**



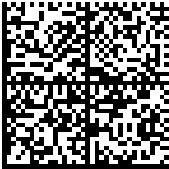
**AMD.FC996**

**(162) 21.08.2026**



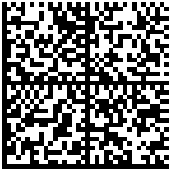
**AMD.FC996**

**(163) 21.08.2026**



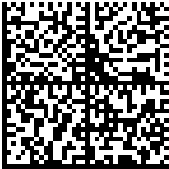
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**(164) 21.08.2026**



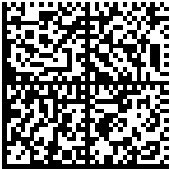
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**(165) 21.08.2026**



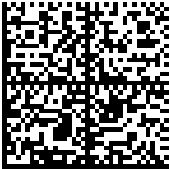
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**(166) 21.08.2026**



**AMD.FC996**

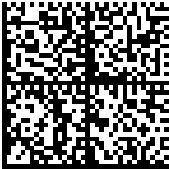
**(167) 21.08.2026**



**AMD.FC996**

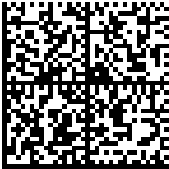
**(168) 21.08.2026**





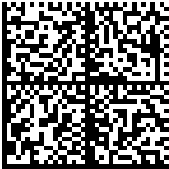
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**(169) 21.08.2026**



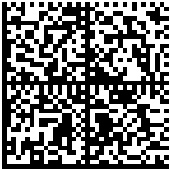
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**(170) 21.08.2026**



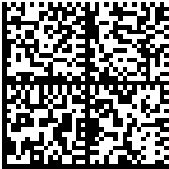
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**(171) 21.08.2026**



**AMD.FC996**

**(172) 21.08.2026**



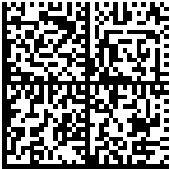
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**(173) 21.08.2026**



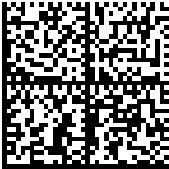
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**(174) 21.08.2026**



**AMD.FC996**

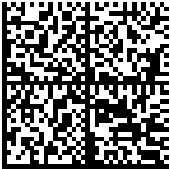
**(175) 21.08.2026**



**AMD.FC996**

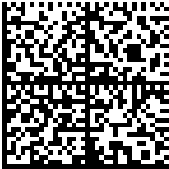
**(176) 21.08.2026**





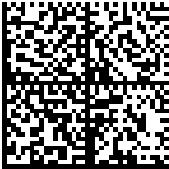
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**(177) 21.08.2026**



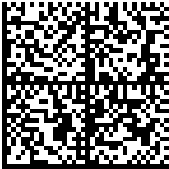
**AMD.FC996**

**(178) 21.08.2026**



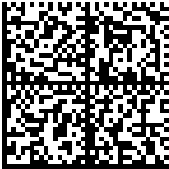
**AMD.FC996**

**(179) 21.08.2026**



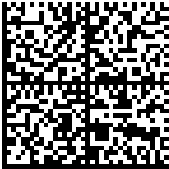
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**(180) 21.08.2026**



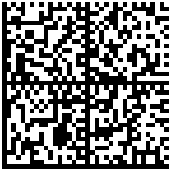
**AMD.FC996**

**(181) 21.08.2026**



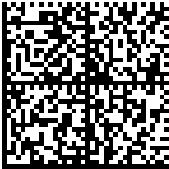
**AMD.FC996**

**(182) 21.08.2026**



**AMD.FC996**

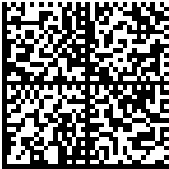
**(183) 21.08.2026**



**AMD.FC996**

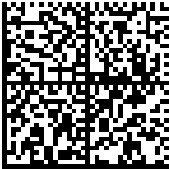
**(184) 21.08.2026**





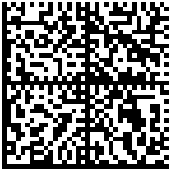
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**(185) 21.08.2026**



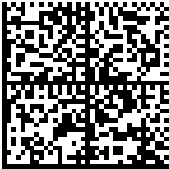
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**(186) 21.08.2026**



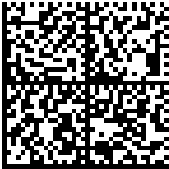
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**(187) 21.08.2026**



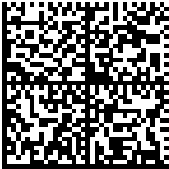
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**(188) 21.08.2026**



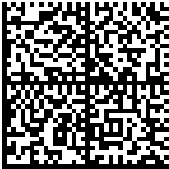
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**(189) 21.08.2026**



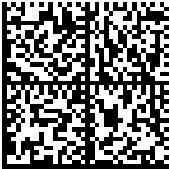
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**(190) 21.08.2026**



**AMD.FC996**

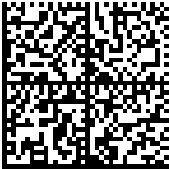
**(191) 21.08.2026**



**AMD.FC996**

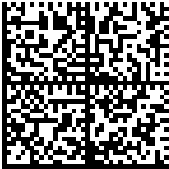
**(192) 21.08.2026**





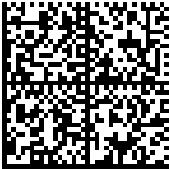
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**(193) 21.08.2026**



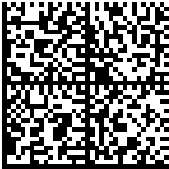
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**(194) 21.08.2026**



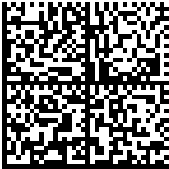
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**(195) 21.08.2026**



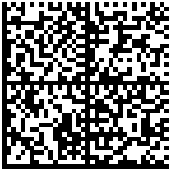
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**(196) 21.08.2026**



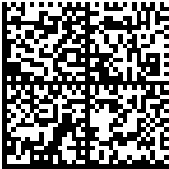
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**(197) 21.08.2026**



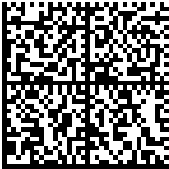
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**AMD.FC996**

**(199) 21.08.2026**



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**(200) 21.08.2026**