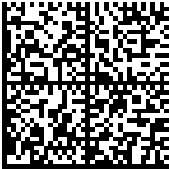


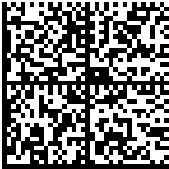
AMD.FP164

(1) 07.08.2026



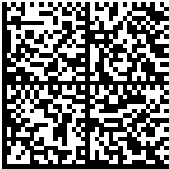
AMD.FP164

(2) 07.08.2026



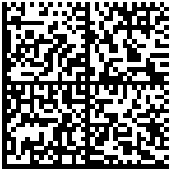
AMD.FP164

(3) 07.08.2026



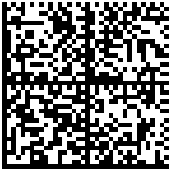
AMD.FP164

(4) 07.08.2026



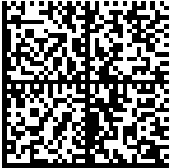
AMD.FP164

(5) 07.08.2026



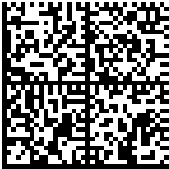
AMD.FP164

(6) 07.08.2026



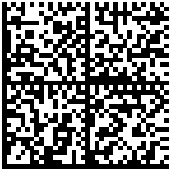
AMD.FP164

(7) 07.08.2026



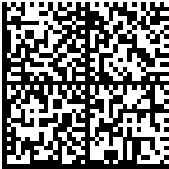
AMD.FP164

(8) 07.08.2026



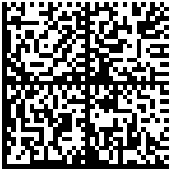
AMD.FP164

(9) 07.08.2026



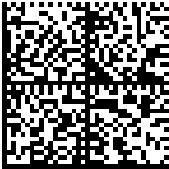
AMD.FP164

(10) 07.08.2026



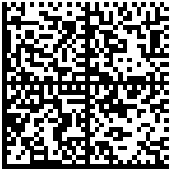
AMD.FP164

(11) 07.08.2026



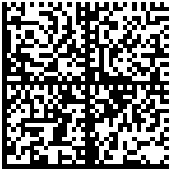
AMD.FP164

(12) 07.08.2026



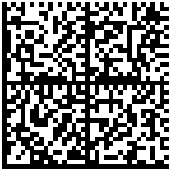
AMD.FP164

(13) 07.08.2026



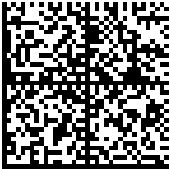
AMD.FP164

(14) 07.08.2026



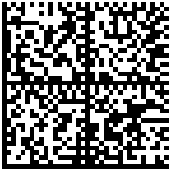
AMD.FP164

(15) 07.08.2026



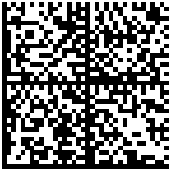
AMD.FP164

(16) 07.08.2026



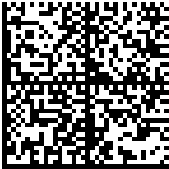
AMD.FP164

(17) 07.08.2026



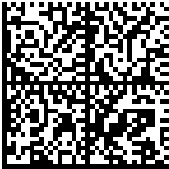
AMD.FP164

(18) 07.08.2026



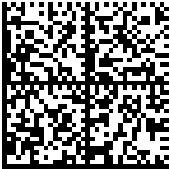
AMD.FP164

(19) 07.08.2026



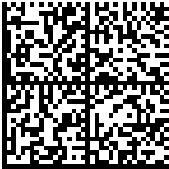
AMD.FP164

(20) 07.08.2026



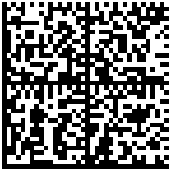
AMD.FP164

(21) 07.08.2026



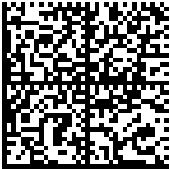
AMD.FP164

(22) 07.08.2026



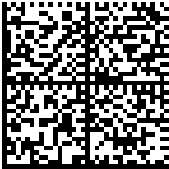
AMD.FP164

(23) 07.08.2026



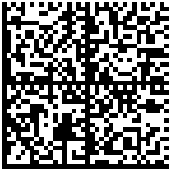
AMD.FP164

(24) 07.08.2026



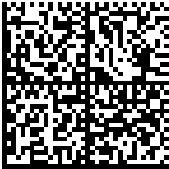
AMD.FP164

(25) 07.08.2026



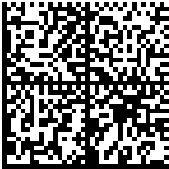
AMD.FP164

(26) 07.08.2026



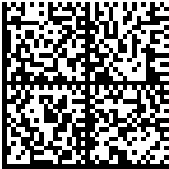
AMD.FP164

(27) 07.08.2026



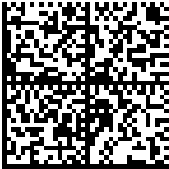
AMD.FP164

(28) 07.08.2026



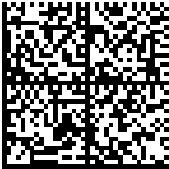
AMD.FP164

(29) 07.08.2026



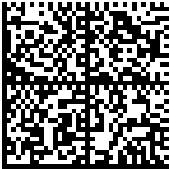
AMD.FP164

(30) 07.08.2026



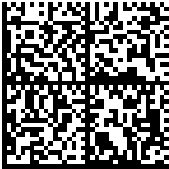
AMD.FP164

(31) 07.08.2026



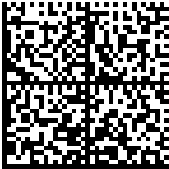
AMD.FP164

(32) 07.08.2026



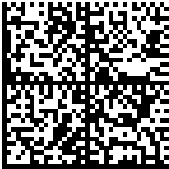
AMD.FP164

(33) 07.08.2026



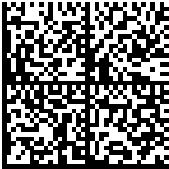
AMD.FP164

(34) 07.08.2026



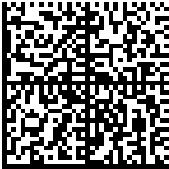
AMD.FP164

(35) 07.08.2026



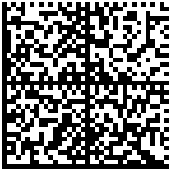
AMD.FP164

(36) 07.08.2026



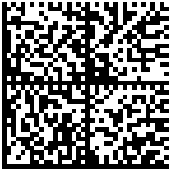
AMD.FP164

(37) 07.08.2026



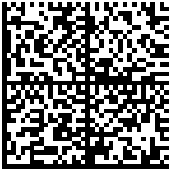
AMD.FP164

(38) 07.08.2026



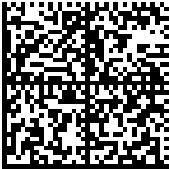
AMD.FP164

(39) 07.08.2026



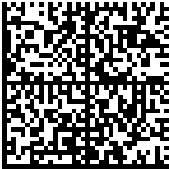
AMD.FP164

(40) 07.08.2026



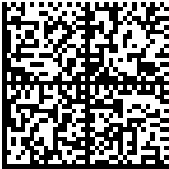
AMD.FP164

(41) 07.08.2026



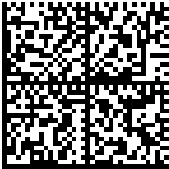
AMD.FP164

(42) 07.08.2026



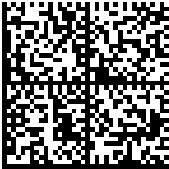
AMD.FP164

(43) 07.08.2026



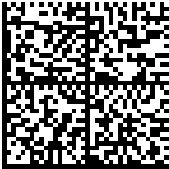
AMD.FP164

(44) 07.08.2026



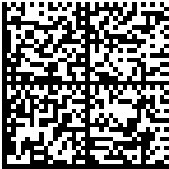
AMD.FP164

(45) 07.08.2026



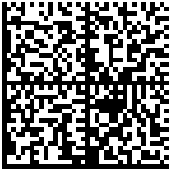
AMD.FP164

(46) 07.08.2026



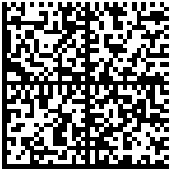
AMD.FP164

(47) 07.08.2026



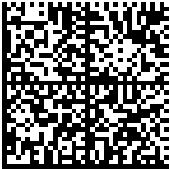
AMD.FP164

(48) 07.08.2026



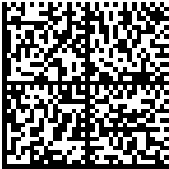
AMD.FP164

(49) 07.08.2026



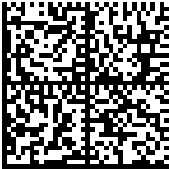
AMD.FP164

(50) 07.08.2026



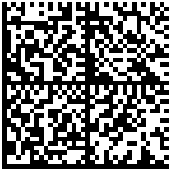
AMD.FP164

(51) 07.08.2026



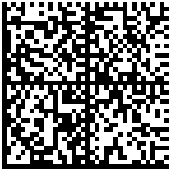
AMD.FP164

(52) 07.08.2026



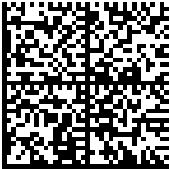
AMD.FP164

(53) 07.08.2026



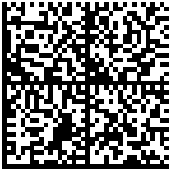
AMD.FP164

(54) 07.08.2026



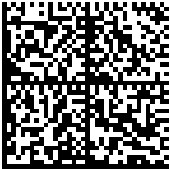
AMD.FP164

(55) 07.08.2026



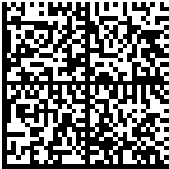
AMD.FP164

(56) 07.08.2026



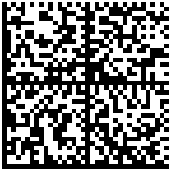
AMD.FP164

(57) 07.08.2026



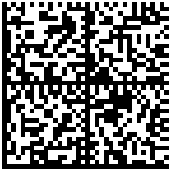
AMD.FP164

(58) 07.08.2026



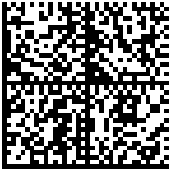
AMD.FP164

(59) 07.08.2026



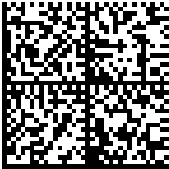
AMD.FP164

(60) 07.08.2026



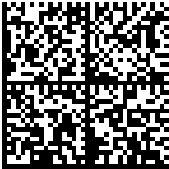
AMD.FP164

(61) 07.08.2026



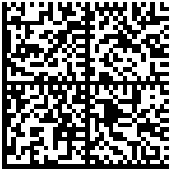
AMD.FP164

(62) 07.08.2026



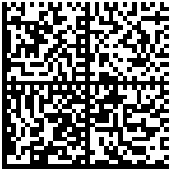
AMD.FP164

(63) 07.08.2026



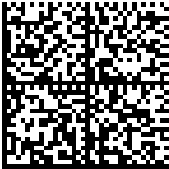
AMD.FP164

(64) 07.08.2026



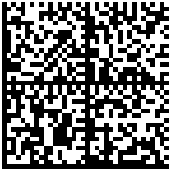
AMD.FP164

(65) 07.08.2026



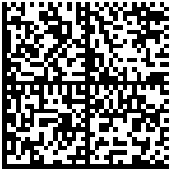
AMD.FP164

(66) 07.08.2026



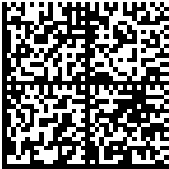
AMD.FP164

(67) 07.08.2026



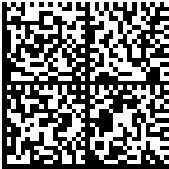
AMD.FP164

(68) 07.08.2026



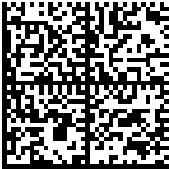
AMD.FP164

(69) 07.08.2026



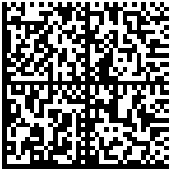
AMD.FP164

(70) 07.08.2026



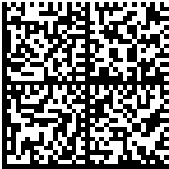
AMD.FP164

(71) 07.08.2026



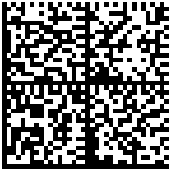
AMD.FP164

(72) 07.08.2026



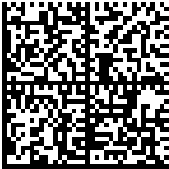
AMD.FP164

(73) 07.08.2026



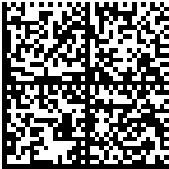
AMD.FP164

(74) 07.08.2026



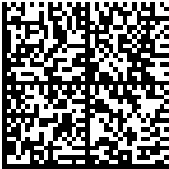
AMD.FP164

(75) 07.08.2026



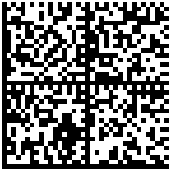
AMD.FP164

(76) 07.08.2026



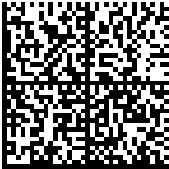
AMD.FP164

(77) 07.08.2026



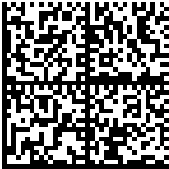
AMD.FP164

(78) 07.08.2026



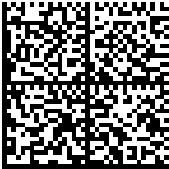
AMD.FP164

(79) 07.08.2026



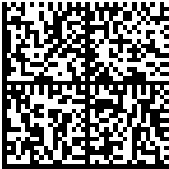
AMD.FP164

(80) 07.08.2026



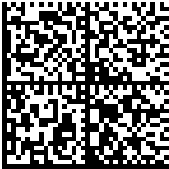
AMD.FP164

(81) 07.08.2026



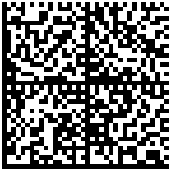
AMD.FP164

(82) 07.08.2026



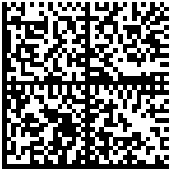
AMD.FP164

(83) 07.08.2026



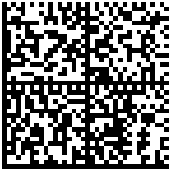
AMD.FP164

(84) 07.08.2026



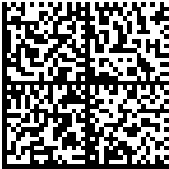
AMD.FP164

(85) 07.08.2026



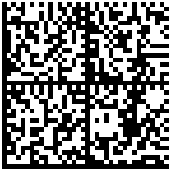
AMD.FP164

(86) 07.08.2026



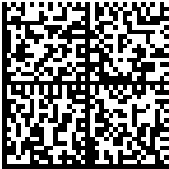
AMD.FP164

(87) 07.08.2026



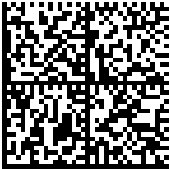
AMD.FP164

(88) 07.08.2026



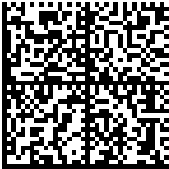
AMD.FP164

(89) 07.08.2026



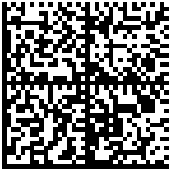
AMD.FP164

(90) 07.08.2026



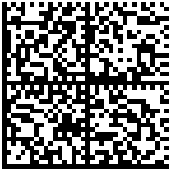
AMD.FP164

(91) 07.08.2026



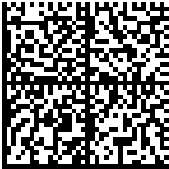
AMD.FP164

(92) 07.08.2026



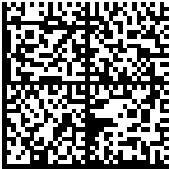
AMD.FP164

(93) 07.08.2026



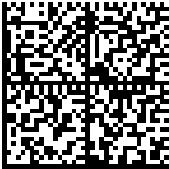
AMD.FP164

(94) 07.08.2026



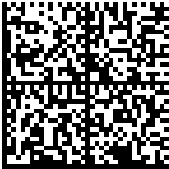
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(95) 07.08.2026



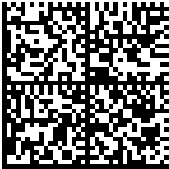
AMD.FP164

(96) 07.08.2026



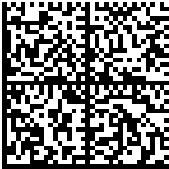
AMD.FP164

(97) 07.08.2026



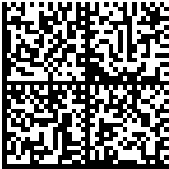
AMD.FP164

(98) 07.08.2026



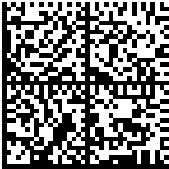
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(99) 07.08.2026



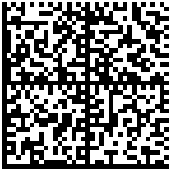
AMD.FP164

(100) 07.08.2026



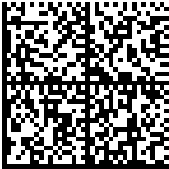
AMD.FP164

(101) 07.08.2026



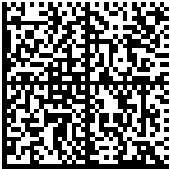
AMD.FP164

(102) 07.08.2026



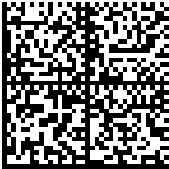
AMD.FP164

(103) 07.08.2026



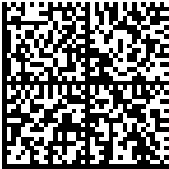
AMD.FP164

(104) 07.08.2026



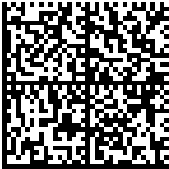
AMD.FP164

(105) 07.08.2026



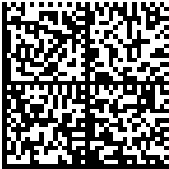
AMD.FP164

(106) 07.08.2026



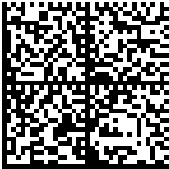
AMD.FP164

(107) 07.08.2026



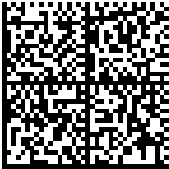
AMD.FP164

(108) 07.08.2026



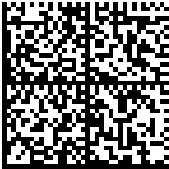
AMD.FP164

(109) 07.08.2026



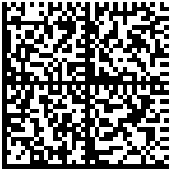
AMD.FP164

(110) 07.08.2026



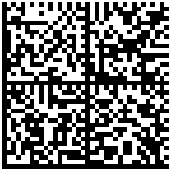
AMD.FP164

(111) 07.08.2026



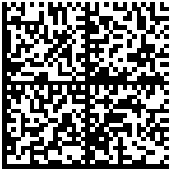
AMD.FP164

(112) 07.08.2026



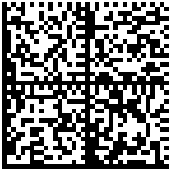
AMD.FP164

(113) 07.08.2026



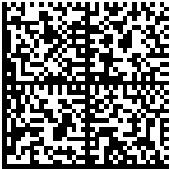
AMD.FP164

(114) 07.08.2026



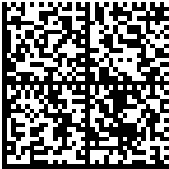
AMD.FP164

(115) 07.08.2026



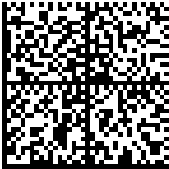
AMD.FP164

(116) 07.08.2026



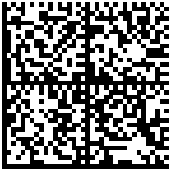
AMD.FP164

(117) 07.08.2026



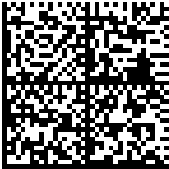
AMD.FP164

(118) 07.08.2026



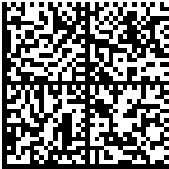
AMD.FP164

(119) 07.08.2026



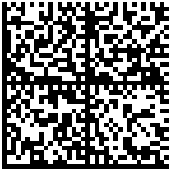
AMD.FP164

(120) 07.08.2026



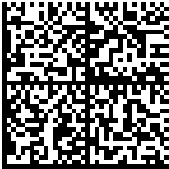
AMD.FP164

(121) 07.08.2026



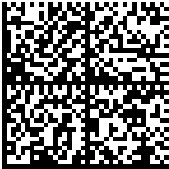
AMD.FP164

(122) 07.08.2026



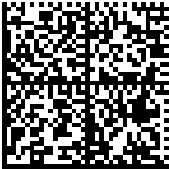
AMD.FP164

(123) 07.08.2026



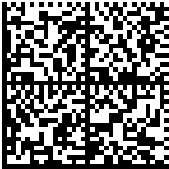
AMD.FP164

(124) 07.08.2026



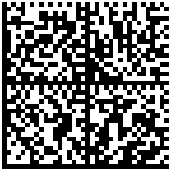
AMD.FP164

(125) 07.08.2026



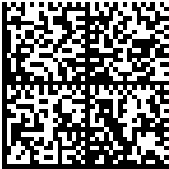
AMD.FP164

(126) 07.08.2026



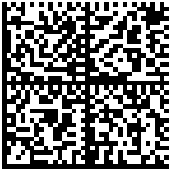
AMD.FP164

(127) 07.08.2026



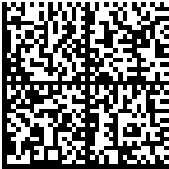
AMD.FP164

(128) 07.08.2026



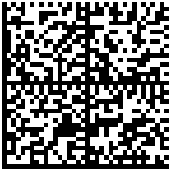
AMD.FP164

(129) 07.08.2026



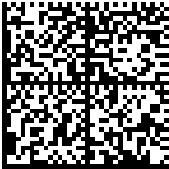
AMD.FP164

(130) 07.08.2026



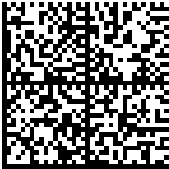
AMD.FP164

(131) 07.08.2026



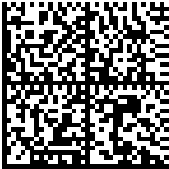
AMD.FP164

(132) 07.08.2026



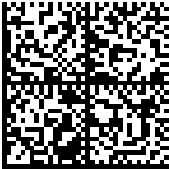
AMD.FP164

(133) 07.08.2026



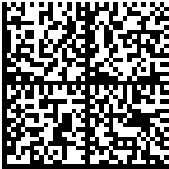
AMD.FP164

(134) 07.08.2026



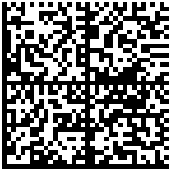
AMD.FP164

(135) 07.08.2026



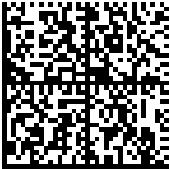
AMD.FP164

(136) 07.08.2026



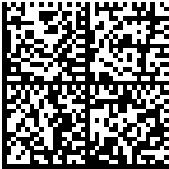
AMD.FP164

(137) 07.08.2026



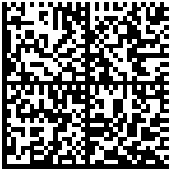
AMD.FP164

(138) 07.08.2026



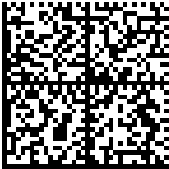
AMD.FP164

(139) 07.08.2026



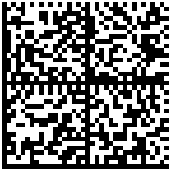
AMD.FP164

(140) 07.08.2026



AMD.FP164

(141) 07.08.2026



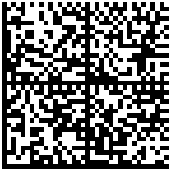
AMD.FP164

(142) 07.08.2026



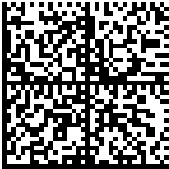
AMD.FP164

(143) 07.08.2026



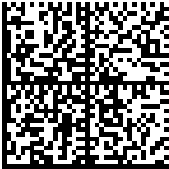
AMD.FP164

(144) 07.08.2026



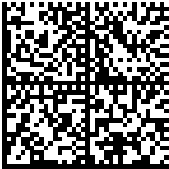
AMD.FP164

(145) 07.08.2026



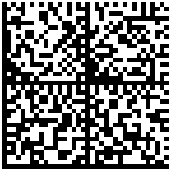
AMD.FP164

(146) 07.08.2026



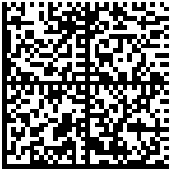
AMD.FP164

(147) 07.08.2026



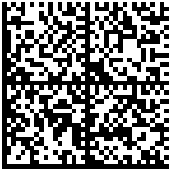
AMD.FP164

(148) 07.08.2026



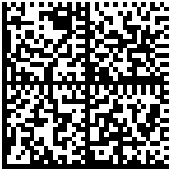
AMD.FP164

(149) 07.08.2026



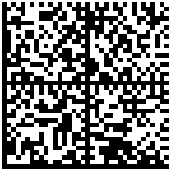
AMD.FP164

(150) 07.08.2026



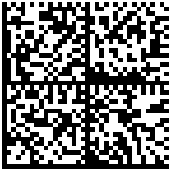
AMD.FP164

(151) 07.08.2026



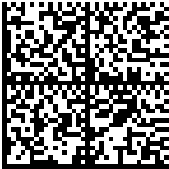
AMD.FP164

(152) 07.08.2026



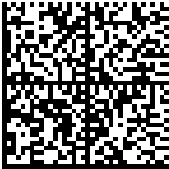
AMD.FP164

(153) 07.08.2026



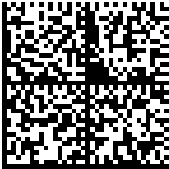
AMD.FP164

(154) 07.08.2026



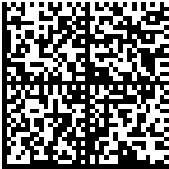
AMD.FP164

(155) 07.08.2026



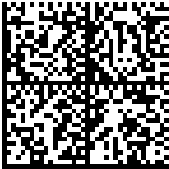
AMD.FP164

(156) 07.08.2026



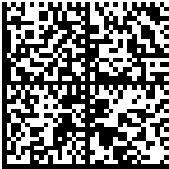
AMD.FP164

(157) 07.08.2026



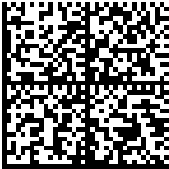
AMD.FP164

(158) 07.08.2026



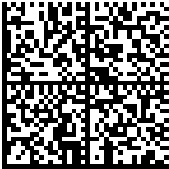
AMD.FP164

(159) 07.08.2026



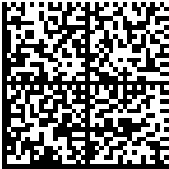
AMD.FP164

(160) 07.08.2026



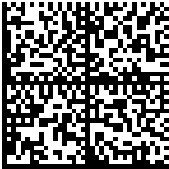
AMD.FP164

(161) 07.08.2026



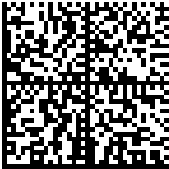
AMD.FP164

(162) 07.08.2026



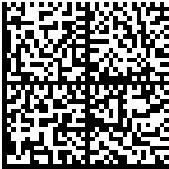
AMD.FP164

(163) 07.08.2026



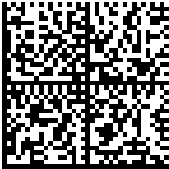
AMD.FP164

(164) 07.08.2026



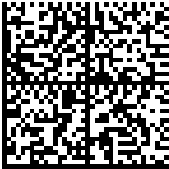
AMD.FP164

(165) 07.08.2026



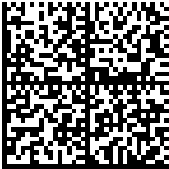
AMD.FP164

(166) 07.08.2026



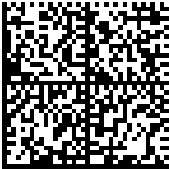
AMD.FP164

(167) 07.08.2026



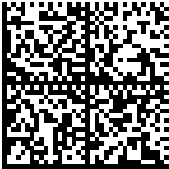
AMD.FP164

(168) 07.08.2026



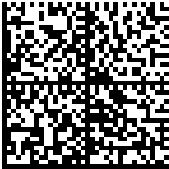
AMD.FP164

(169) 07.08.2026



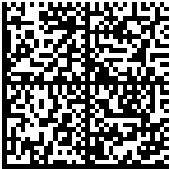
AMD.FP164

(170) 07.08.2026



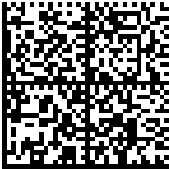
AMD.FP164

(171) 07.08.2026



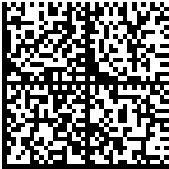
AMD.FP164

(172) 07.08.2026



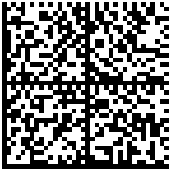
AMD.FP164

(173) 07.08.2026



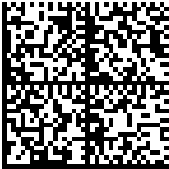
AMD.FP164

(174) 07.08.2026



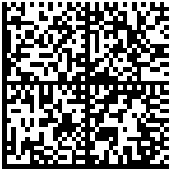
AMD.FP164

(175) 07.08.2026



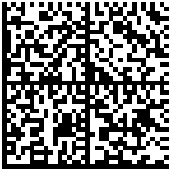
AMD.FP164

(176) 07.08.2026



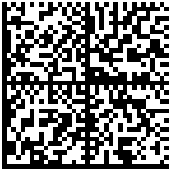
AMD.FP164

(177) 07.08.2026



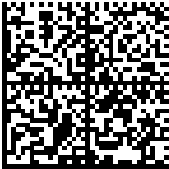
AMD.FP164

(178) 07.08.2026



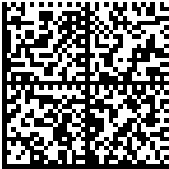
AMD.FP164

(179) 07.08.2026



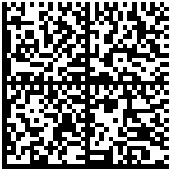
AMD.FP164

(180) 07.08.2026



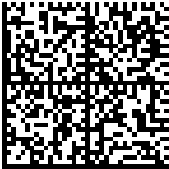
AMD.FP164

(181) 07.08.2026



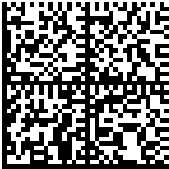
AMD.FP164

(182) 07.08.2026



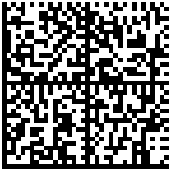
AMD.FP164

(183) 07.08.2026



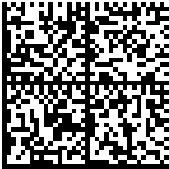
AMD.FP164

(184) 07.08.2026



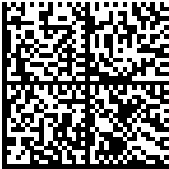
AMD.FP164

(185) 07.08.2026



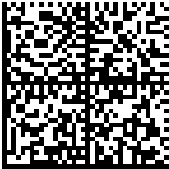
AMD.FP164

(186) 07.08.2026



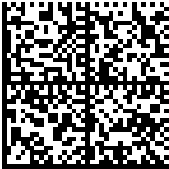
AMD.FP164

(187) 07.08.2026



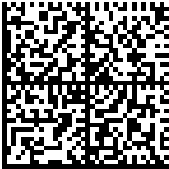
AMD.FP164

(188) 07.08.2026



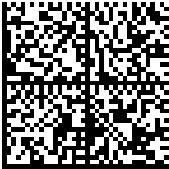
AMD.FP164

(189) 07.08.2026



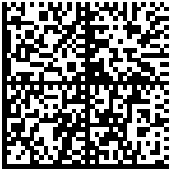
AMD.FP164

(190) 07.08.2026



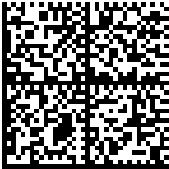
AMD.FP164

(191) 07.08.2026



AMD.FP164

(192) 07.08.2026



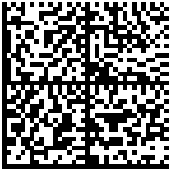
AMD.FP164

(193) 07.08.2026



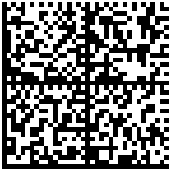
AMD.FP164

(194) 07.08.2026



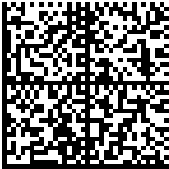
AMD.FP164

(195) 07.08.2026



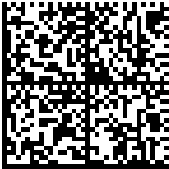
AMD.FP164

(196) 07.08.2026



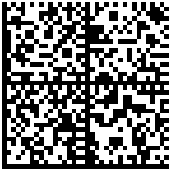
AMD.FP164

(197) 07.08.2026



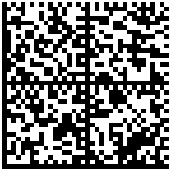
AMD.FP164

(198) 07.08.2026



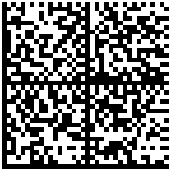
AMD.FP164

(199) 07.08.2026



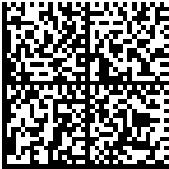
AMD.FP164

(200) 07.08.2026



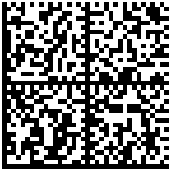
AMD.FP164

(201) 07.08.2026



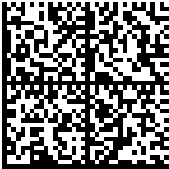
AMD.FP164

(202) 07.08.2026



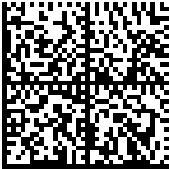
AMD.FP164

(203) 07.08.2026



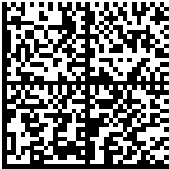
AMD.FP164

(204) 07.08.2026



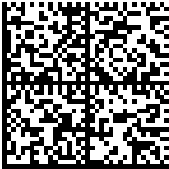
AMD.FP164

(205) 07.08.2026



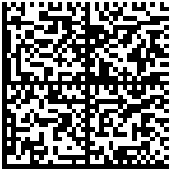
AMD.FP164

(206) 07.08.2026



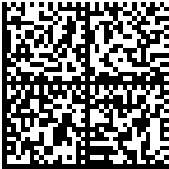
AMD.FP164

(207) 07.08.2026



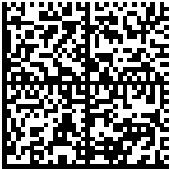
AMD.FP164

(208) 07.08.2026



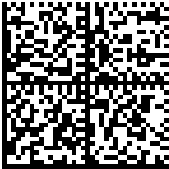
AMD.FP164

(209) 07.08.2026



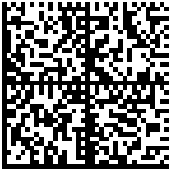
AMD.FP164

(210) 07.08.2026



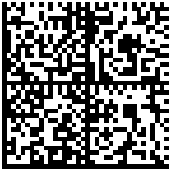
AMD.FP164

(211) 07.08.2026



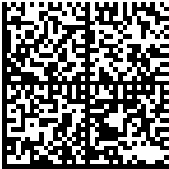
AMD.FP164

(212) 07.08.2026



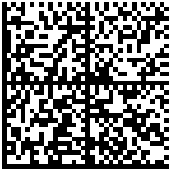
AMD.FP164

(213) 07.08.2026



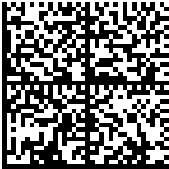
AMD.FP164

(214) 07.08.2026



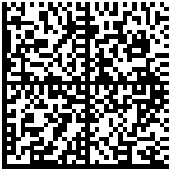
AMD.FP164

(215) 07.08.2026



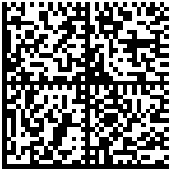
AMD.FP164

(216) 07.08.2026



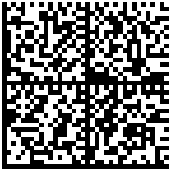
AMD.FP164

(217) 07.08.2026



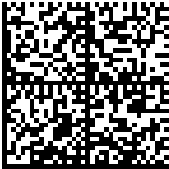
AMD.FP164

(218) 07.08.2026



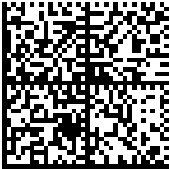
AMD.FP164

(219) 07.08.2026



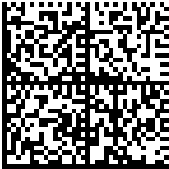
AMD.FP164

(220) 07.08.2026



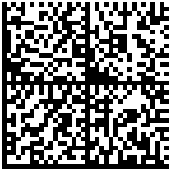
AMD.FP164

(221) 07.08.2026



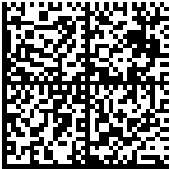
AMD.FP164

(222) 07.08.2026



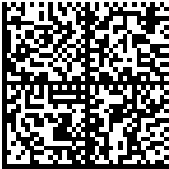
AMD.FP164

(223) 07.08.2026



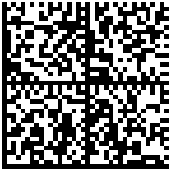
AMD.FP164

(224) 07.08.2026



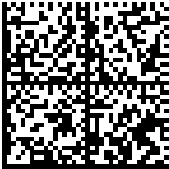
AMD.FP164

(225) 07.08.2026



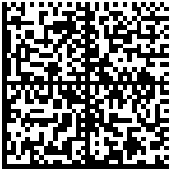
AMD.FP164

(226) 07.08.2026



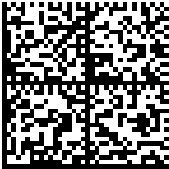
AMD.FP164

(227) 07.08.2026



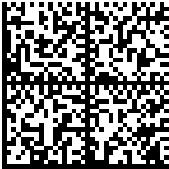
AMD.FP164

(228) 07.08.2026



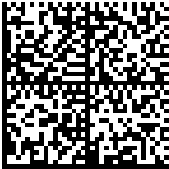
AMD.FP164

(229) 07.08.2026



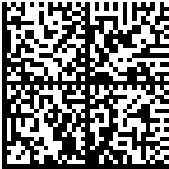
AMD.FP164

(230) 07.08.2026



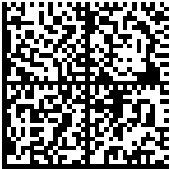
AMD.FP164

(231) 07.08.2026



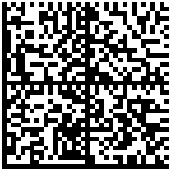
AMD.FP164

(232) 07.08.2026



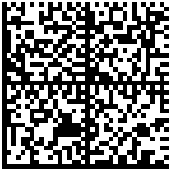
AMD.FP164

(233) 07.08.2026



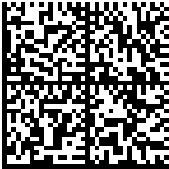
AMD.FP164

(234) 07.08.2026



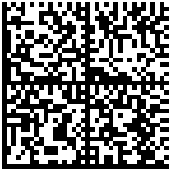
AMD.FP164

(235) 07.08.2026



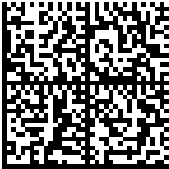
AMD.FP164

(236) 07.08.2026



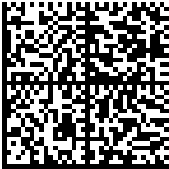
AMD.FP164

(237) 07.08.2026



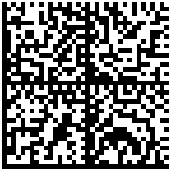
AMD.FP164

(238) 07.08.2026



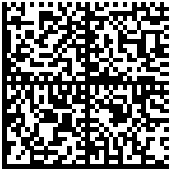
AMD.FP164

(239) 07.08.2026



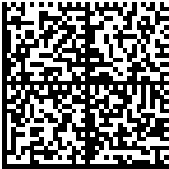
AMD.FP164

(240) 07.08.2026



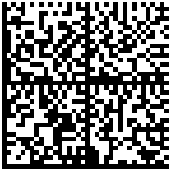
AMD.FP164

(241) 07.08.2026



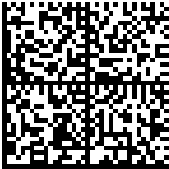
AMD.FP164

(242) 07.08.2026



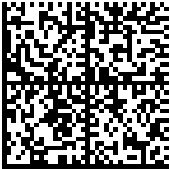
AMD.FP164

(243) 07.08.2026



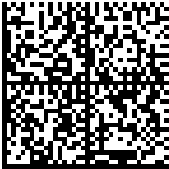
AMD.FP164

(244) 07.08.2026



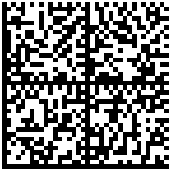
AMD.FP164

(245) 07.08.2026



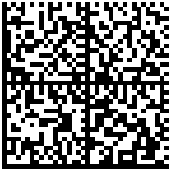
AMD.FP164

(246) 07.08.2026



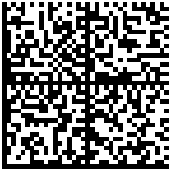
AMD.FP164

(247) 07.08.2026



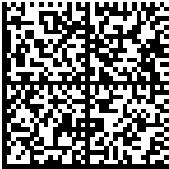
AMD.FP164

(248) 07.08.2026



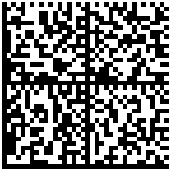
AMD.FP164

(249) 07.08.2026



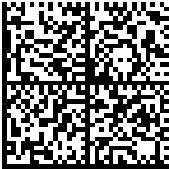
AMD.FP164

(250) 07.08.2026



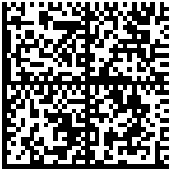
AMD.FP164

(251) 07.08.2026



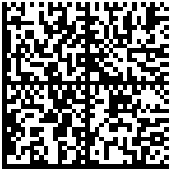
AMD.FP164

(252) 07.08.2026



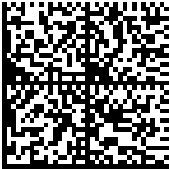
AMD.FP164

(253) 07.08.2026



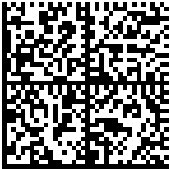
AMD.FP164

(254) 07.08.2026



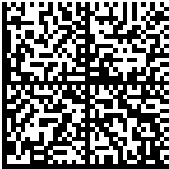
AMD.FP164

(255) 07.08.2026



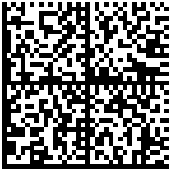
AMD.FP164

(256) 07.08.2026



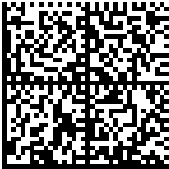
AMD.FP164

(257) 07.08.2026



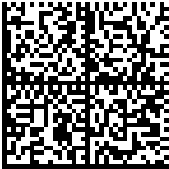
AMD.FP164

(258) 07.08.2026



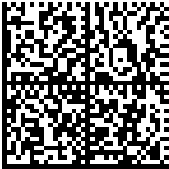
AMD.FP164

(259) 07.08.2026



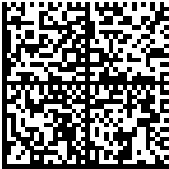
AMD.FP164

(260) 07.08.2026



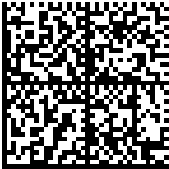
AMD.FP164

(261) 07.08.2026



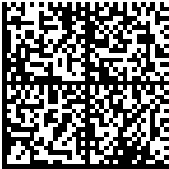
AMD.FP164

(262) 07.08.2026



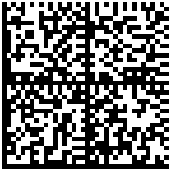
AMD.FP164

(263) 07.08.2026



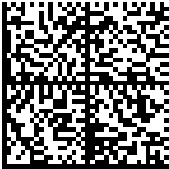
AMD.FP164

(264) 07.08.2026



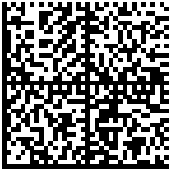
AMD.FP164

(265) 07.08.2026



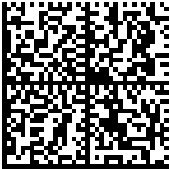
AMD.FP164

(266) 07.08.2026



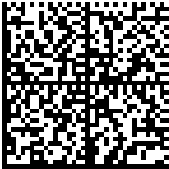
AMD.FP164

(267) 07.08.2026



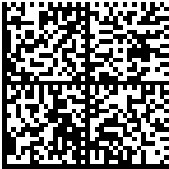
AMD.FP164

(268) 07.08.2026



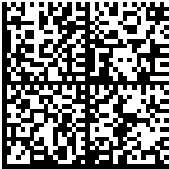
AMD.FP164

(269) 07.08.2026



AMD.FP164

(270) 07.08.2026



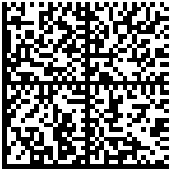
AMD.FP164

(271) 07.08.2026



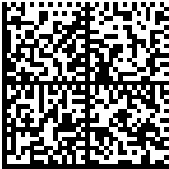
AMD.FP164

(272) 07.08.2026



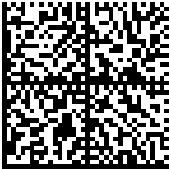
AMD.FP164

(273) 07.08.2026



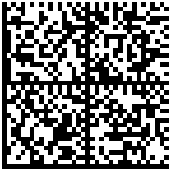
AMD.FP164

(274) 07.08.2026



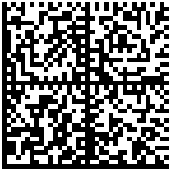
AMD.FP164

(275) 07.08.2026



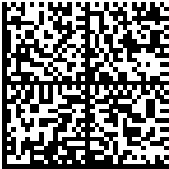
AMD.FP164

(276) 07.08.2026



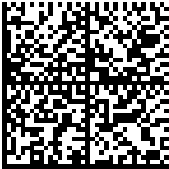
AMD.FP164

(277) 07.08.2026



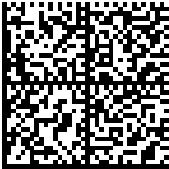
AMD.FP164

(278) 07.08.2026



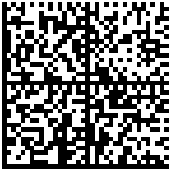
AMD.FP164

(279) 07.08.2026



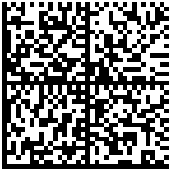
AMD.FP164

(280) 07.08.2026



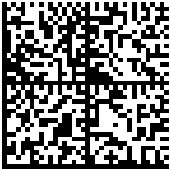
AMD.FP164

(281) 07.08.2026



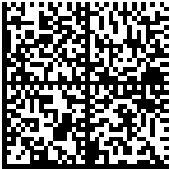
AMD.FP164

(282) 07.08.2026



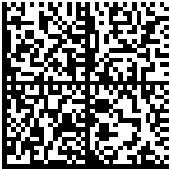
AMD.FP164

(283) 07.08.2026



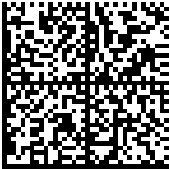
AMD.FP164

(284) 07.08.2026



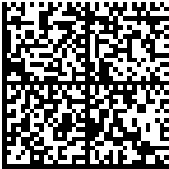
AMD.FP164

(285) 07.08.2026



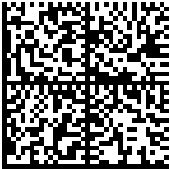
AMD.FP164

(286) 07.08.2026



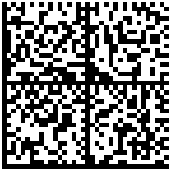
AMD.FP164

(287) 07.08.2026



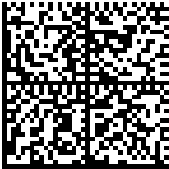
AMD.FP164

(288) 07.08.2026



AMD.FP164

(289) 07.08.2026



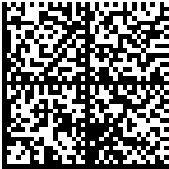
AMD.FP164

(290) 07.08.2026



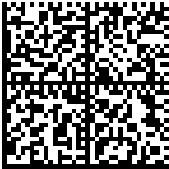
AMD.FP164

(291) 07.08.2026



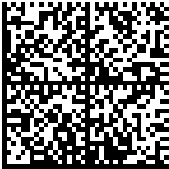
AMD.FP164

(292) 07.08.2026



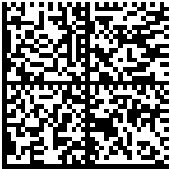
AMD.FP164

(293) 07.08.2026



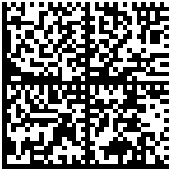
AMD.FP164

(294) 07.08.2026



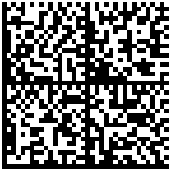
AMD.FP164

(295) 07.08.2026



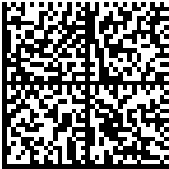
AMD.FP164

(296) 07.08.2026



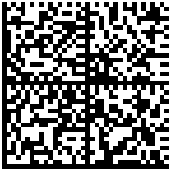
AMD.FP164

(297) 07.08.2026



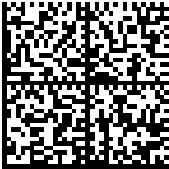
AMD.FP164

(298) 07.08.2026



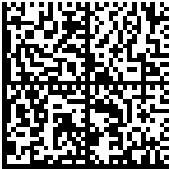
AMD.FP164

(299) 07.08.2026



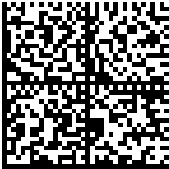
AMD.FP164

(300) 07.08.2026



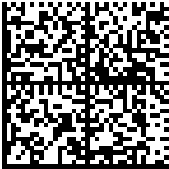
AMD.FP164

(301) 07.08.2026



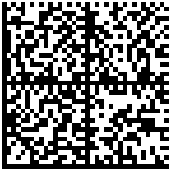
AMD.FP164

(302) 07.08.2026



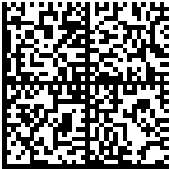
AMD.FP164

(303) 07.08.2026



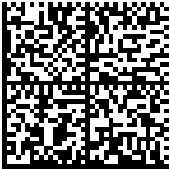
AMD.FP164

(304) 07.08.2026



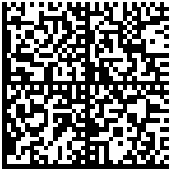
AMD.FP164

(305) 07.08.2026



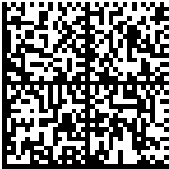
AMD.FP164

(306) 07.08.2026



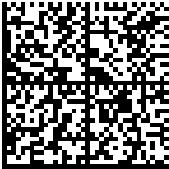
AMD.FP164

(307) 07.08.2026



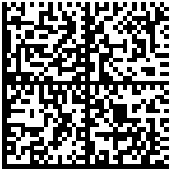
AMD.FP164

(308) 07.08.2026



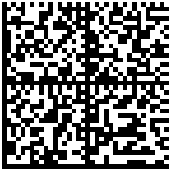
AMD.FP164

(309) 07.08.2026



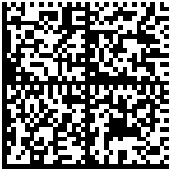
AMD.FP164

(310) 07.08.2026



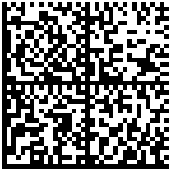
AMD.FP164

(311) 07.08.2026



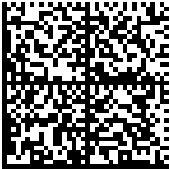
AMD.FP164

(312) 07.08.2026



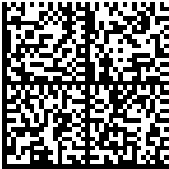
AMD.FP164

(313) 07.08.2026



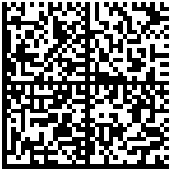
AMD.FP164

(314) 07.08.2026



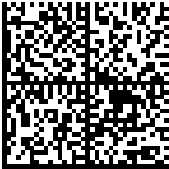
AMD.FP164

(315) 07.08.2026



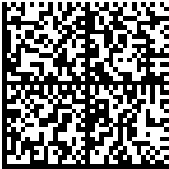
AMD.FP164

(316) 07.08.2026



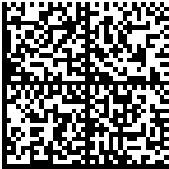
AMD.FP164

(317) 07.08.2026



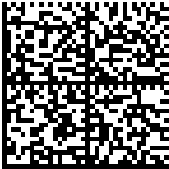
AMD.FP164

(318) 07.08.2026



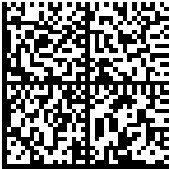
AMD.FP164

(319) 07.08.2026



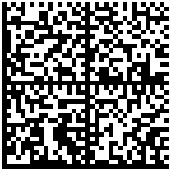
AMD.FP164

(320) 07.08.2026



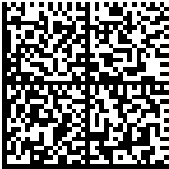
AMD.FP164

(321) 07.08.2026



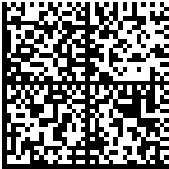
AMD.FP164

(322) 07.08.2026



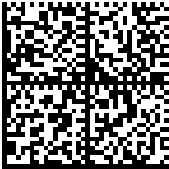
AMD.FP164

(323) 07.08.2026



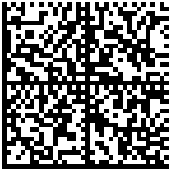
AMD.FP164

(324) 07.08.2026



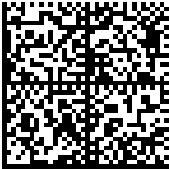
AMD.FP164

(325) 07.08.2026



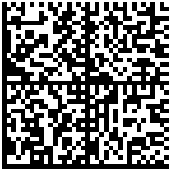
AMD.FP164

(326) 07.08.2026



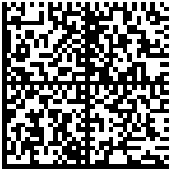
AMD.FP164

(327) 07.08.2026



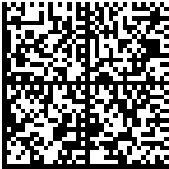
AMD.FP164

(328) 07.08.2026



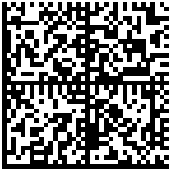
AMD.FP164

(329) 07.08.2026



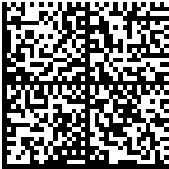
AMD.FP164

(330) 07.08.2026



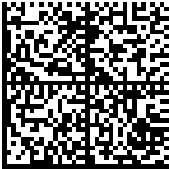
AMD.FP164

(331) 07.08.2026



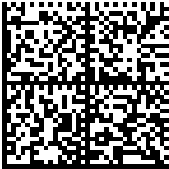
AMD.FP164

(332) 07.08.2026



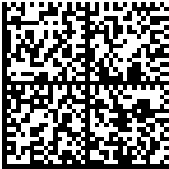
AMD.FP164

(333) 07.08.2026



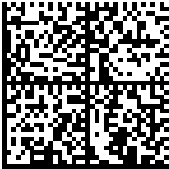
AMD.FP164

(334) 07.08.2026



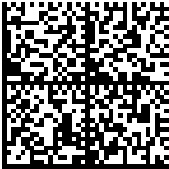
AMD.FP164

(335) 07.08.2026



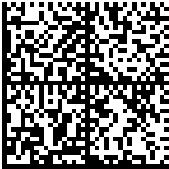
AMD.FP164

(336) 07.08.2026



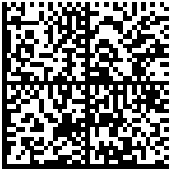
AMD.FP164

(337) 07.08.2026



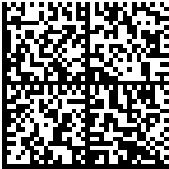
AMD.FP164

(338) 07.08.2026



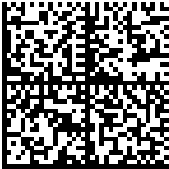
AMD.FP164

(339) 07.08.2026



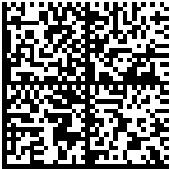
AMD.FP164

(340) 07.08.2026



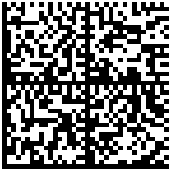
AMD.FP164

(341) 07.08.2026



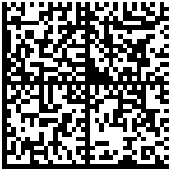
AMD.FP164

(342) 07.08.2026



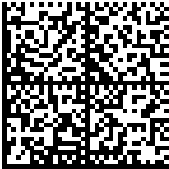
AMD.FP164

(343) 07.08.2026



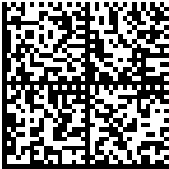
AMD.FP164

(344) 07.08.2026



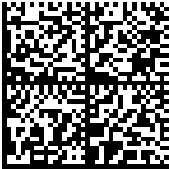
AMD.FP164

(345) 07.08.2026



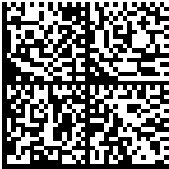
AMD.FP164

(346) 07.08.2026



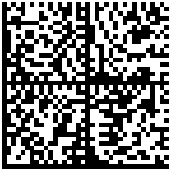
AMD.FP164

(347) 07.08.2026



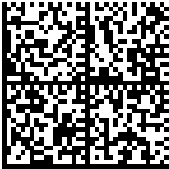
AMD.FP164

(348) 07.08.2026



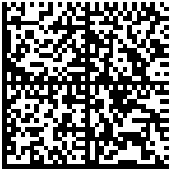
AMD.FP164

(349) 07.08.2026



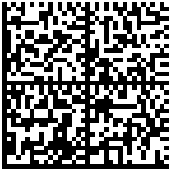
AMD.FP164

(350) 07.08.2026



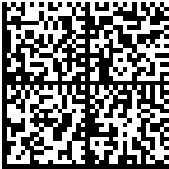
AMD.FP164

(351) 07.08.2026



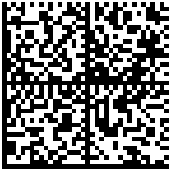
AMD.FP164

(352) 07.08.2026



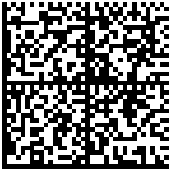
AMD.FP164

(353) 07.08.2026



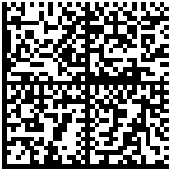
AMD.FP164

(354) 07.08.2026



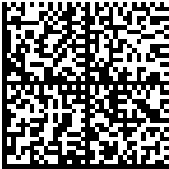
AMD.FP164

(355) 07.08.2026



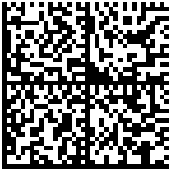
AMD.FP164

(356) 07.08.2026



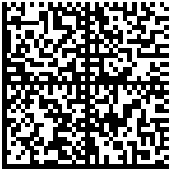
AMD.FP164

(357) 07.08.2026



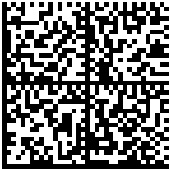
AMD.FP164

(358) 07.08.2026



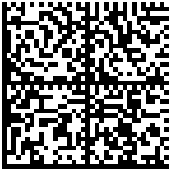
AMD.FP164

(359) 07.08.2026



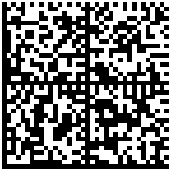
AMD.FP164

(360) 07.08.2026



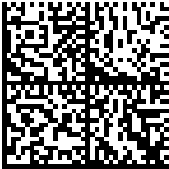
AMD.FP164

(361) 07.08.2026



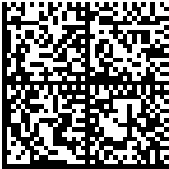
AMD.FP164

(362) 07.08.2026



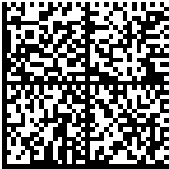
AMD.FP164

(363) 07.08.2026



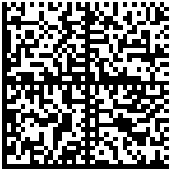
AMD.FP164

(364) 07.08.2026



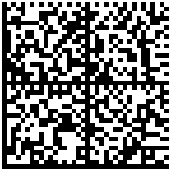
AMD.FP164

(365) 07.08.2026



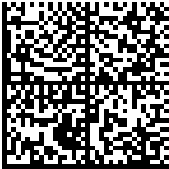
AMD.FP164

(366) 07.08.2026



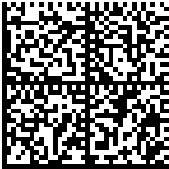
AMD.FP164

(367) 07.08.2026



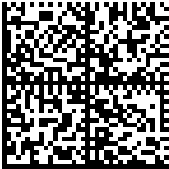
AMD.FP164

(368) 07.08.2026



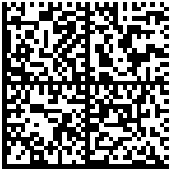
AMD.FP164

(369) 07.08.2026



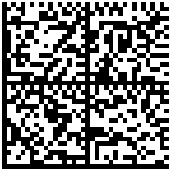
AMD.FP164

(370) 07.08.2026



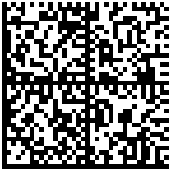
AMD.FP164

(371) 07.08.2026



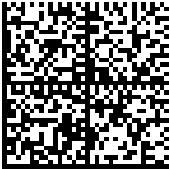
AMD.FP164

(372) 07.08.2026



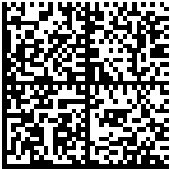
AMD.FP164

(373) 07.08.2026



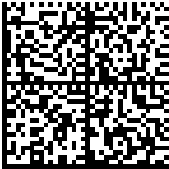
AMD.FP164

(374) 07.08.2026



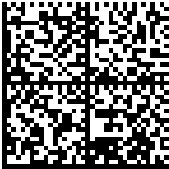
AMD.FP164

(375) 07.08.2026



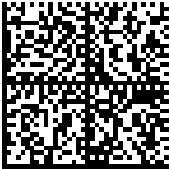
AMD.FP164

(376) 07.08.2026



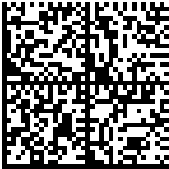
AMD.FP164

(377) 07.08.2026



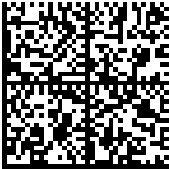
AMD.FP164

(378) 07.08.2026



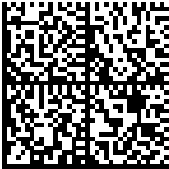
AMD.FP164

(379) 07.08.2026



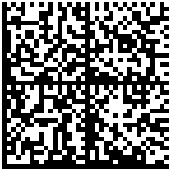
AMD.FP164

(380) 07.08.2026



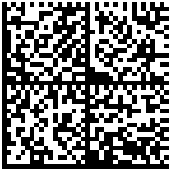
AMD.FP164

(381) 07.08.2026



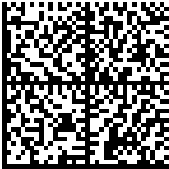
AMD.FP164

(382) 07.08.2026



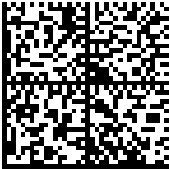
AMD.FP164

(383) 07.08.2026



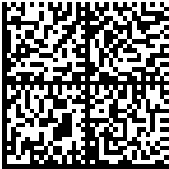
AMD.FP164

(384) 07.08.2026



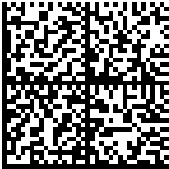
AMD.FP164

(385) 07.08.2026



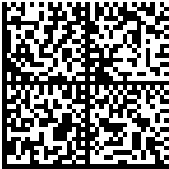
AMD.FP164

(386) 07.08.2026



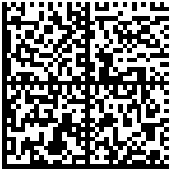
AMD.FP164

(387) 07.08.2026



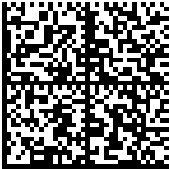
AMD.FP164

(388) 07.08.2026



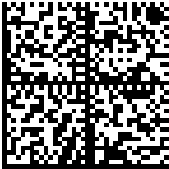
AMD.FP164

(389) 07.08.2026



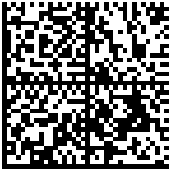
AMD.FP164

(390) 07.08.2026



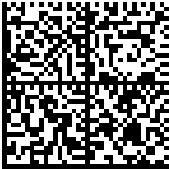
AMD.FP164

(391) 07.08.2026



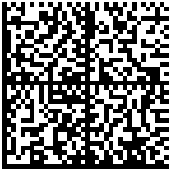
AMD.FP164

(392) 07.08.2026



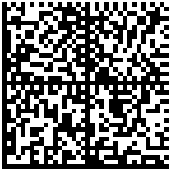
AMD.FP164

(393) 07.08.2026



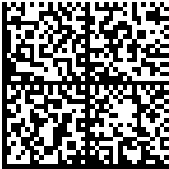
AMD.FP164

(394) 07.08.2026



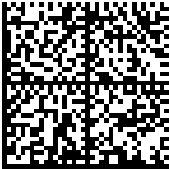
AMD.FP164

(395) 07.08.2026



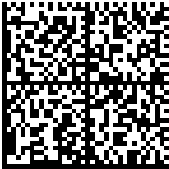
AMD.FP164

(396) 07.08.2026



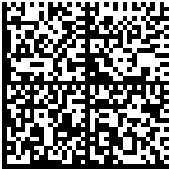
AMD.FP164

(397) 07.08.2026



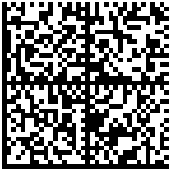
AMD.FP164

(398) 07.08.2026



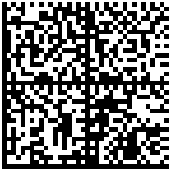
AMD.FP164

(399) 07.08.2026



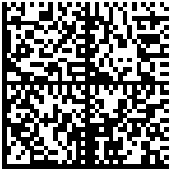
AMD.FP164

(400) 07.08.2026



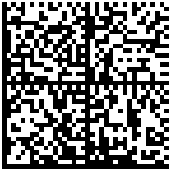
AMD.FP164

(401) 07.08.2026



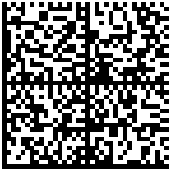
AMD.FP164

(402) 07.08.2026



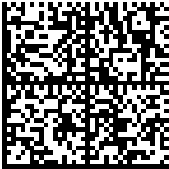
AMD.FP164

(403) 07.08.2026



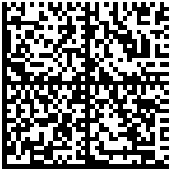
AMD.FP164

(404) 07.08.2026



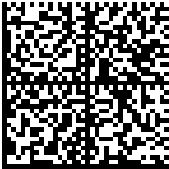
AMD.FP164

(405) 07.08.2026



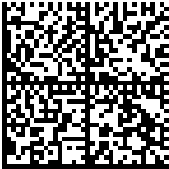
AMD.FP164

(406) 07.08.2026



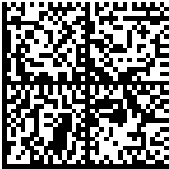
AMD.FP164

(407) 07.08.2026



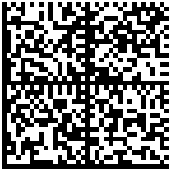
AMD.FP164

(408) 07.08.2026



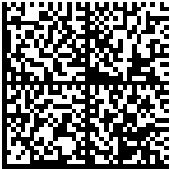
AMD.FP164

(409) 07.08.2026



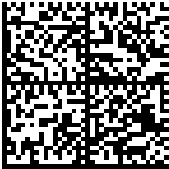
AMD.FP164

(410) 07.08.2026



AMD.FP164

(411) 07.08.2026



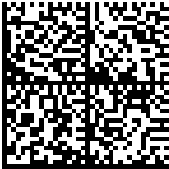
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(412) 07.08.2026



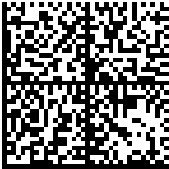
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(413) 07.08.2026



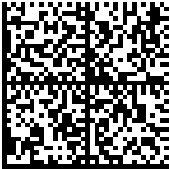
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(414) 07.08.2026



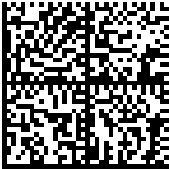
AMD.FP164

(415) 07.08.2026



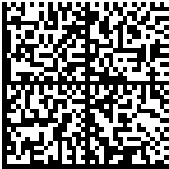
AMD.FP164

(416) 07.08.2026



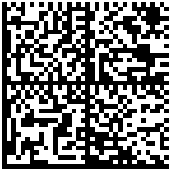
AMD.FP164

(417) 07.08.2026



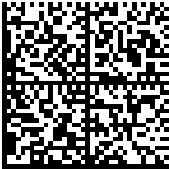
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(418) 07.08.2026



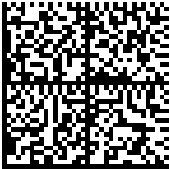
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(419) 07.08.2026



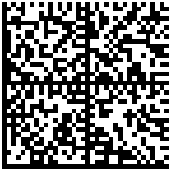
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(420) 07.08.2026



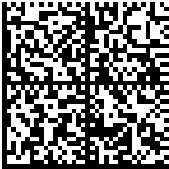
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(421) 07.08.2026



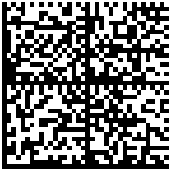
AMD.FP164

(422) 07.08.2026



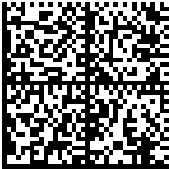
AMD.FP164

(423) 07.08.2026



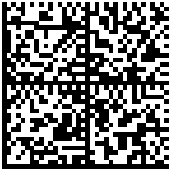
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(424) 07.08.2026



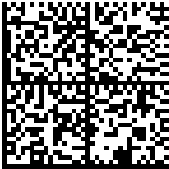
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(425) 07.08.2026



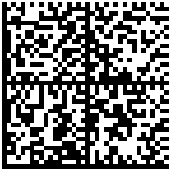
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(426) 07.08.2026



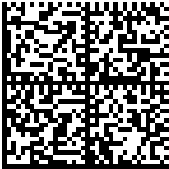
AMD.FP164

(427) 07.08.2026



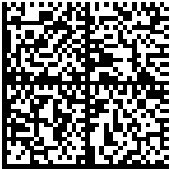
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(428) 07.08.2026



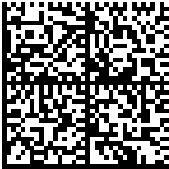
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(429) 07.08.2026



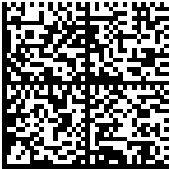
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(430) 07.08.2026



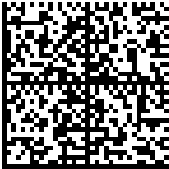
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(431) 07.08.2026



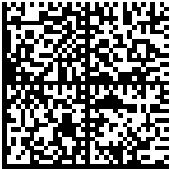
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(432) 07.08.2026



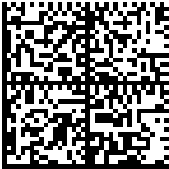
AMD.FP164

(433) 07.08.2026



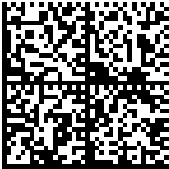
AMD.FP164

(434) 07.08.2026



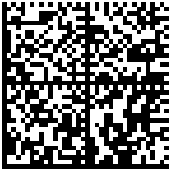
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(435) 07.08.2026



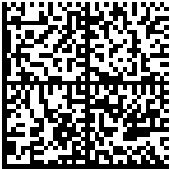
AMD.FP164

(436) 07.08.2026



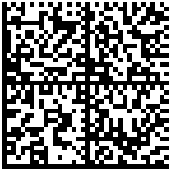
AMD.FP164

(437) 07.08.2026



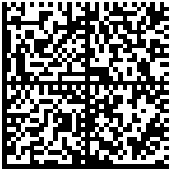
AMD.FP164

(438) 07.08.2026



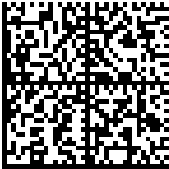
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(439) 07.08.2026



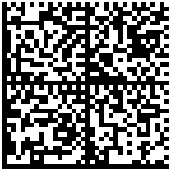
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(440) 07.08.2026



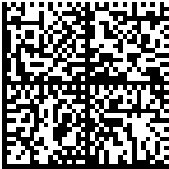
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(441) 07.08.2026



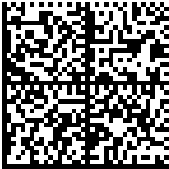
AMD.FP164

(442) 07.08.2026



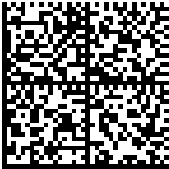
AMD.FP164

(443) 07.08.2026



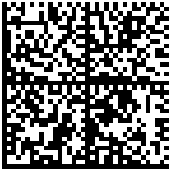
AMD.FP164

(444) 07.08.2026



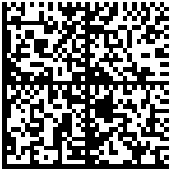
AMD.FP164

(445) 07.08.2026



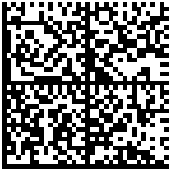
AMD.FP164

(446) 07.08.2026



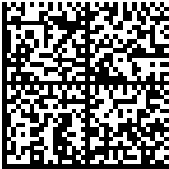
AMD.FP164

(447) 07.08.2026



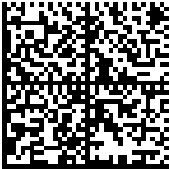
AMD.FP164

(448) 07.08.2026



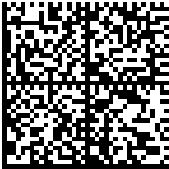
AMD.FP164

(449) 07.08.2026



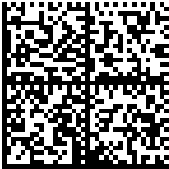
AMD.FP164

(450) 07.08.2026



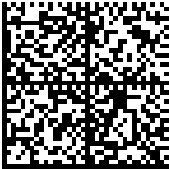
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(451) 07.08.2026



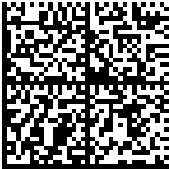
AMD.FP164

(452) 07.08.2026



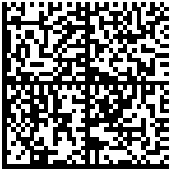
AMD.FP164

(453) 07.08.2026



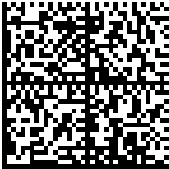
AMD.FP164

(454) 07.08.2026



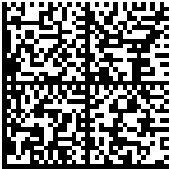
AMD.FP164

(455) 07.08.2026



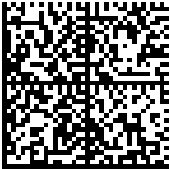
AMD.FP164

(456) 07.08.2026



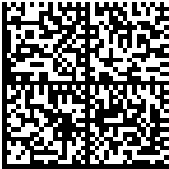
AMD.FP164

(457) 07.08.2026



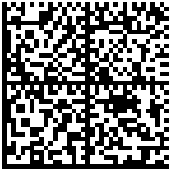
AMD.FP164

(458) 07.08.2026



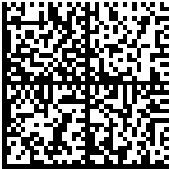
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(459) 07.08.2026



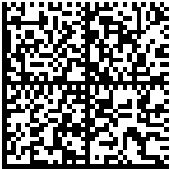
AMD.FP164

(460) 07.08.2026



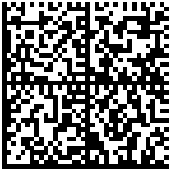
AMD.FP164

(461) 07.08.2026



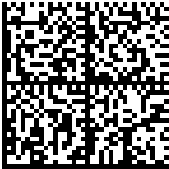
AMD.FP164

(462) 07.08.2026



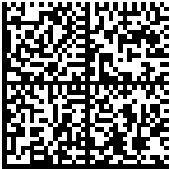
AMD.FP164

(463) 07.08.2026



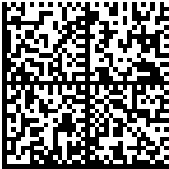
AMD.FP164

(464) 07.08.2026



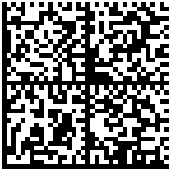
AMD.FP164

(465) 07.08.2026



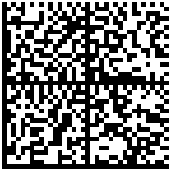
AMD.FP164

(466) 07.08.2026



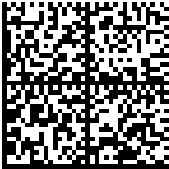
AMD.FP164

(467) 07.08.2026



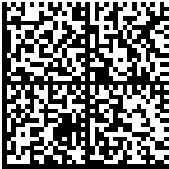
AMD.FP164

(468) 07.08.2026



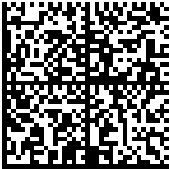
AMD.FP164

(469) 07.08.2026



AMD.FP164

(470) 07.08.2026



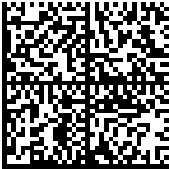
AMD.FP164

(471) 07.08.2026



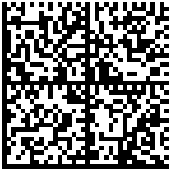
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(472) 07.08.2026



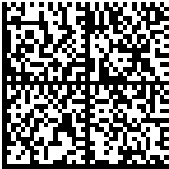
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(473) 07.08.2026



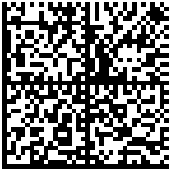
AMD.FP164

(474) 07.08.2026



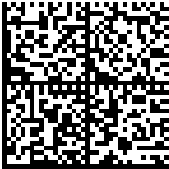
AMD.FP164

(475) 07.08.2026



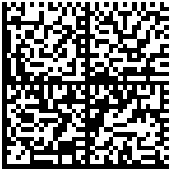
AMD.FP164

(476) 07.08.2026



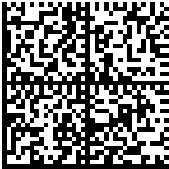
AMD.FP164

(477) 07.08.2026



AMD.FP164

(478) 07.08.2026



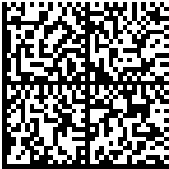
AMD.FP164

(479) 07.08.2026



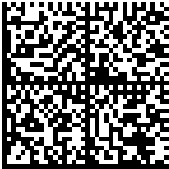
AMD.FP164

(480) 07.08.2026



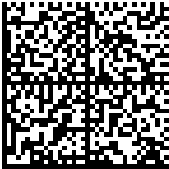
AMD.FP164

(481) 07.08.2026



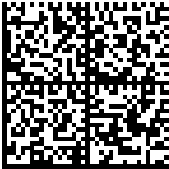
AMD.FP164

(482) 07.08.2026



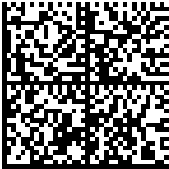
AMD.FP164

(483) 07.08.2026



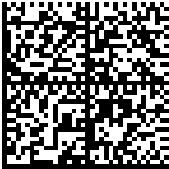
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(484) 07.08.2026



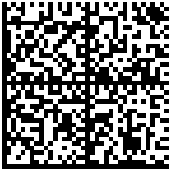
AMD.FP164

(485) 07.08.2026



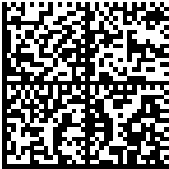
AMD.FP164

(486) 07.08.2026



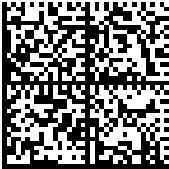
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(487) 07.08.2026



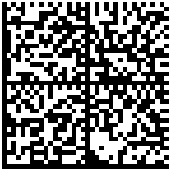
AMD.FP164

(488) 07.08.2026



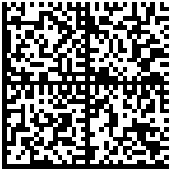
AMD.FP164

(489) 07.08.2026



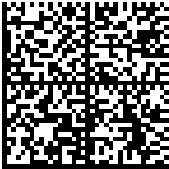
AMD.FP164

(490) 07.08.2026



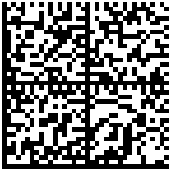
AMD.FP164

(491) 07.08.2026



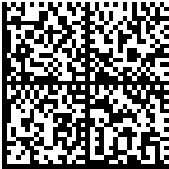
AMD.FP164

(492) 07.08.2026



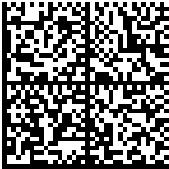
AMD.FP164

(493) 07.08.2026



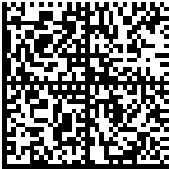
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(494) 07.08.2026



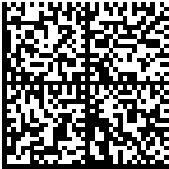
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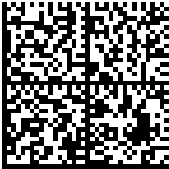
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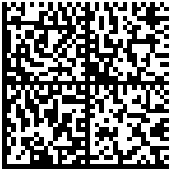
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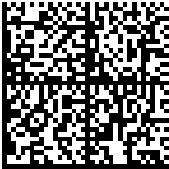
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