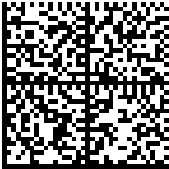


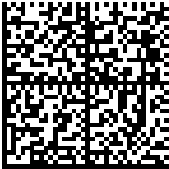
**AMD.BD964**

**(1) 21.09.2026**



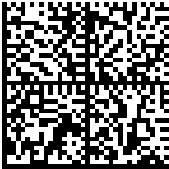
**AMD.BD964**

**(2) 21.09.2026**



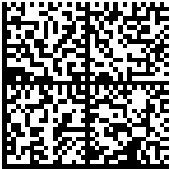
**AMD.BD964**

**(3) 21.09.2026**



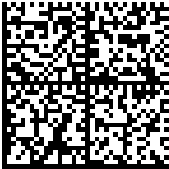
**AMD.BD964**

**(4) 21.09.2026**



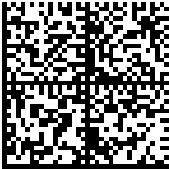
**AMD.BD964**

**(5) 21.09.2026**



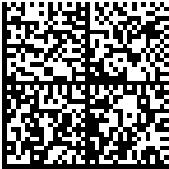
**AMD.BD964**

**(6) 21.09.2026**



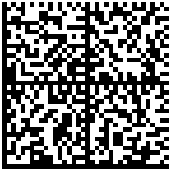
**AMD.BD964**

**(7) 21.09.2026**



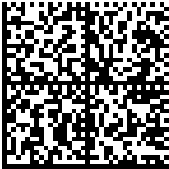
**AMD.BD964**

**(8) 21.09.2026**



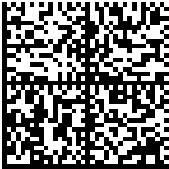
**AMD.BD964**

**(9) 21.09.2026**



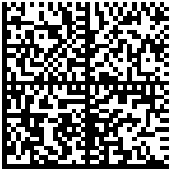
**AMD.BD964**

**(10) 21.09.2026**



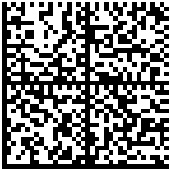
**AMD.BD964**

**(11) 21.09.2026**



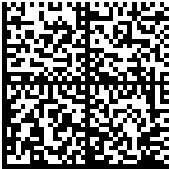
**AMD.BD964**

**(12) 21.09.2026**



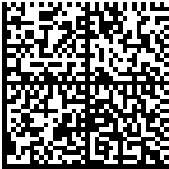
**AMD.BD964**

**(13) 21.09.2026**



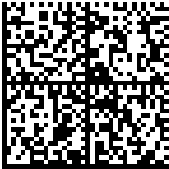
**AMD.BD964**

**(14) 21.09.2026**



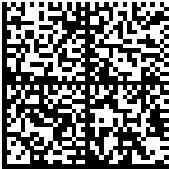
**AMD.BD964**

**(15) 21.09.2026**



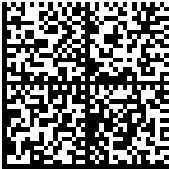
**AMD.BD964**

**(16) 21.09.2026**



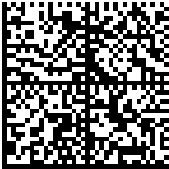
**AMD.BD964**

**(17) 21.09.2026**



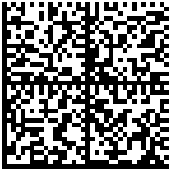
**AMD.BD964**

**(18) 21.09.2026**



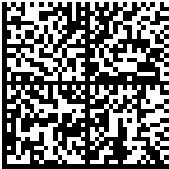
**AMD.BD964**

**(19) 21.09.2026**



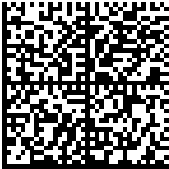
**AMD.BD964**

**(20) 21.09.2026**



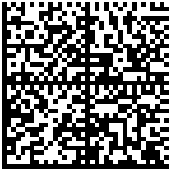
**AMD.BD964**

**(21) 21.09.2026**



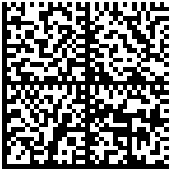
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**(22) 21.09.2026**



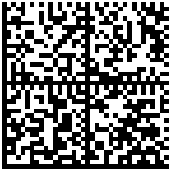
**AMD.BD964**

**(23) 21.09.2026**



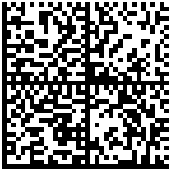
**AMD.BD964**

**(24) 21.09.2026**



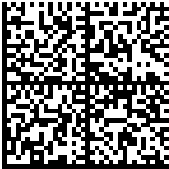
**AMD.BD964**

**(25) 21.09.2026**



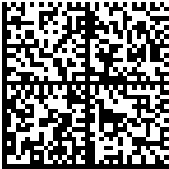
**AMD.BD964**

**(26) 21.09.2026**



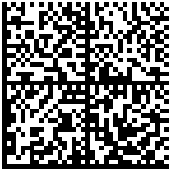
**AMD.BD964**

**(27) 21.09.2026**



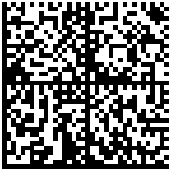
**AMD.BD964**

**(28) 21.09.2026**



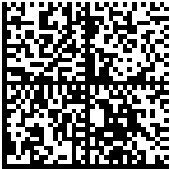
**AMD.BD964**

**(29) 21.09.2026**



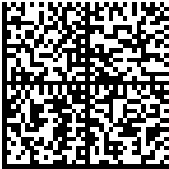
**AMD.BD964**

**(30) 21.09.2026**



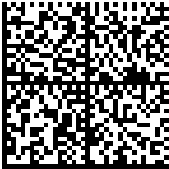
**AMD.BD964**

**(31) 21.09.2026**



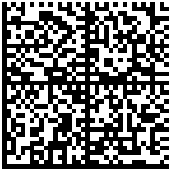
**AMD.BD964**

**(32) 21.09.2026**



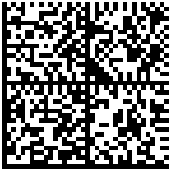
**AMD.BD964**

**(33) 21.09.2026**



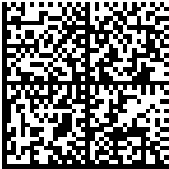
**AMD.BD964**

**(34) 21.09.2026**



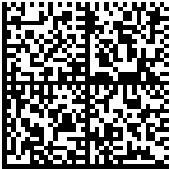
**AMD.BD964**

**(35) 21.09.2026**



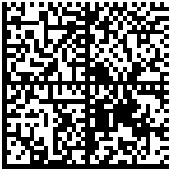
**AMD.BD964**

**(36) 21.09.2026**



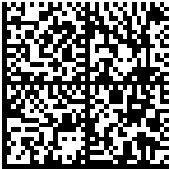
**AMD.BD964**

**(37) 21.09.2026**



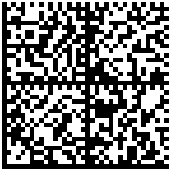
**AMD.BD964**

**(38) 21.09.2026**



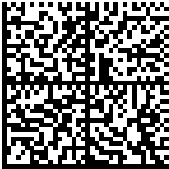
**AMD.BD964**

**(39) 21.09.2026**



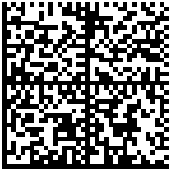
**AMD.BD964**

**(40) 21.09.2026**



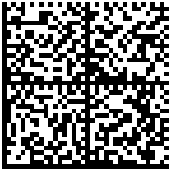
**AMD.BD964**

**(41) 21.09.2026**



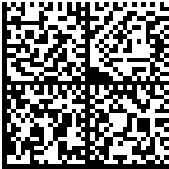
**AMD.BD964**

**(42) 21.09.2026**



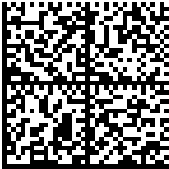
**AMD.BD964**

**(43) 21.09.2026**



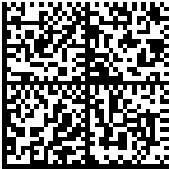
**AMD.BD964**

**(44) 21.09.2026**



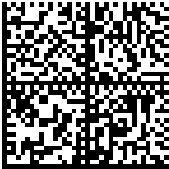
**AMD.BD964**

**(45) 21.09.2026**



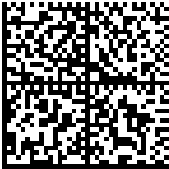
**AMD.BD964**

**(46) 21.09.2026**



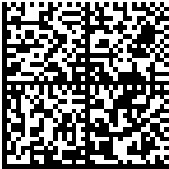
**AMD.BD964**

**(47) 21.09.2026**



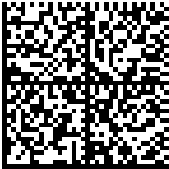
**AMD.BD964**

**(48) 21.09.2026**



**AMD.BD964**

**(49) 21.09.2026**



**AMD.BD964**

**(50) 21.09.2026**