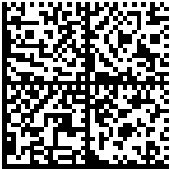


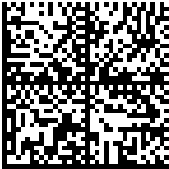
**AMD.BF632**

**(1) 29.07.2026**



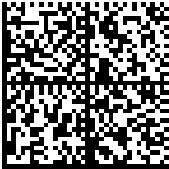
**AMD.BF632**

**(2) 29.07.2026**



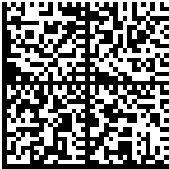
**AMD.BF632**

**(3) 29.07.2026**



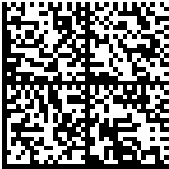
**AMD.BF632**

**(4) 29.07.2026**



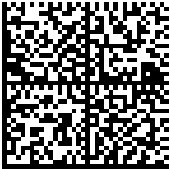
**AMD.BF632**

**(5) 29.07.2026**



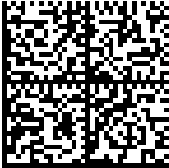
**AMD.BF632**

**(6) 29.07.2026**



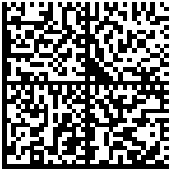
**AMD.BF632**

**(7) 29.07.2026**



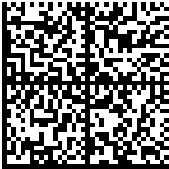
**AMD.BF632**

**(8) 29.07.2026**



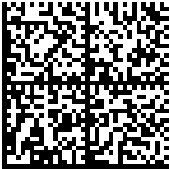
**AMD.BF632**

**(9) 29.07.2026**



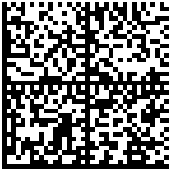
**AMD.BF632**

**(10) 29.07.2026**



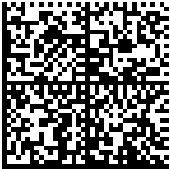
**AMD.BF632**

**(11) 29.07.2026**



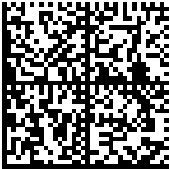
**AMD.BF632**

**(12) 29.07.2026**



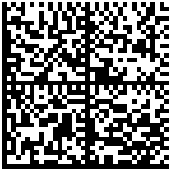
**AMD.BF632**

**(13) 29.07.2026**



**AMD.BF632**

**(14) 29.07.2026**



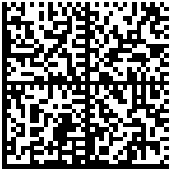
**AMD.BF632**

**(15) 29.07.2026**



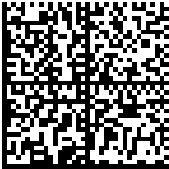
**AMD.BF632**

**(16) 29.07.2026**



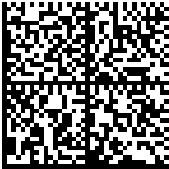
**AMD.BF632**

**(17) 29.07.2026**



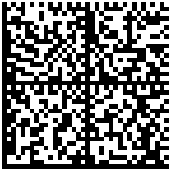
**AMD.BF632**

**(18) 29.07.2026**



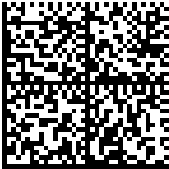
**AMD.BF632**

**(19) 29.07.2026**



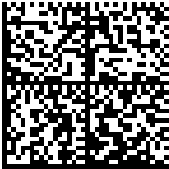
**AMD.BF632**

**(20) 29.07.2026**



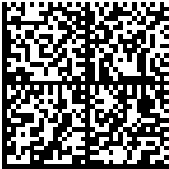
**AMD.BF632**

**(21) 29.07.2026**



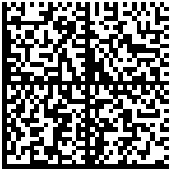
**AMD.BF632**

**(22) 29.07.2026**



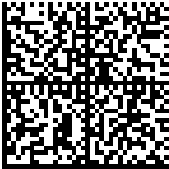
**AMD.BF632**

**(23) 29.07.2026**



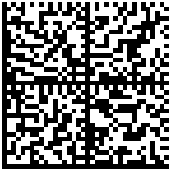
**AMD.BF632**

**(24) 29.07.2026**



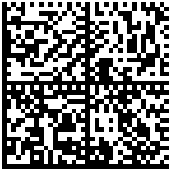
**AMD.BF632**

**(25) 29.07.2026**



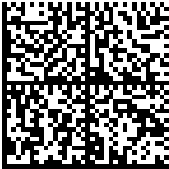
**AMD.BF632**

**(26) 29.07.2026**



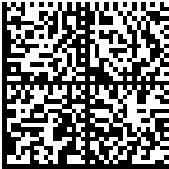
**AMD.BF632**

**(27) 29.07.2026**



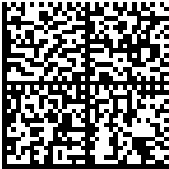
**AMD.BF632**

**(28) 29.07.2026**



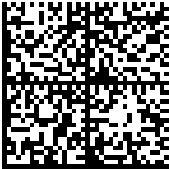
**AMD.BF632**

**(29) 29.07.2026**



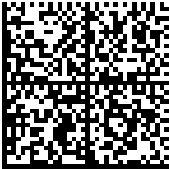
**AMD.BF632**

**(30) 29.07.2026**



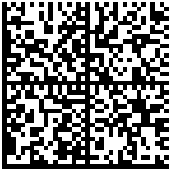
**AMD.BF632**

**(31) 29.07.2026**



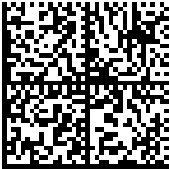
**AMD.BF632**

**(32) 29.07.2026**



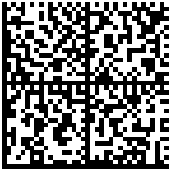
**AMD.BF632**

**(33) 29.07.2026**



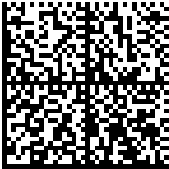
**AMD.BF632**

**(34) 29.07.2026**



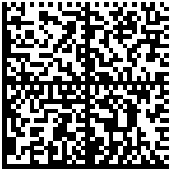
**AMD.BF632**

**(35) 29.07.2026**



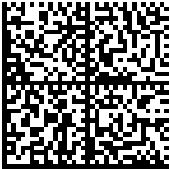
**AMD.BF632**

**(36) 29.07.2026**



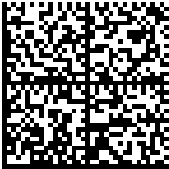
**AMD.BF632**

**(37) 29.07.2026**



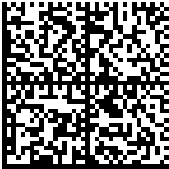
**AMD.BF632**

**(38) 29.07.2026**



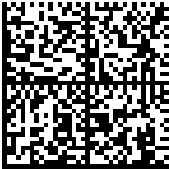
**AMD.BF632**

**(39) 29.07.2026**



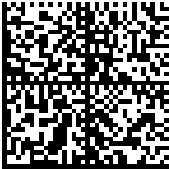
**AMD.BF632**

**(40) 29.07.2026**



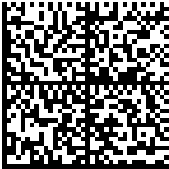
**AMD.BF632**

**(41) 29.07.2026**



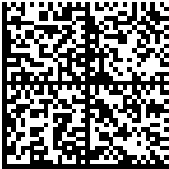
**AMD.BF632**

**(42) 29.07.2026**



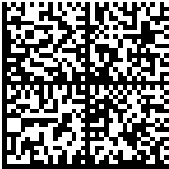
**AMD.BF632**

**(43) 29.07.2026**



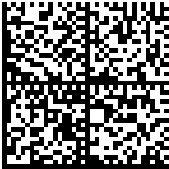
**AMD.BF632**

**(44) 29.07.2026**



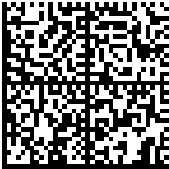
**AMD.BF632**

**(45) 29.07.2026**



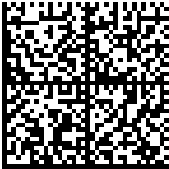
**AMD.BF632**

**(46) 29.07.2026**



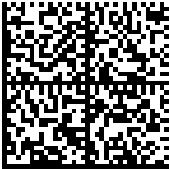
**AMD.BF632**

**(47) 29.07.2026**



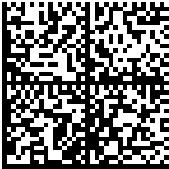
**AMD.BF632**

**(48) 29.07.2026**



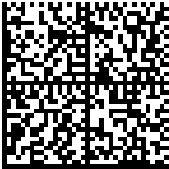
**AMD.BF632**

**(49) 29.07.2026**



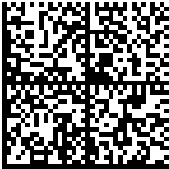
**AMD.BF632**

**(50) 29.07.2026**



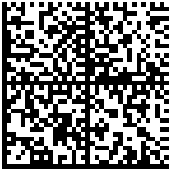
**AMD.BF632**

**(51) 29.07.2026**



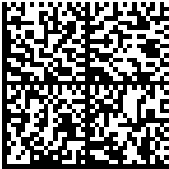
**AMD.BF632**

**(52) 29.07.2026**



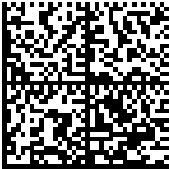
**AMD.BF632**

**(53) 29.07.2026**



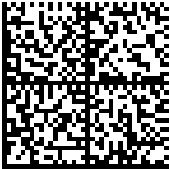
**AMD.BF632**

**(54) 29.07.2026**



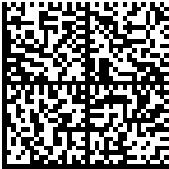
**AMD.BF632**

**(55) 29.07.2026**



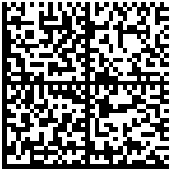
**AMD.BF632**

**(56) 29.07.2026**



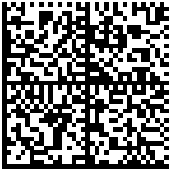
**AMD.BF632**

**(57) 29.07.2026**



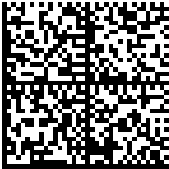
**AMD.BF632**

**(58) 29.07.2026**



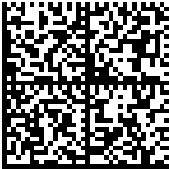
**AMD.BF632**

**(59) 29.07.2026**



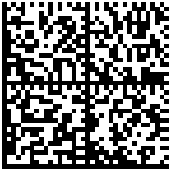
**AMD.BF632**

**(60) 29.07.2026**



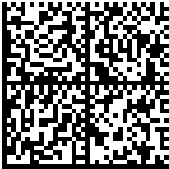
**AMD.BF632**

**(61) 29.07.2026**



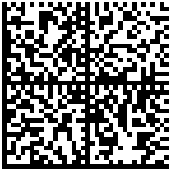
**AMD.BF632**

**(62) 29.07.2026**



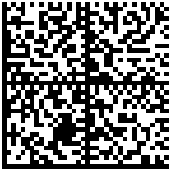
**AMD.BF632**

**(63) 29.07.2026**



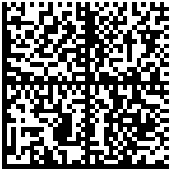
**AMD.BF632**

**(64) 29.07.2026**



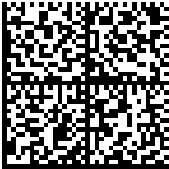
**AMD.BF632**

**(65) 29.07.2026**



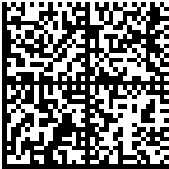
**AMD.BF632**

**(66) 29.07.2026**



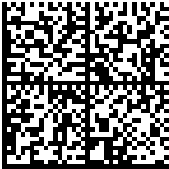
**AMD.BF632**

**(67) 29.07.2026**



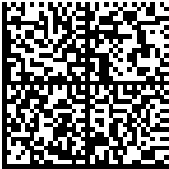
**AMD.BF632**

**(68) 29.07.2026**



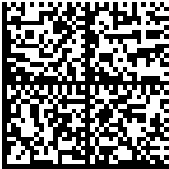
**AMD.BF632**

**(69) 29.07.2026**



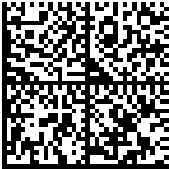
**AMD.BF632**

**(70) 29.07.2026**



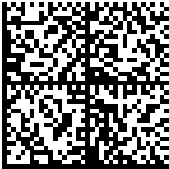
**AMD.BF632**

**(71) 29.07.2026**



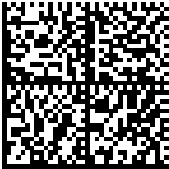
**AMD.BF632**

**(72) 29.07.2026**



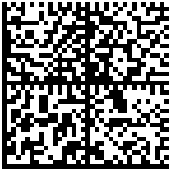
**AMD.BF632**

**(73) 29.07.2026**



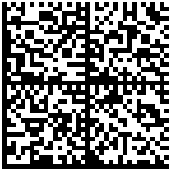
**AMD.BF632**

**(74) 29.07.2026**



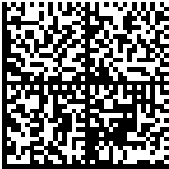
**AMD.BF632**

**(75) 29.07.2026**



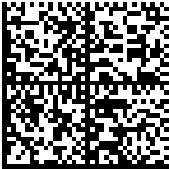
**AMD.BF632**

**(76) 29.07.2026**



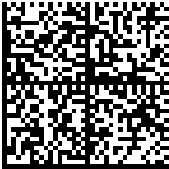
**AMD.BF632**

**(77) 29.07.2026**



**AMD.BF632**

**(78) 29.07.2026**



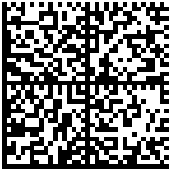
**AMD.BF632**

**(79) 29.07.2026**



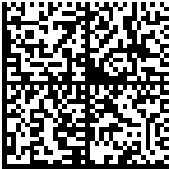
**AMD.BF632**

**(80) 29.07.2026**



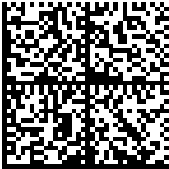
**AMD.BF632**

**(81) 29.07.2026**



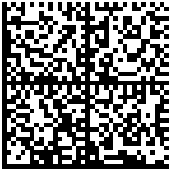
**AMD.BF632**

**(82) 29.07.2026**



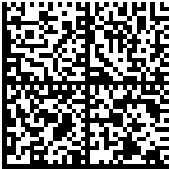
**AMD.BF632**

**(83) 29.07.2026**



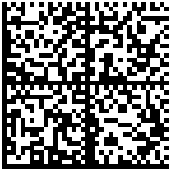
**AMD.BF632**

**(84) 29.07.2026**



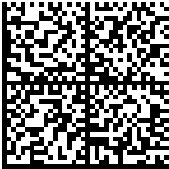
**AMD.BF632**

**(85) 29.07.2026**



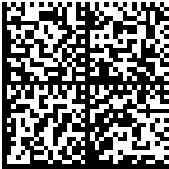
**AMD.BF632**

**(86) 29.07.2026**



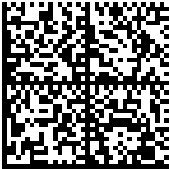
**AMD.BF632**

**(87) 29.07.2026**



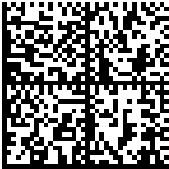
**AMD.BF632**

**(88) 29.07.2026**



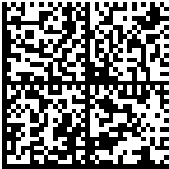
**AMD.BF632**

**(89) 29.07.2026**



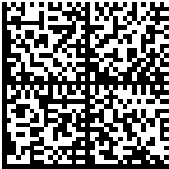
**AMD.BF632**

**(90) 29.07.2026**



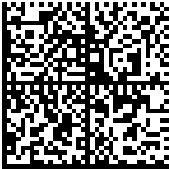
**AMD.BF632**

**(91) 29.07.2026**



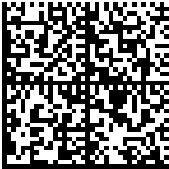
**AMD.BF632**

**(92) 29.07.2026**



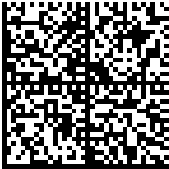
**AMD.BF632**

**(93) 29.07.2026**



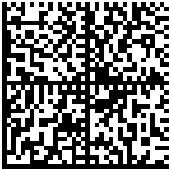
**AMD.BF632**

**(94) 29.07.2026**



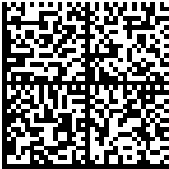
**AMD.BF632**

**(95) 29.07.2026**



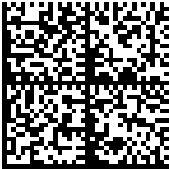
**AMD.BF632**

**(96) 29.07.2026**



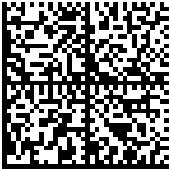
**AMD.BF632**

**(97) 29.07.2026**



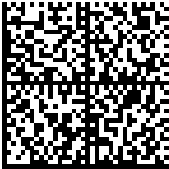
**AMD.BF632**

**(98) 29.07.2026**



**AMD.BF632**

**(99) 29.07.2026**



**AMD.BF632**

**(100) 29.07.2026**