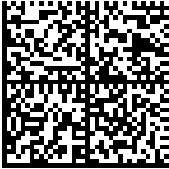
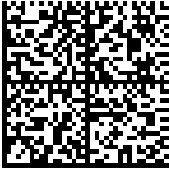


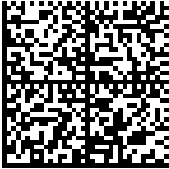
-1-



AMD.BD487



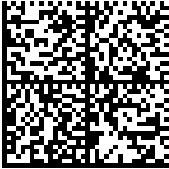
**AMD.BD487**



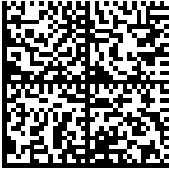
-3-

AMD.BD487

-4-



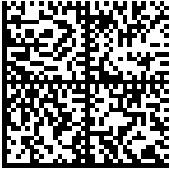
AMD.BD487



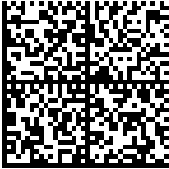
**-5-**

**AMD.BD487**

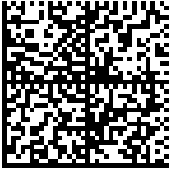
**-6-**



**AMD.BD487**



**AMD.BD487**

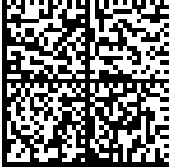


-8-

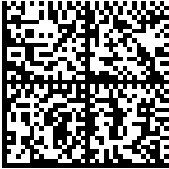
AMD.BD487



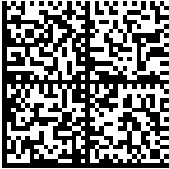
-9-



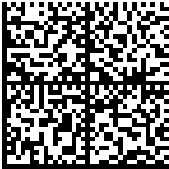
AMD.BD487



**AMD.BD487**

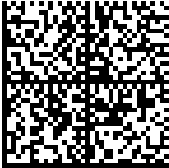


**AMD.BD487**

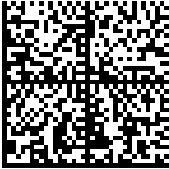


**AMD.BD487**

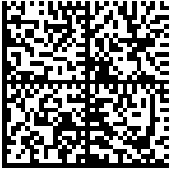
**-12-**



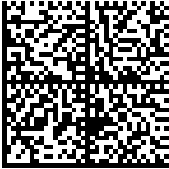
AMD.BD487



AMD.BD487

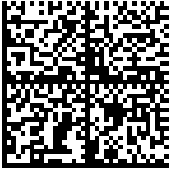


**AMD.BD487**

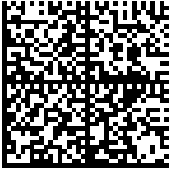


**AMD.BD487**

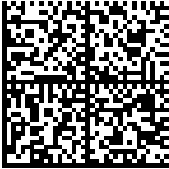




**AMD.BD487**

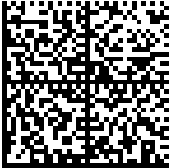


AMD.BD487

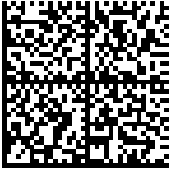


**AMD.BD487**

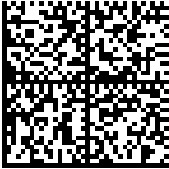
**-19-**



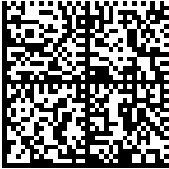
AMD.BD487



**AMD.BD487**

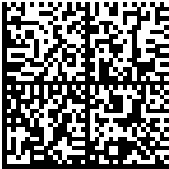


**AMD.BD487**



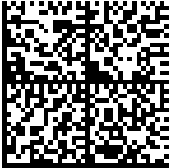
**AMD.BD487**

**-23-**

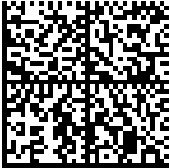


**AMD.BD487**

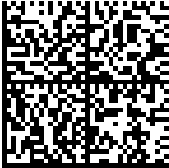




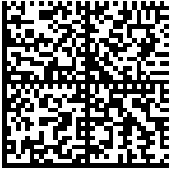
**AMD.BD487**



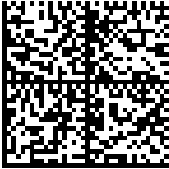
AMD.BD487



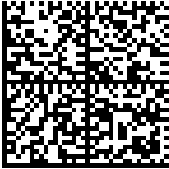
**AMD.BD487**



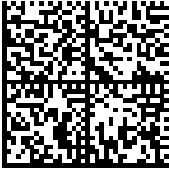
AMD.BD487



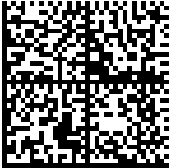
**AMD.BD487**



AMD.BD487

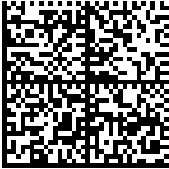


AMD.BD487

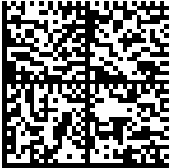


AMD.BD487

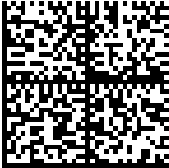




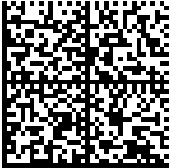
**AMD.BD487**



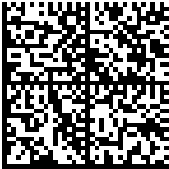
AMD.BD487



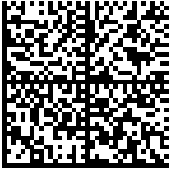
**AMD.BD487**



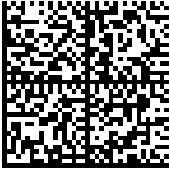
**AMD.BD487**



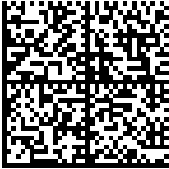
**AMD.BD487**



**AMD.BD487**

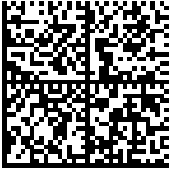


**AMD.BD487**

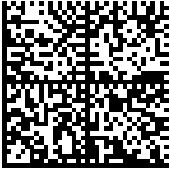


**AMD.BD487**

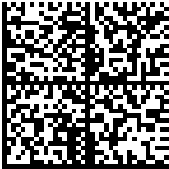




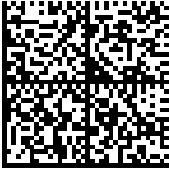
**AMD.BD487**



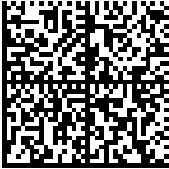
AMD.BD487



AMD.BD487



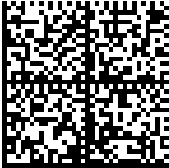
AMD.BD487



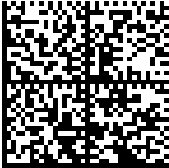
**AMD.BD487**



**AMD.BD487**

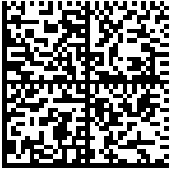


**AMD.BD487**

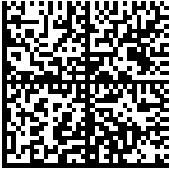


AMD.BD487

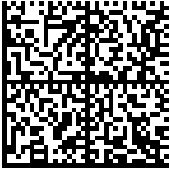




**AMD.BD487**

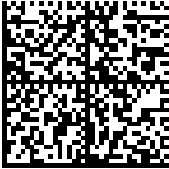


**AMD.BD487**

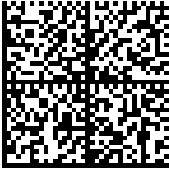


**AMD.BD487**

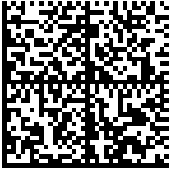
**-51-**



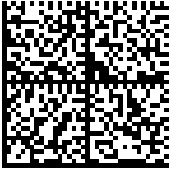
**AMD.BD487**



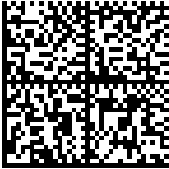
**AMD.BD487**



**AMD.BD487**

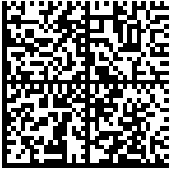


**AMD.BD487**

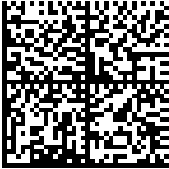


**AMD.BD487**

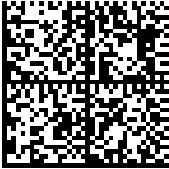




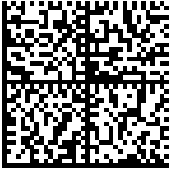
**AMD.BD487**



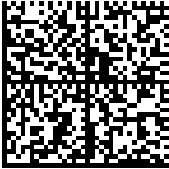
**AMD.BD487**



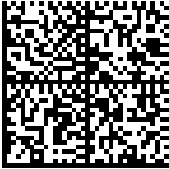
**AMD.BD487**



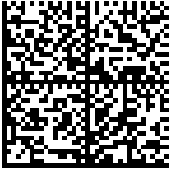
**AMD.BD487**



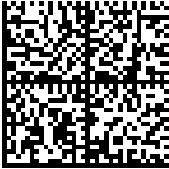
AMD.BD487



**AMD.BD487**

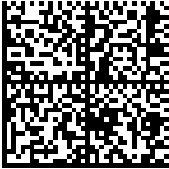


AMD.BD487



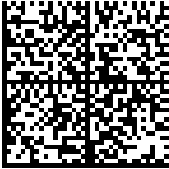
**AMD.BD487**



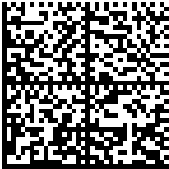


**AMD.BD487**

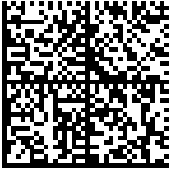
**-65-**



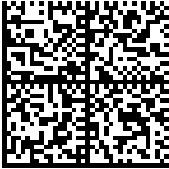
**AMD.BD487**



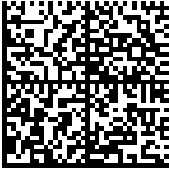
AMD.BD487



**AMD.BD487**

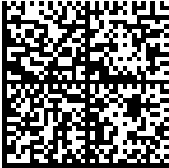


AMD.BD487

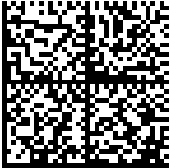


**AMD.BD487**

**-70-**

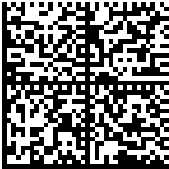


**AMD.BD487**



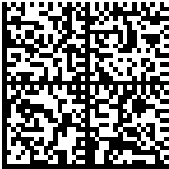
**AMD.BD487**



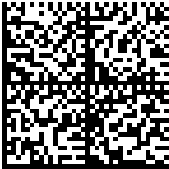


**AMD.BD487**

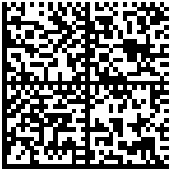
**-73-**



AMD.BD487

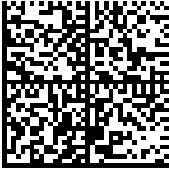


**AMD.BD487**

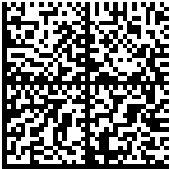


**AMD.BD487**

**-76-**

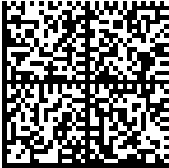


**AMD.BD487**

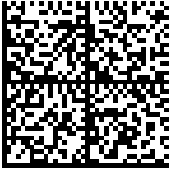


AMD.BD487

-78-



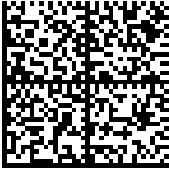
**AMD.BD487**



**-80-**

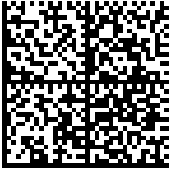
**AMD.BD487**



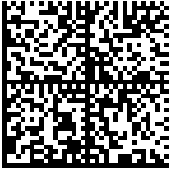


AMD.BD487

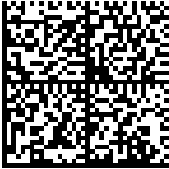
-81-



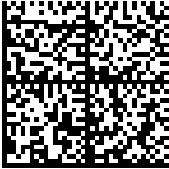
**AMD.BD487**



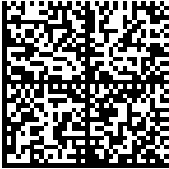
**AMD.BD487**



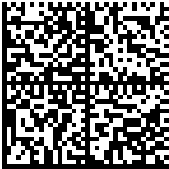
AMD.BD487



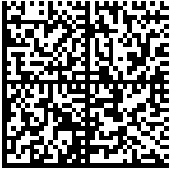
**AMD.BD487**



AMD.BD487



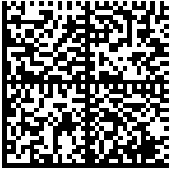
**AMD.BD487**



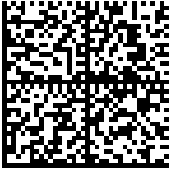
-88-

AMD.BD487

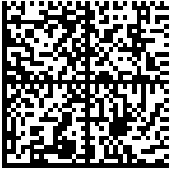




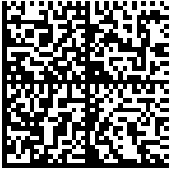
AMD.BD487



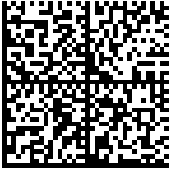
**AMD.BD487**



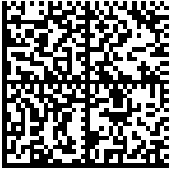
AMD.BD487



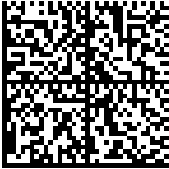
**AMD.BD487**



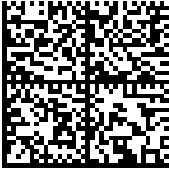
AMD.BD487



AMD.BD487

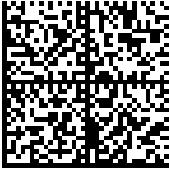


AMD.BD487

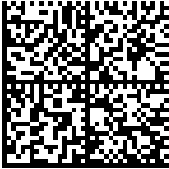


**AMD.BD487**

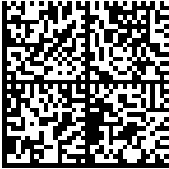




AMD.BD487

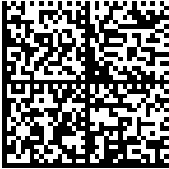


**AMD.BD487**



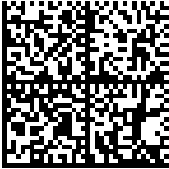
**-99-**

**AMD.BD487**

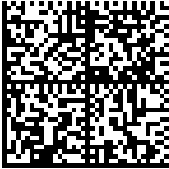


**-100-**

**AMD.BD487**

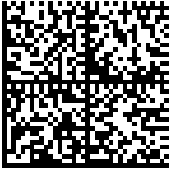


AMD.BD487



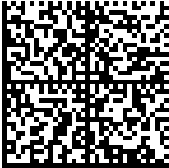
**AMD.BD487**

**-102-**



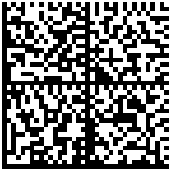
**AMD.BD487**

**-103-**



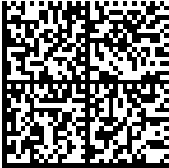
AMD.BD487





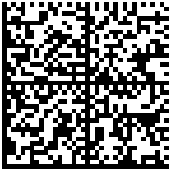
**AMD.BD487**

**-105-**



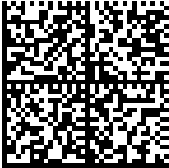
**AMD.BD487**

**-106-**



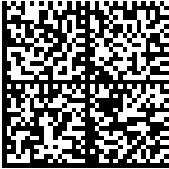
AMD.BD487

-107-



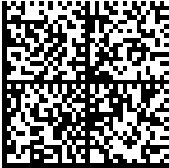
AMD.BD487

-108-



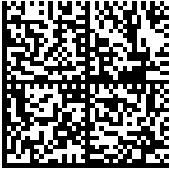
**-109-**

**AMD.BD487**



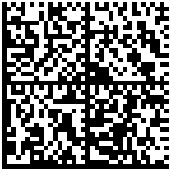
**AMD.BD487**

**-110-**



-111-

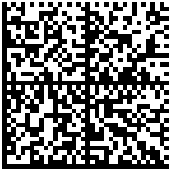
AMD.BD487



**AMD.BD487**

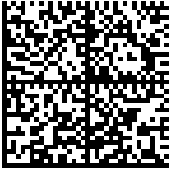
**-112-**





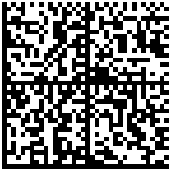
**AMD.BD487**

**-113-**



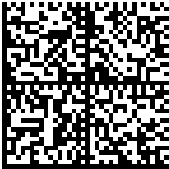
**AMD.BD487**

**-114-**



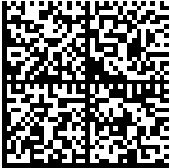
**AMD.BD487**

**-115-**



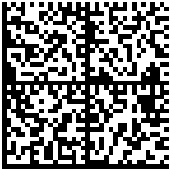
AMD.BD487

-116-



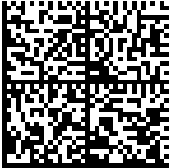
**AMD.BD487**

**-117-**



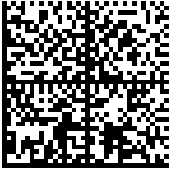
**AMD.BD487**

**-118-**



**AMD.BD487**

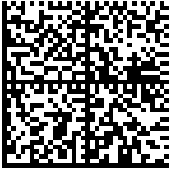
**-119-**



**AMD.BD487**

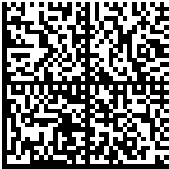
**-120-**



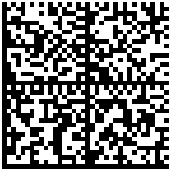


**AMD.BD487**

**-121-**

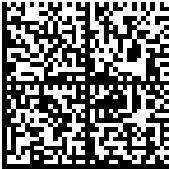


**AMD.BD487**



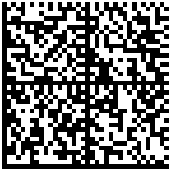
AMD.BD487

-123-



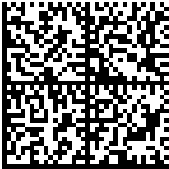
**AMD.BD487**

**-124-**



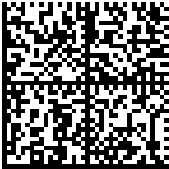
**AMD.BD487**

**-125-**



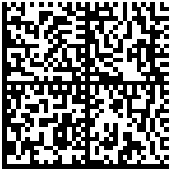
**AMD.BD487**

**-126-**



AMD.BD487

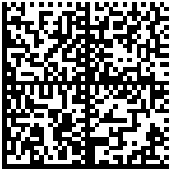
-127-



**AMD.BD487**

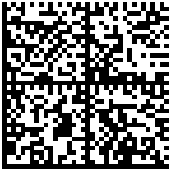
**-128-**





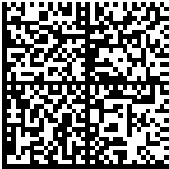
AMD.BD487

-129-



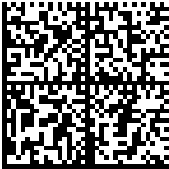
**AMD.BD487**

**-130-**



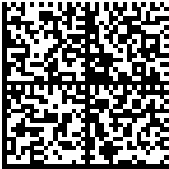
AMD.BD487

-131-



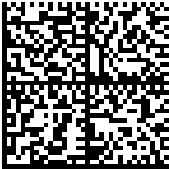
**AMD.BD487**

**-132-**



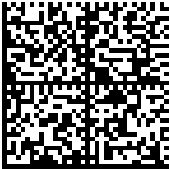
**AMD.BD487**

**-133-**



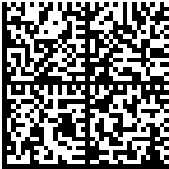
AMD.BD487

-134-



AMD.BD487

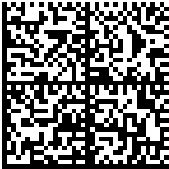
-135-



**AMD.BD487**

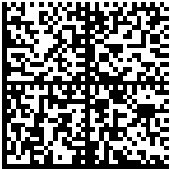
**-136-**





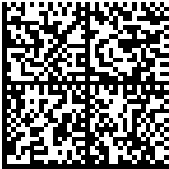
**AMD.BD487**

**-137-**



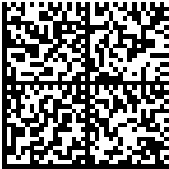
AMD.BD487

-138-



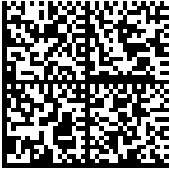
**AMD.BD487**

**-139-**

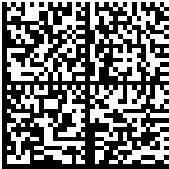


**AMD.BD487**

**-140-**

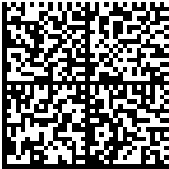


AMD.BD487



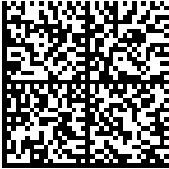
**AMD.BD487**

**-142-**



**AMD.BD487**

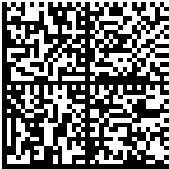
**-143-**



**AMD.BD487**

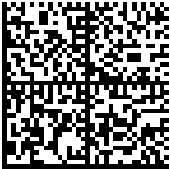
**-144-**





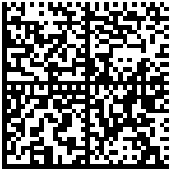
**AMD.BD487**

**-145-**



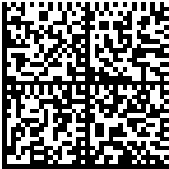
AMD.BD487

-146-



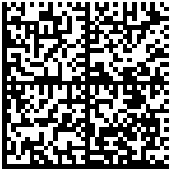
AMD.BD487

-147-



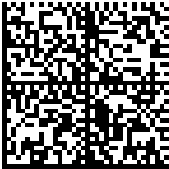
AMD.BD487

-148-



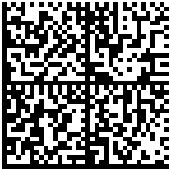
**AMD.BD487**

**-149-**



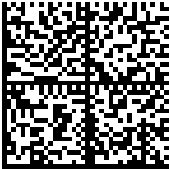
**AMD.BD487**

**-150-**



**AMD.BD487**

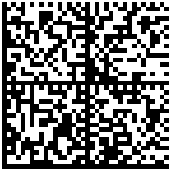
**-151-**



**AMD.BD487**

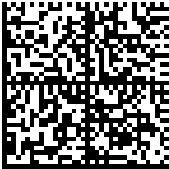
**-152-**



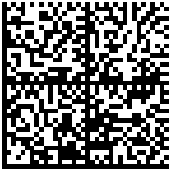


**AMD.BD487**

**-153-**

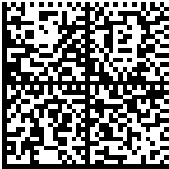


AMD.BD487



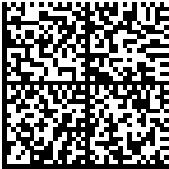
**AMD.BD487**

**-155-**



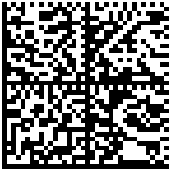
**AMD.BD487**

**-156-**



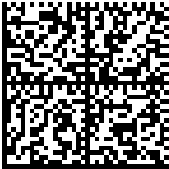
**AMD.BD487**

**-157-**



AMD.BD487

-158-



**AMD.BD487**

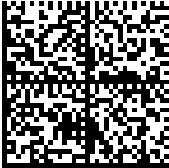
**-159-**



**AMD.BD487**

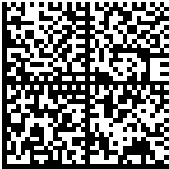
**-160-**





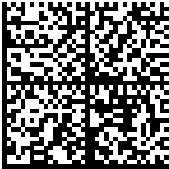
AMD.BD487

-161-



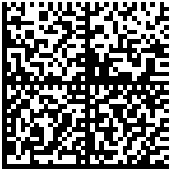
**AMD.BD487**

**-162-**



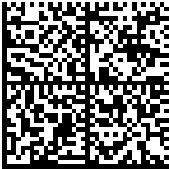
AMD.BD487

-163-



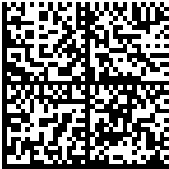
AMD.BD487

-164-



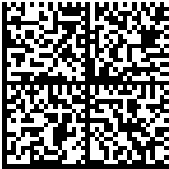
AMD.BD487

-165-



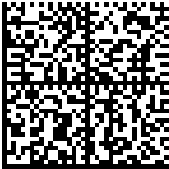
**AMD.BD487**

**-166-**



**AMD.BD487**

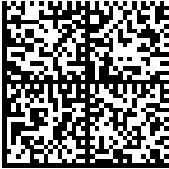
**-167-**



**AMD.BD487**

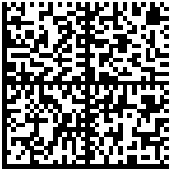
**-168-**





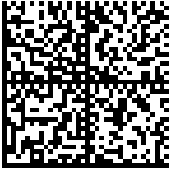
AMD.BD487

-169-



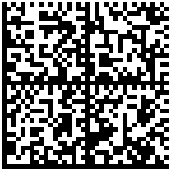
AMD.BD487

-170-



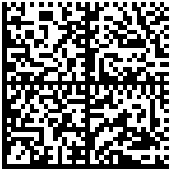
AMD.BD487

-171-



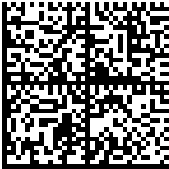
AMD.BD487

-172-

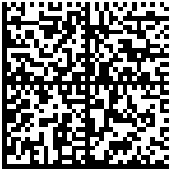


**AMD.BD487**

**-173-**

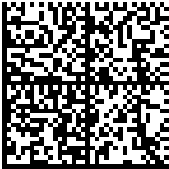


AMD.BD487



**AMD.BD487**

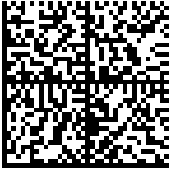
**-175-**



AMD.BD487

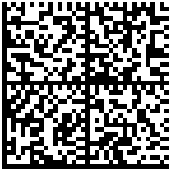
-176-





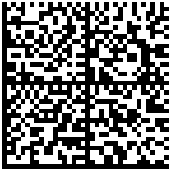
AMD.BD487

-177-



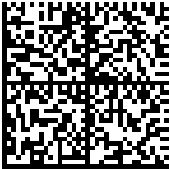
AMD.BD487

-178-



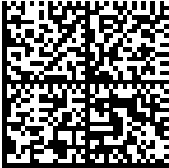
**AMD.BD487**

**-179-**



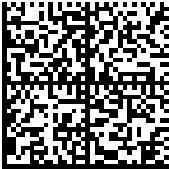
**AMD.BD487**

**-180-**



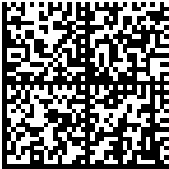
**AMD.BD487**

**-181-**



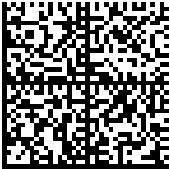
AMD.BD487

-182-



AMD.BD487

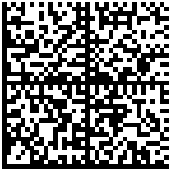
-183-



**AMD.BD487**

**-184-**





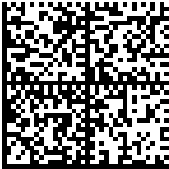
AMD.BD487

-185-



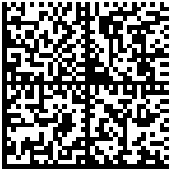
AMD.BD487

-186-



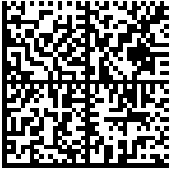
AMD.BD487

-187-

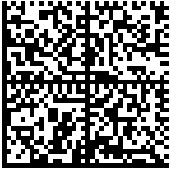


**AMD.BD487**

**-188-**

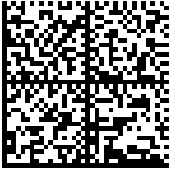


AMD.BD487



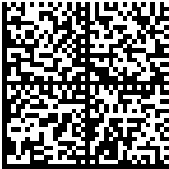
**AMD.BD487**

**-190-**



AMD.BD487

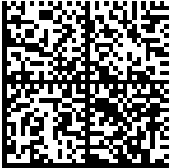
-191-



AMD.BD487

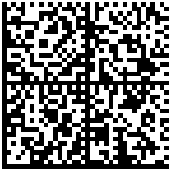
-192-





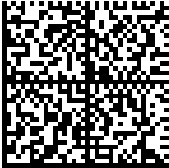
AMD.BD487

-193-



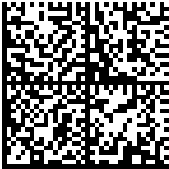
AMD.BD487

-194-



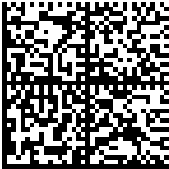
**AMD.BD487**

**-195-**



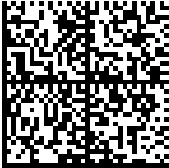
AMD.BD487

-196-

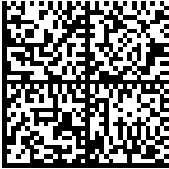


**AMD.BD487**

**-197-**

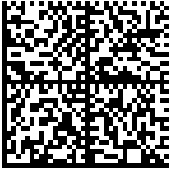


AMD.BD487



-199-

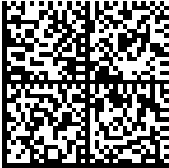
AMD.BD487



**AMD.BD487**

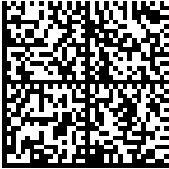
**-200-**





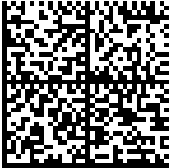
**AMD.BD487**

**-201-**



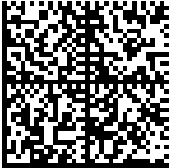
**-202-**

**AMD.BD487**



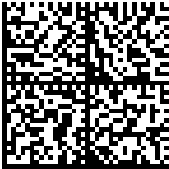
**AMD.BD487**

**-203-**



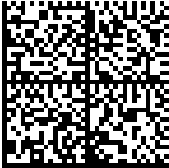
**AMD.BD487**

**-204-**



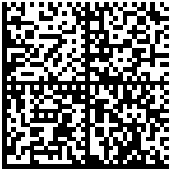
**AMD.BD487**

**-205-**

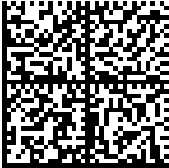


**AMD.BD487**

**-206-**



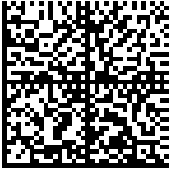
**AMD.BD487**



**AMD.BD487**

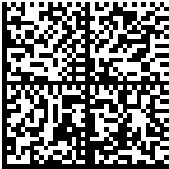
**-208-**





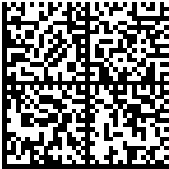
**AMD.BD487**

**-209-**



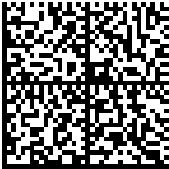
**AMD.BD487**

**-210-**



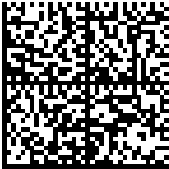
**AMD.BD487**

**-211-**



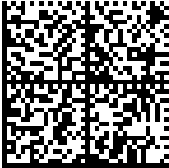
**AMD.BD487**

**-212-**



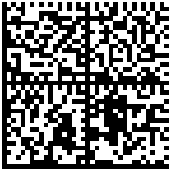
**-213-**

**AMD.BD487**



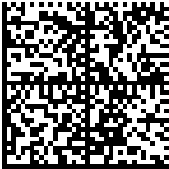
AMD.BD487

-214-



**AMD.BD487**

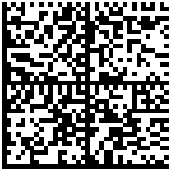
**-215-**



**AMD.BD487**

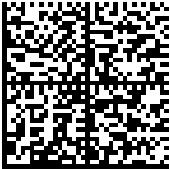
**-216-**





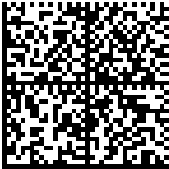
**AMD.BD487**

**-217-**



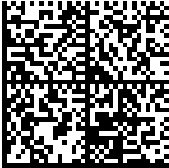
**AMD.BD487**

**-218-**



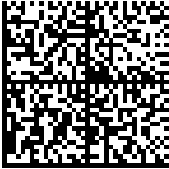
AMD.BD487

-219-



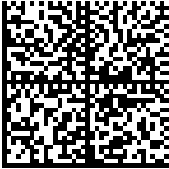
**AMD.BD487**

**-220-**



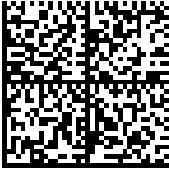
AMD.BD487

-221-



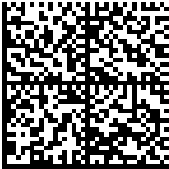
**AMD.BD487**

**-222-**



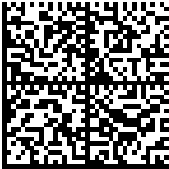
**AMD.BD487**

**-223-**



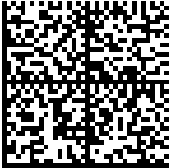
AMD.BD487





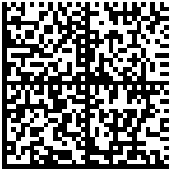
**AMD.BD487**

**-225-**

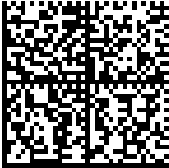


**AMD.BD487**

**-226-**

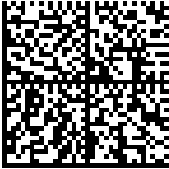


AMD.BD487



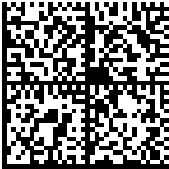
AMD.BD487

-228-



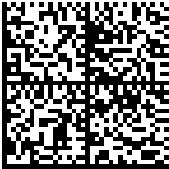
**AMD.BD487**

**-229-**



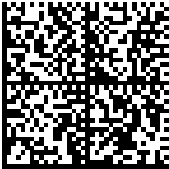
**AMD.BD487**

**-230-**



**AMD.BD487**

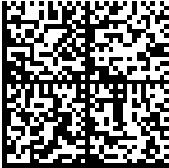
**-231-**



**AMD.BD487**

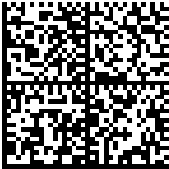
**-232-**





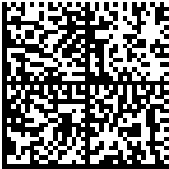
**AMD.BD487**

**-233-**



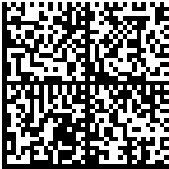
**AMD.BD487**

**-234-**



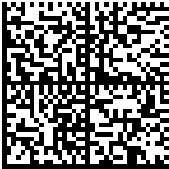
**AMD.BD487**

**-235-**



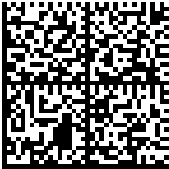
**AMD.BD487**

**-236-**



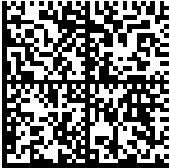
**AMD.BD487**

**-237-**



**AMD.BD487**

**-238-**



AMD.BD487

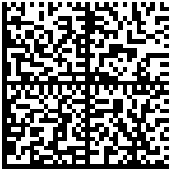
-239-



**AMD.BD487**

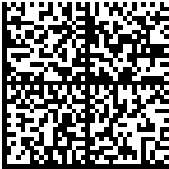
**-240-**





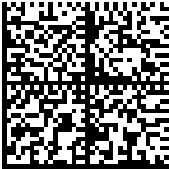
AMD.BD487

-241-



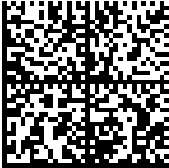
AMD.BD487

-242-



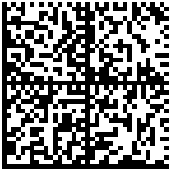
AMD.BD487

-243-



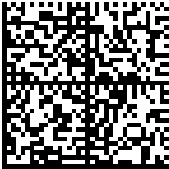
AMD.BD487

-244-



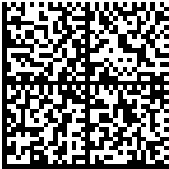
AMD.BD487

-245-



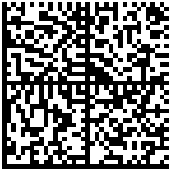
AMD.BD487

-246-



**AMD.BD487**

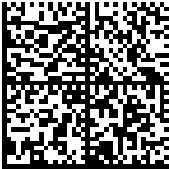
**-247-**



AMD.BD487

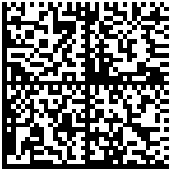
-248-





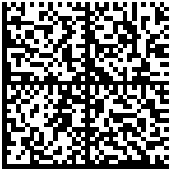
AMD.BD487

-249-



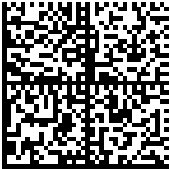
**AMD.BD487**

**-250-**



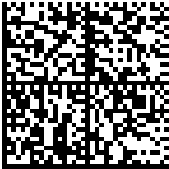
**AMD.BD487**

**-251-**



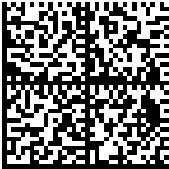
AMD.BD487

-252-

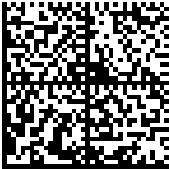


**AMD.BD487**

**-253-**

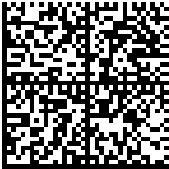


AMD.BD487



**-255-**

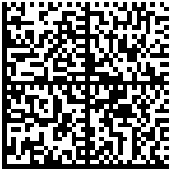
**AMD.BD487**



**-256-**

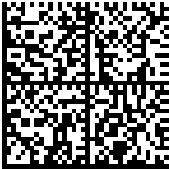
**AMD.BD487**





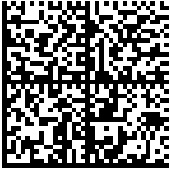
**AMD.BD487**

**-257-**



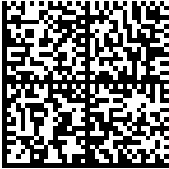
**AMD.BD487**

**-258-**



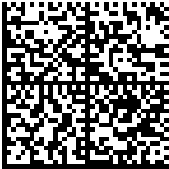
**AMD.BD487**

**-259-**



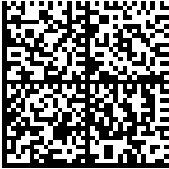
**AMD.BD487**

**-260-**



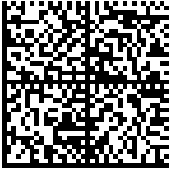
**AMD.BD487**

**-261-**



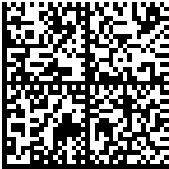
**AMD.BD487**

**-262-**



**AMD.BD487**

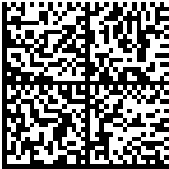
**-263-**



**AMD.BD487**

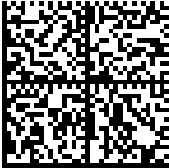
**-264-**





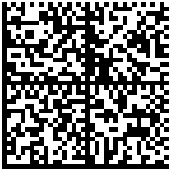
**AMD.BD487**

**-265-**



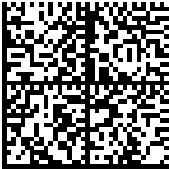
**AMD.BD487**

**-266-**



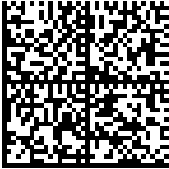
**AMD.BD487**

**-267-**



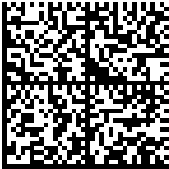
**AMD.BD487**

**-268-**



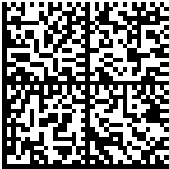
**AMD.BD487**

**-269-**



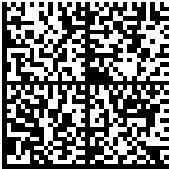
**AMD.BD487**

**-270-**



AMD.BD487

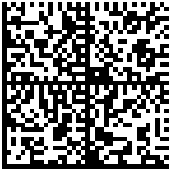
-271-



AMD.BD487

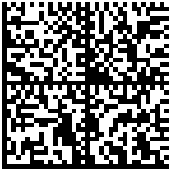
-272-





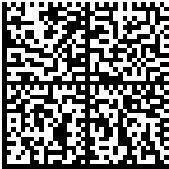
**AMD.BD487**

**-273-**



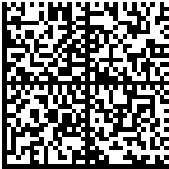
**AMD.BD487**

**-274-**

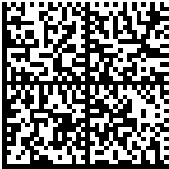


**AMD.BD487**

**-275-**

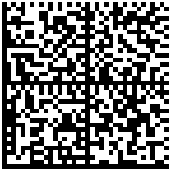


AMD.BD487



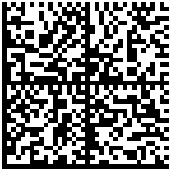
**AMD.BD487**

**-277-**



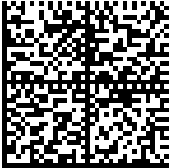
**AMD.BD487**

**-278-**



AMD.BD487

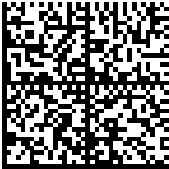
-279-



**AMD.BD487**

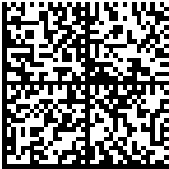
**-280-**





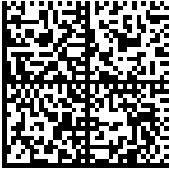
AMD.BD487

-281-



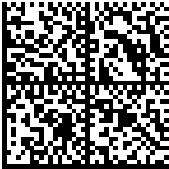
**AMD.BD487**

**-282-**



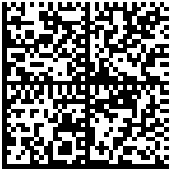
AMD.BD487

-283-



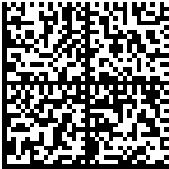
AMD.BD487

-284-



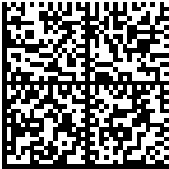
AMD.BD487

-285-



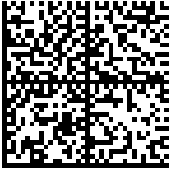
**AMD.BD487**

**-286-**



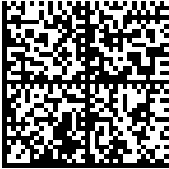
**AMD.BD487**

**-287-**



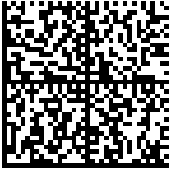
AMD.BD487





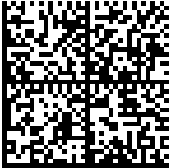
**AMD.BD487**

**-289-**



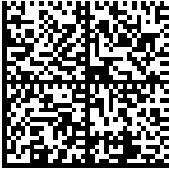
**AMD.BD487**

**-290-**

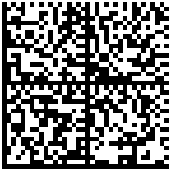


**AMD.BD487**

**-291-**

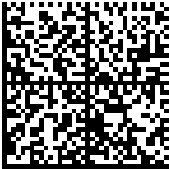


**AMD.BD487**

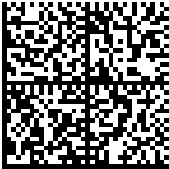


**AMD.BD487**

**-293-**

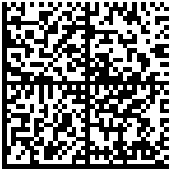


AMD.BD487



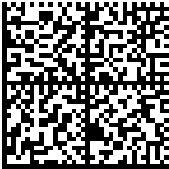
**-295-**

**AMD.BD487**



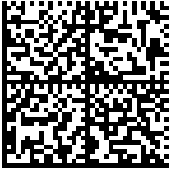
AMD.BD487



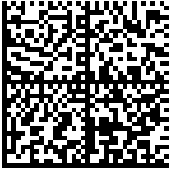


AMD.BD487

-297-

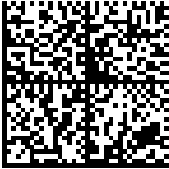


AMD.BD487



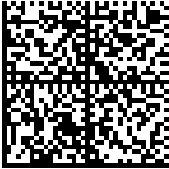
**AMD.BD487**

**-299-**



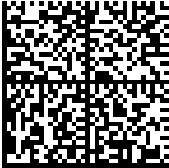
**-300-**

**AMD.BD487**



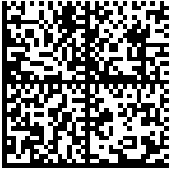
**AMD.BD487**

**-301-**



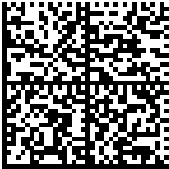
**AMD.BD487**

**-302-**



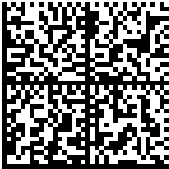
**AMD.BD487**

**-303-**



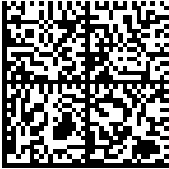
AMD.BD487





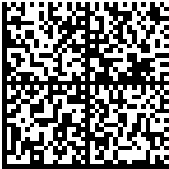
**-305-**

**AMD.BD487**



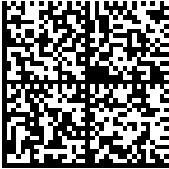
AMD.BD487

-306-



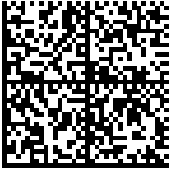
**AMD.BD487**

**-307-**



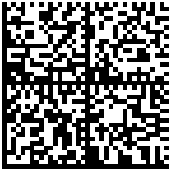
**AMD.BD487**

**-308-**



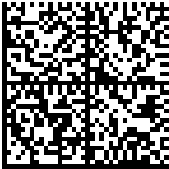
**-309-**

**AMD.BD487**



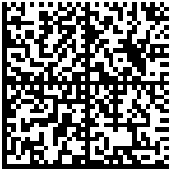
**AMD.BD487**

**-310-**



**AMD.BD487**

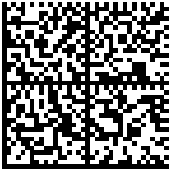
**-311-**



**AMD.BD487**

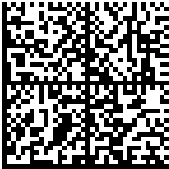
**-312-**





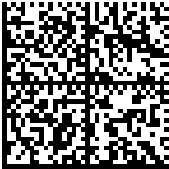
**AMD.BD487**

**-313-**



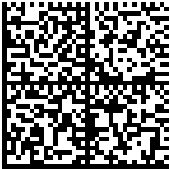
AMD.BD487

-314-



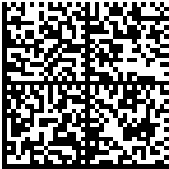
**AMD.BD487**

**-315-**



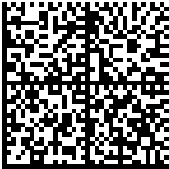
**AMD.BD487**

**-316-**



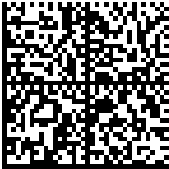
AMD.BD487

-317-



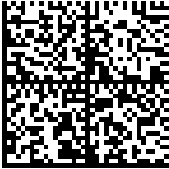
**AMD.BD487**

**-318-**



**AMD.BD487**

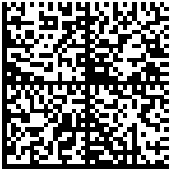
**-319-**



AMD.BD487

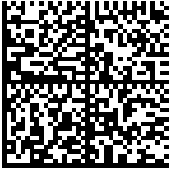
-320-





**AMD.BD487**

**-321-**



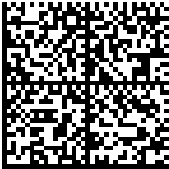
**AMD.BD487**

**-322-**



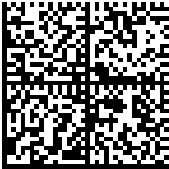
**AMD.BD487**

**-323-**



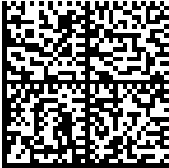
**AMD.BD487**

**-324-**



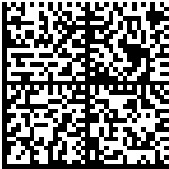
**AMD.BD487**

**-325-**



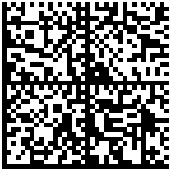
AMD.BD487

-326-



**AMD.BD487**

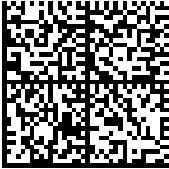
**-327-**



AMD.BD487

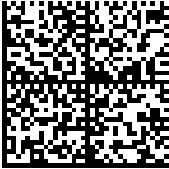
-328-





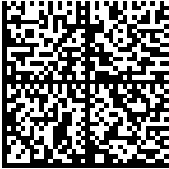
AMD.BD487

-329-



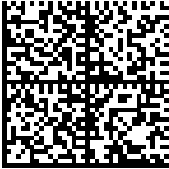
**AMD.BD487**

**-330-**



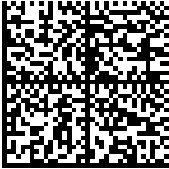
AMD.BD487

-331-



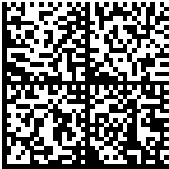
**AMD.BD487**

**-332-**



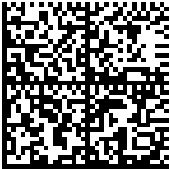
**-333-**

**AMD.BD487**



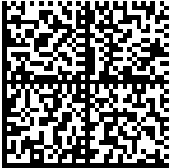
**AMD.BD487**

**-334-**



AMD.BD487

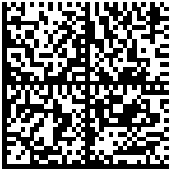
-335-



**AMD.BD487**

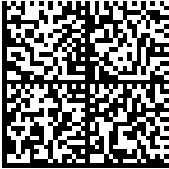
**-336-**





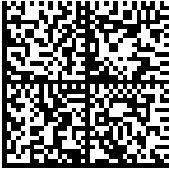
**AMD.BD487**

**-337-**



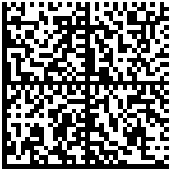
**-338-**

**AMD.BD487**



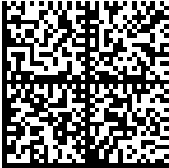
AMD.BD487

-339-



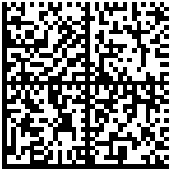
**AMD.BD487**

**-340-**



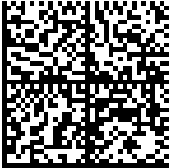
**AMD.BD487**

**-341-**



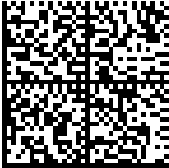
AMD.BD487

-342-



AMD.BD487

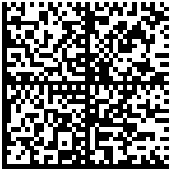
-343-



AMD.BD487

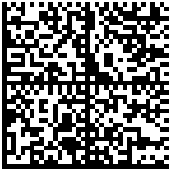
-344-





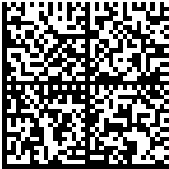
**AMD.BD487**

**-345-**



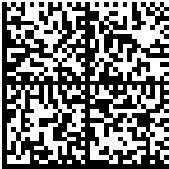
**AMD.BD487**

**-346-**



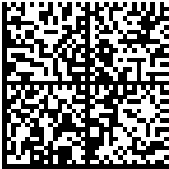
AMD.BD487

-347-



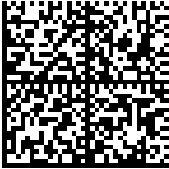
AMD.BD487

-348-



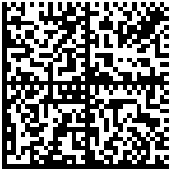
AMD.BD487

-349-



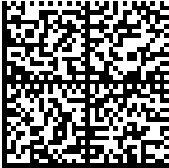
**AMD.BD487**

**-350-**



**AMD.BD487**

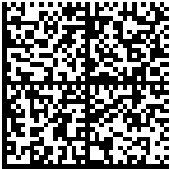
**-351-**



**AMD.BD487**

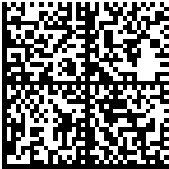
**-352-**





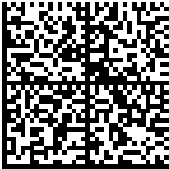
**AMD.BD487**

**-353-**



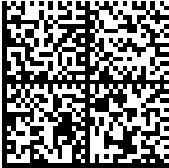
**AMD.BD487**

**-354-**



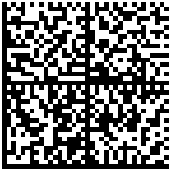
**AMD.BD487**

**-355-**



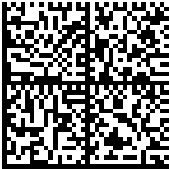
AMD.BD487

-356-



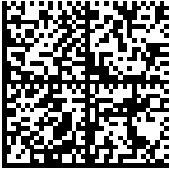
**AMD.BD487**

**-357-**



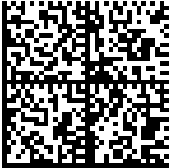
**AMD.BD487**

**-358-**



AMD.BD487

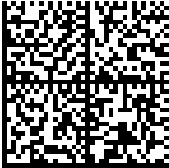
-359-



**AMD.BD487**

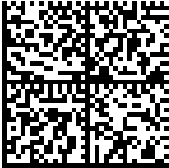
**-360-**





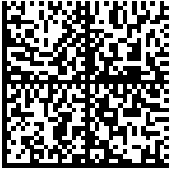
AMD.BD487

-361-



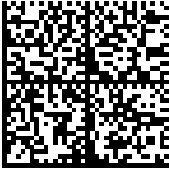
**AMD.BD487**

**-362-**

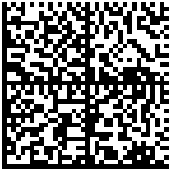


**AMD.BD487**

**-363-**

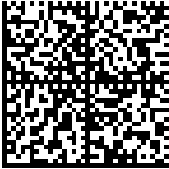


AMD.BD487



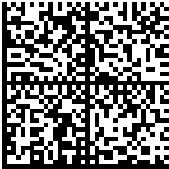
**AMD.BD487**

**-365-**



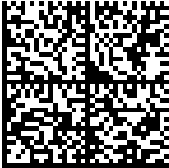
**-366-**

**AMD.BD487**



**AMD.BD487**

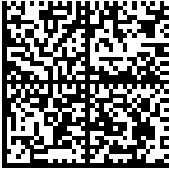
**-367-**



**-368-**

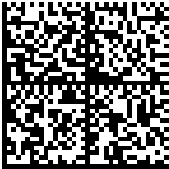
**AMD.BD487**





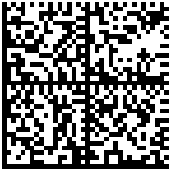
**AMD.BD487**

**-369-**



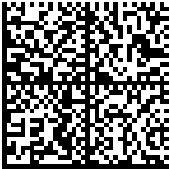
**AMD.BD487**

**-370-**



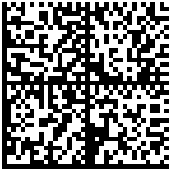
**AMD.BD487**

**-371-**



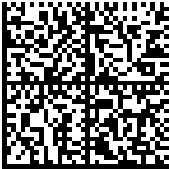
**AMD.BD487**

**-372-**



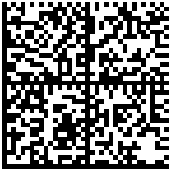
**AMD.BD487**

**-373-**



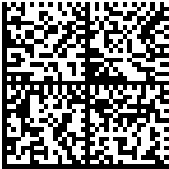
AMD.BD487

-374-



**AMD.BD487**

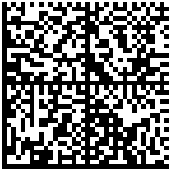
**-375-**



**AMD.BD487**

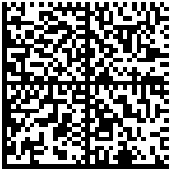
**-376-**





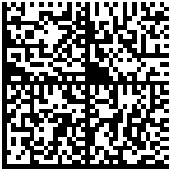
**AMD.BD487**

**-377-**



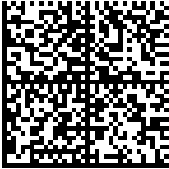
**AMD.BD487**

**-378-**



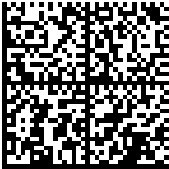
AMD.BD487

-379-

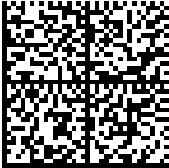


**-380-**

**AMD.BD487**



AMD.BD487



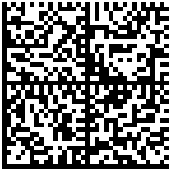
**AMD.BD487**

**-382-**



**-383-**

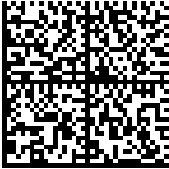
**AMD.BD487**



**AMD.BD487**

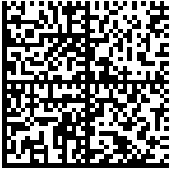
**-384-**





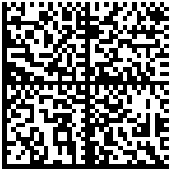
**AMD.BD487**

**-385-**



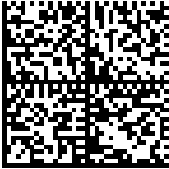
**-386-**

**AMD.BD487**



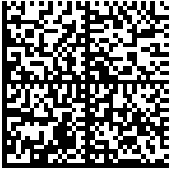
**AMD.BD487**

**-387-**

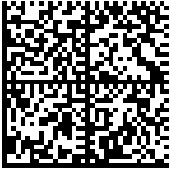


**-388-**

**AMD.BD487**

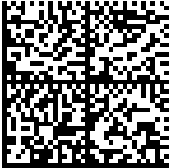


AMD.BD487



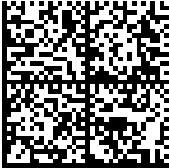
**AMD.BD487**

**-390-**



**AMD.BD487**

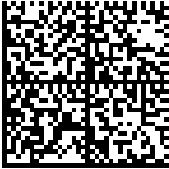
**-391-**



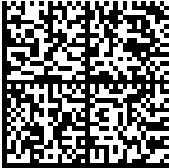
**AMD.BD487**

**-392-**



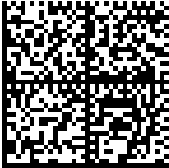


**AMD.BD487**



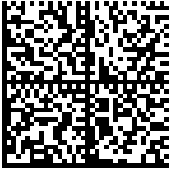
**AMD.BD487**

**-394-**



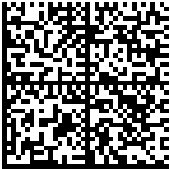
**AMD.BD487**

**-395-**



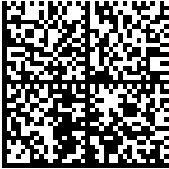
**-396-**

**AMD.BD487**



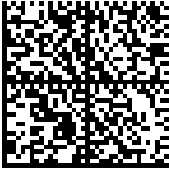
AMD.BD487

-397-



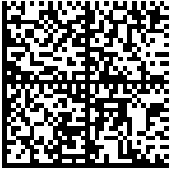
**AMD.BD487**

**-398-**



**-399-**

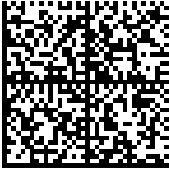
**AMD.BD487**



**AMD.BD487**

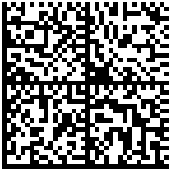
**-400-**





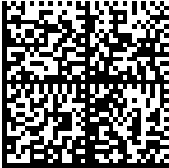
AMD.BD487

-401-



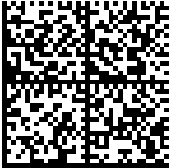
AMD.BD487

-402-



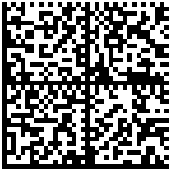
**-403-**

**AMD.BD487**



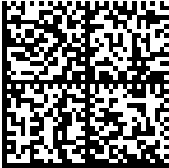
**AMD.BD487**

**-404-**



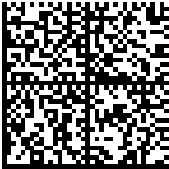
AMD.BD487

-405-



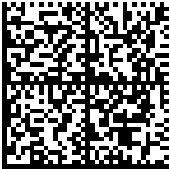
AMD.BD487

-406-



AMD.BD487

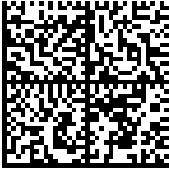
-407-



AMD.BD487

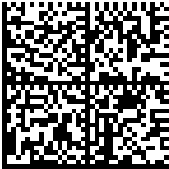
-408-





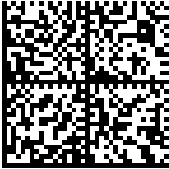
**AMD.BD487**

**-409-**



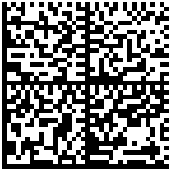
AMD.BD487

-410-



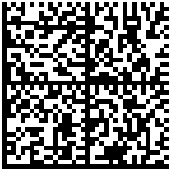
AMD.BD487

-411-



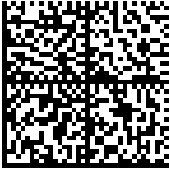
**AMD.BD487**

**-412-**



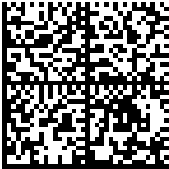
AMD.BD487

-413-



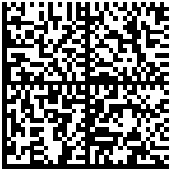
**AMD.BD487**

**-414-**



**AMD.BD487**

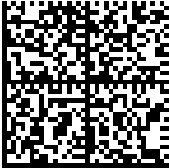
**-415-**



AMD.BD487

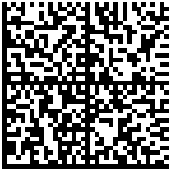
-416-





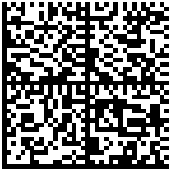
AMD.BD487

-417-



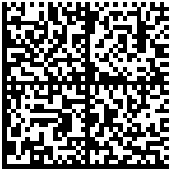
AMD.BD487

-418-



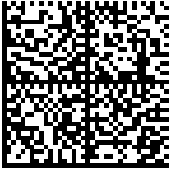
AMD.BD487

-419-



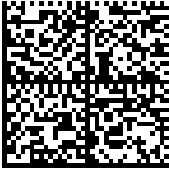
**AMD.BD487**

**-420-**



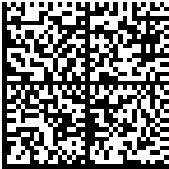
AMD.BD487

-421-



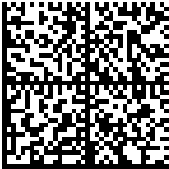
AMD.BD487

-422-



AMD.BD487

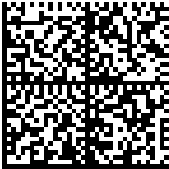
-423-



**AMD.BD487**

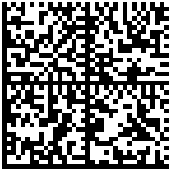
**-424-**





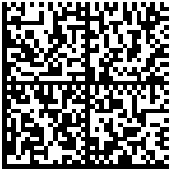
**AMD.BD487**

**-425-**



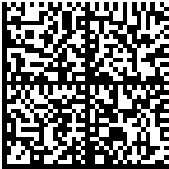
**AMD.BD487**

**-426-**



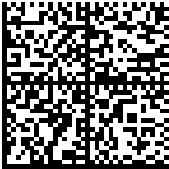
**AMD.BD487**

**-427-**



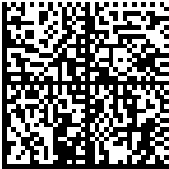
AMD.BD487

-428-



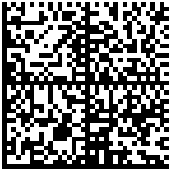
**AMD.BD487**

**-429-**



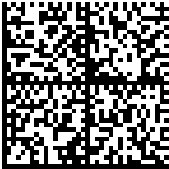
**AMD.BD487**

**-430-**



AMD.BD487

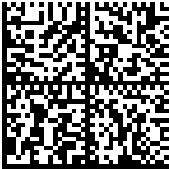
-431-



**AMD.BD487**

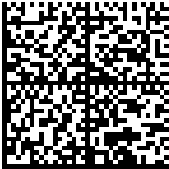
**-432-**





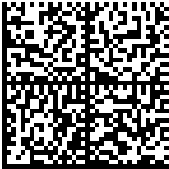
AMD.BD487

-433-



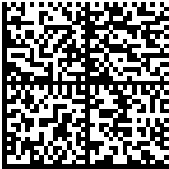
AMD.BD487

-434-



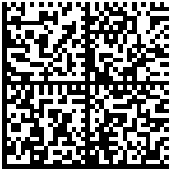
**AMD.BD487**

**-435-**



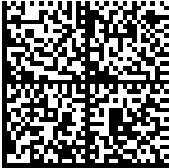
AMD.BD487

-436-



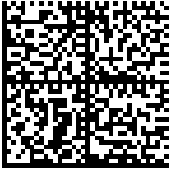
**AMD.BD487**

**-437-**



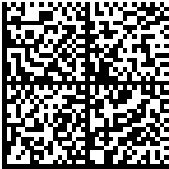
**AMD.BD487**

**-438-**



AMD.BD487

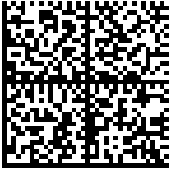
-439-



AMD.BD487

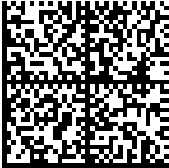
-440-





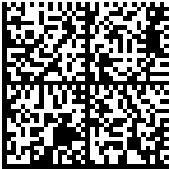
AMD.BD487

-441-



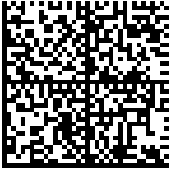
AMD.BD487

-442-

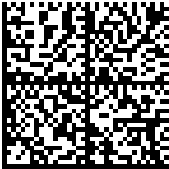


AMD.BD487

-443-

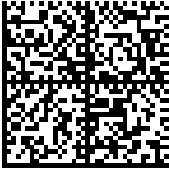


AMD.BD487



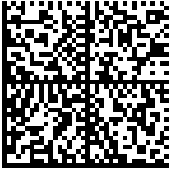
AMD.BD487

-445-



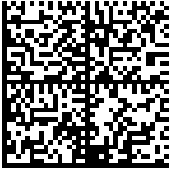
AMD.BD487

-446-



AMD.BD487

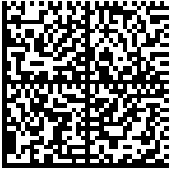
-447-



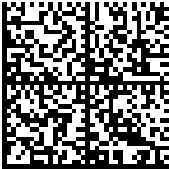
AMD.BD487

-448-



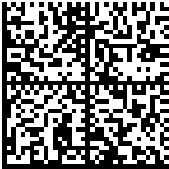


AMD.BD487



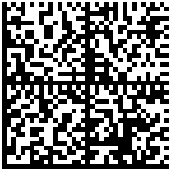
AMD.BD487

-450-



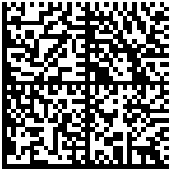
AMD.BD487

-451-



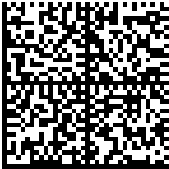
**AMD.BD487**

**-452-**



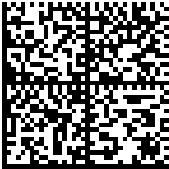
**AMD.BD487**

**-453-**



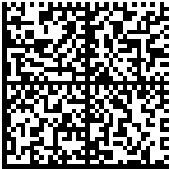
**AMD.BD487**

**-454-**



**AMD.BD487**

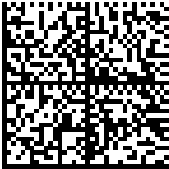
**-455-**



**AMD.BD487**

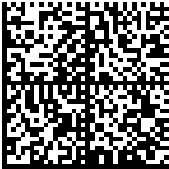
**-456-**





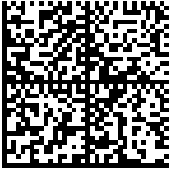
**AMD.BD487**

**-457-**



AMD.BD487

-458-



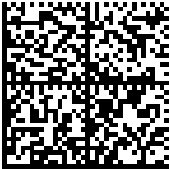
**AMD.BD487**

**-459-**



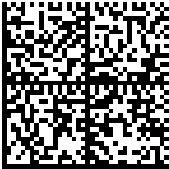
AMD.BD487

-460-



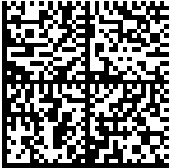
**AMD.BD487**

**-461-**



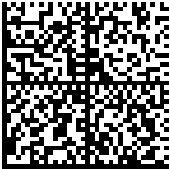
AMD.BD487

-462-



AMD.BD487

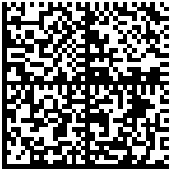
-463-



AMD.BD487

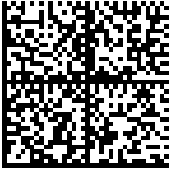
-464-





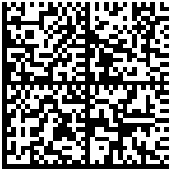
**AMD.BD487**

**-465-**



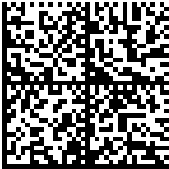
**-466-**

**AMD.BD487**



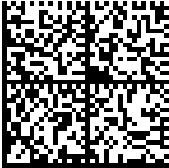
**AMD.BD487**

**-467-**



AMD.BD487

-468-



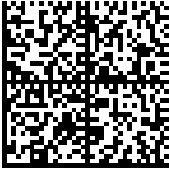
AMD.BD487

-469-



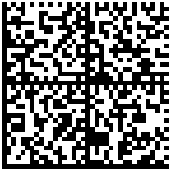
**AMD.BD487**

**-470-**



AMD.BD487

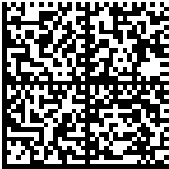
-471-



AMD.BD487

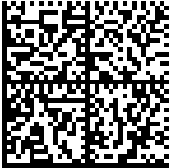
-472-





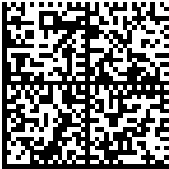
**AMD.BD487**

**-473-**



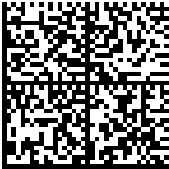
AMD.BD487

-474-



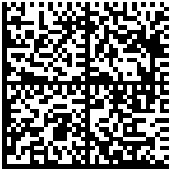
**AMD.BD487**

**-475-**



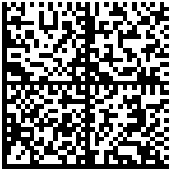
AMD.BD487

-476-



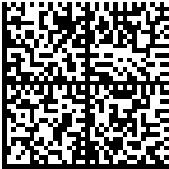
AMD.BD487

-477-



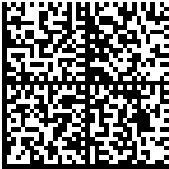
AMD.BD487

-478-



AMD.BD487

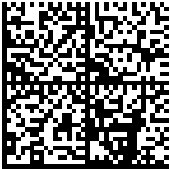
-479-



**AMD.BD487**

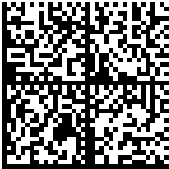
**-480-**





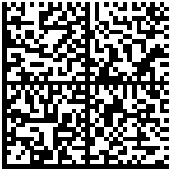
AMD.BD487

-481-



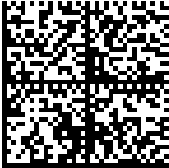
AMD.BD487

-482-



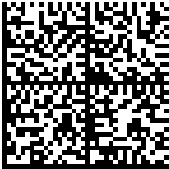
**-483-**

**AMD.BD487**



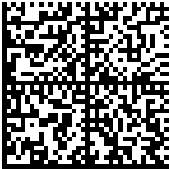
AMD.BD487

-484-



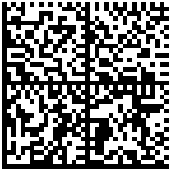
**AMD.BD487**

**-485-**



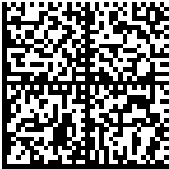
**AMD.BD487**

**-486-**



**AMD.BD487**

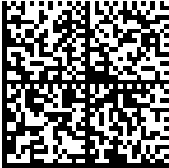
**-487-**



**-488-**

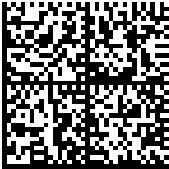
**AMD.BD487**





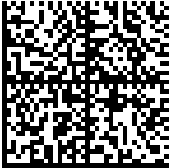
**AMD.BD487**

**-489-**



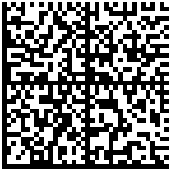
AMD.BD487

-490-



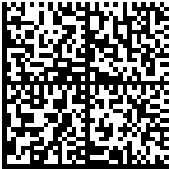
AMD.BD487

-491-



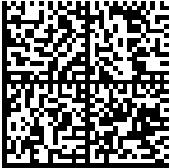
AMD.BD487

-492-



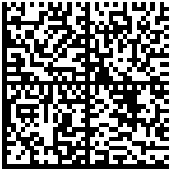
AMD.BD487

-493-



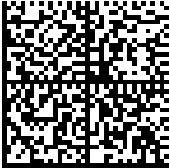
**AMD.BD487**

**-494-**



**AMD.BD487**

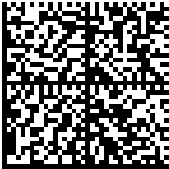
**-495-**



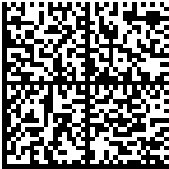
**AMD.BD487**

**-496-**



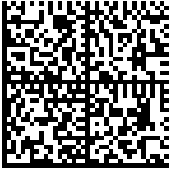


AMD.BD487



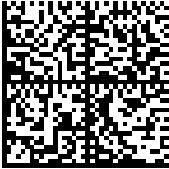
AMD.BD487

-498-



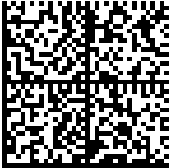
AMD.BD487

-499-



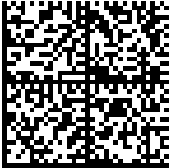
**-500-**

**AMD.BD487**



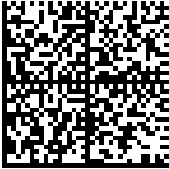
**AMD.BD487**

**-501-**



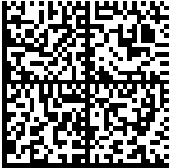
**AMD.BD487**

**-502-**



**AMD.BD487**

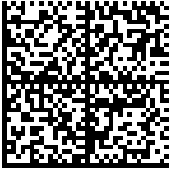
**-503-**



AMD.BD487

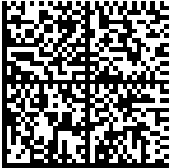
-504-





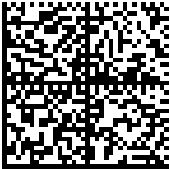
**-505-**

**AMD.BD487**



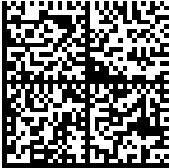
AMD.BD487

-506-



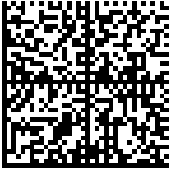
**AMD.BD487**

**-507-**



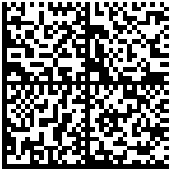
AMD.BD487

-508-



**AMD.BD487**

**-509-**



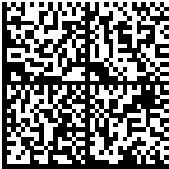
**AMD.BD487**

**-510-**



AMD.BD487

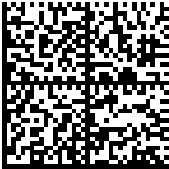
-511-



**-512-**

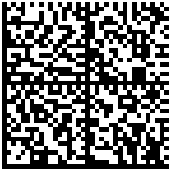
**AMD.BD487**





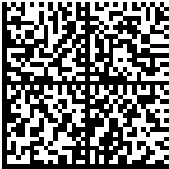
**AMD.BD487**

**-513-**



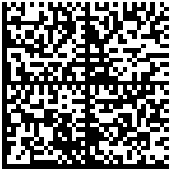
**AMD.BD487**

**-514-**



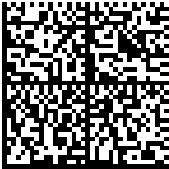
**AMD.BD487**

**-515-**



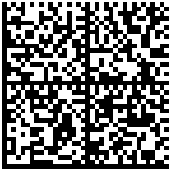
**AMD.BD487**

**-516-**



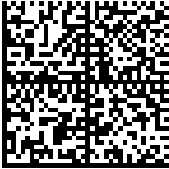
**AMD.BD487**

**-517-**



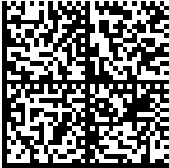
**AMD.BD487**

**-518-**



**-519-**

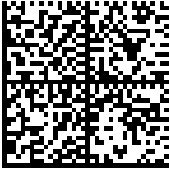
**AMD.BD487**



**AMD.BD487**

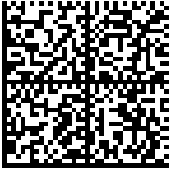
**-520-**





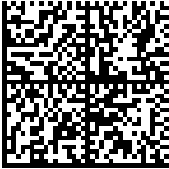
**AMD.BD487**

**-521-**



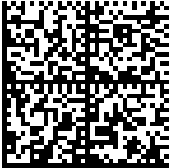
**AMD.BD487**

**-522-**



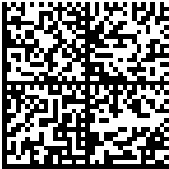
**AMD.BD487**

**-523-**



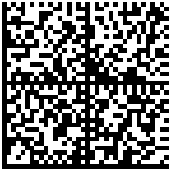
**AMD.BD487**

**-524-**



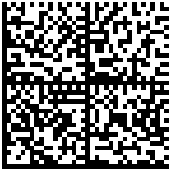
AMD.BD487

-525-



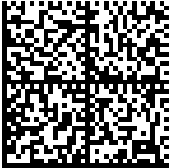
**AMD.BD487**

**-526-**



**AMD.BD487**

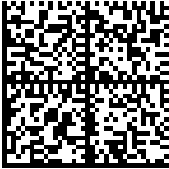
**-527-**



**AMD.BD487**

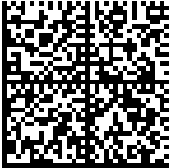
**-528-**





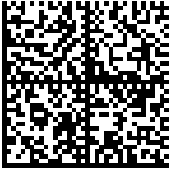
**AMD.BD487**

**-529-**



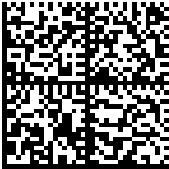
**AMD.BD487**

**-530-**



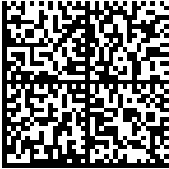
AMD.BD487

-531-



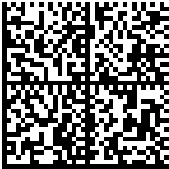
**-532-**

**AMD.BD487**



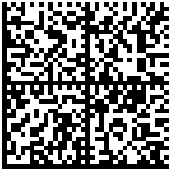
**AMD.BD487**

**-533-**



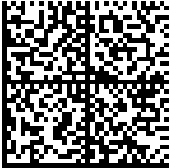
**AMD.BD487**

**-534-**



**AMD.BD487**

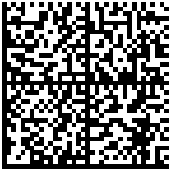
**-535-**



**AMD.BD487**

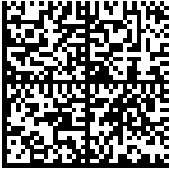
**-536-**





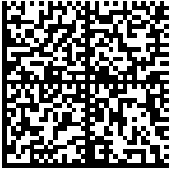
**AMD.BD487**

**-537-**



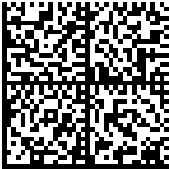
**AMD.BD487**

**-538-**



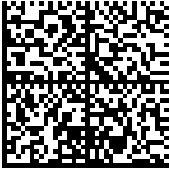
AMD.BD487

-539-



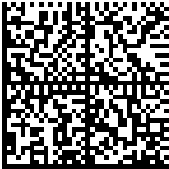
AMD.BD487

-540-

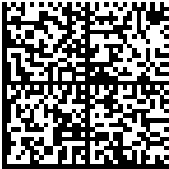


**AMD.BD487**

**-541-**

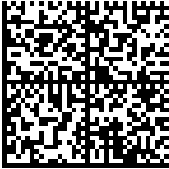


AMD.BD487



AMD.BD487

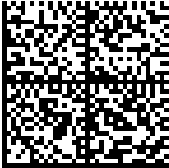
-543-



AMD.BD487

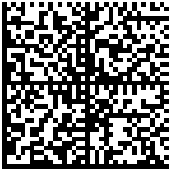
-544-





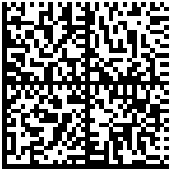
**AMD.BD487**

**-545-**



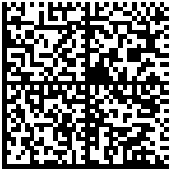
AMD.BD487

-546-



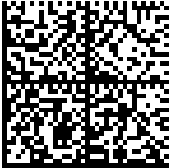
**AMD.BD487**

**-547-**



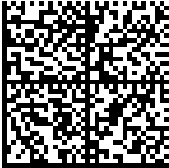
**AMD.BD487**

**-548-**



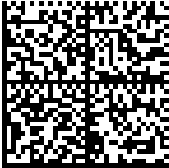
**AMD.BD487**

**-549-**



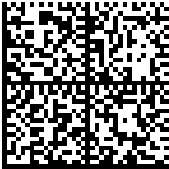
**AMD.BD487**

**-550-**



**AMD.BD487**

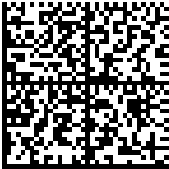
**-551-**



**AMD.BD487**

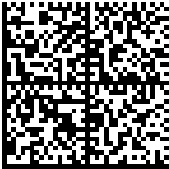
**-552-**





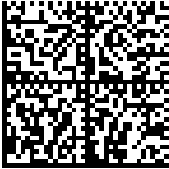
**AMD.BD487**

**-553-**



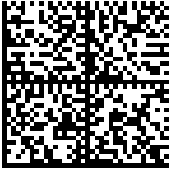
**AMD.BD487**

**-554-**



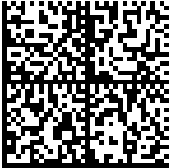
**-555-**

**AMD.BD487**



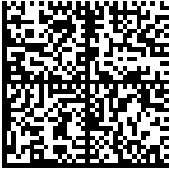
**AMD.BD487**

**-556-**



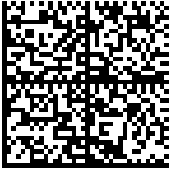
**AMD.BD487**

**-557-**



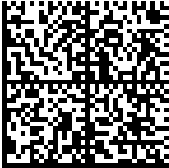
**AMD.BD487**

**-558-**



**AMD.BD487**

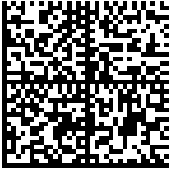
**-559-**



**AMD.BD487**

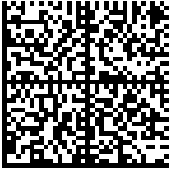
**-560-**





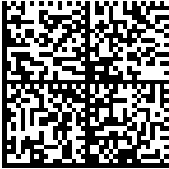
AMD.BD487

-561-



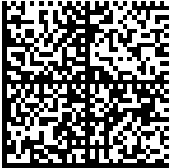
AMD.BD487

-562-



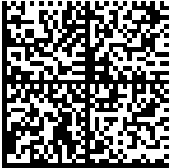
**AMD.BD487**

**-563-**



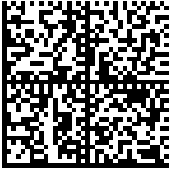
AMD.BD487

-564-



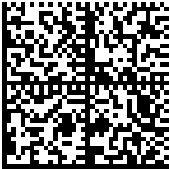
**AMD.BD487**

**-565-**



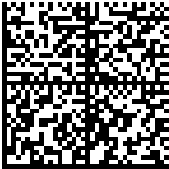
**AMD.BD487**

**-566-**



AMD.BD487

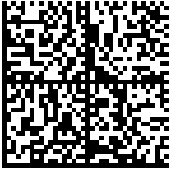
-567-



**-568-**

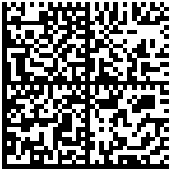
**AMD.BD487**





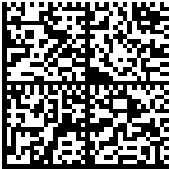
**-569-**

**AMD.BD487**



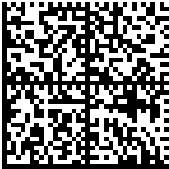
AMD.BD487

-570-



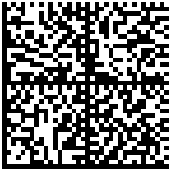
AMD.BD487

-571-



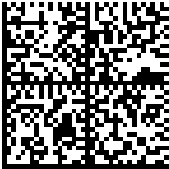
**AMD.BD487**

**-572-**



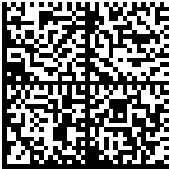
**AMD.BD487**

**-573-**



AMD.BD487

-574-



**AMD.BD487**

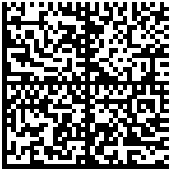
**-575-**



**AMD.BD487**

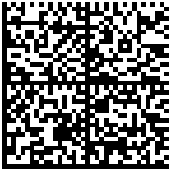
**-576-**





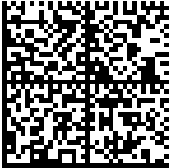
**AMD.BD487**

**-577-**



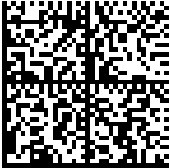
**AMD.BD487**

**-578-**



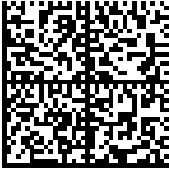
**AMD.BD487**

**-579-**



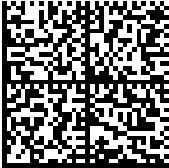
**-580-**

**AMD.BD487**



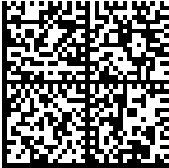
AMD.BD487

-581-



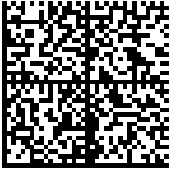
**AMD.BD487**

**-582-**



**AMD.BD487**

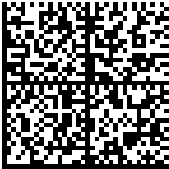
**-583-**



**AMD.BD487**

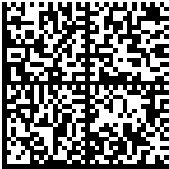
**-584-**





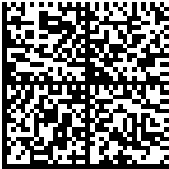
**AMD.BD487**

**-585-**



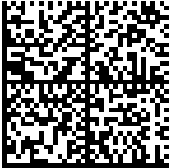
**AMD.BD487**

**-586-**



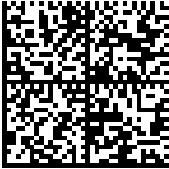
**AMD.BD487**

**-587-**



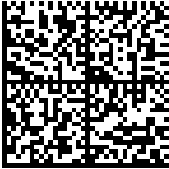
**AMD.BD487**

**-588-**



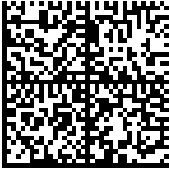
**-589-**

**AMD.BD487**



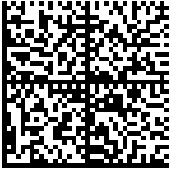
**AMD.BD487**

**-590-**



**AMD.BD487**

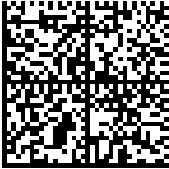
**-591-**



**AMD.BD487**

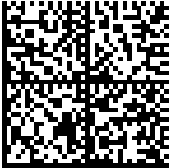
**-592-**





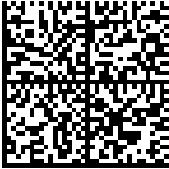
**-593-**

**AMD.BD487**



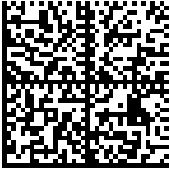
AMD.BD487

-594-



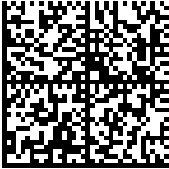
**-595-**

**AMD.BD487**



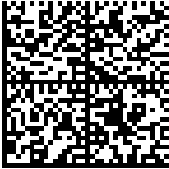
**-596-**

**AMD.BD487**



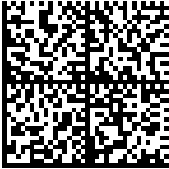
AMD.BD487

-597-



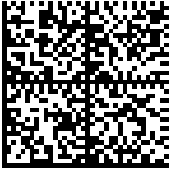
**-598-**

**AMD.BD487**



**-599-**

**AMD.BD487**



**-600-**

**AMD.BD487**