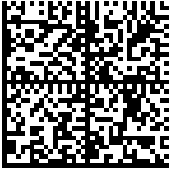


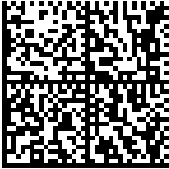
-1-

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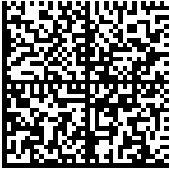
AMD.BD895



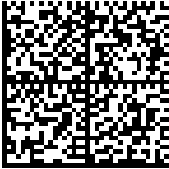
-3-

AMD.BD895

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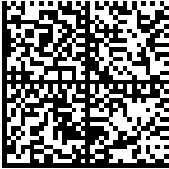
AMD.BD895



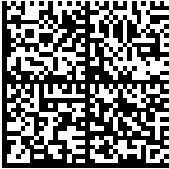
-5-

AMD.BD895

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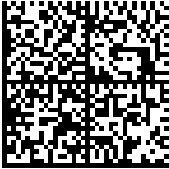


AMD.BD895



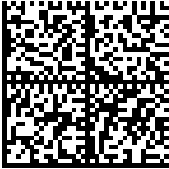
AMD.BD895

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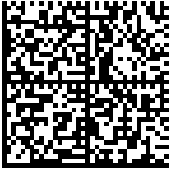


AMD.BD895

-9-

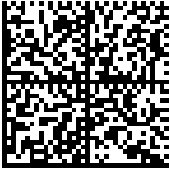


AMD.BD895

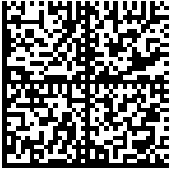


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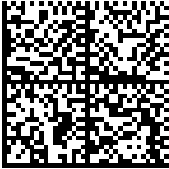
AMD.BD895



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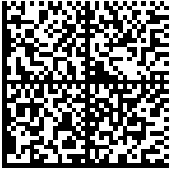


AMD.BD895

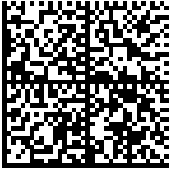


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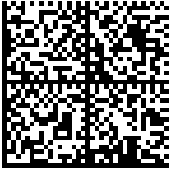
AMD.BD895



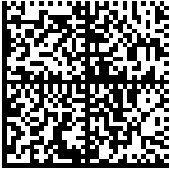
AMD.BD895



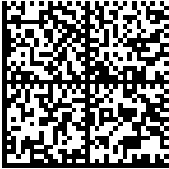
AMD.BD895



AMD.BD895

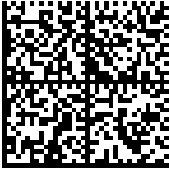


AMD.BD895



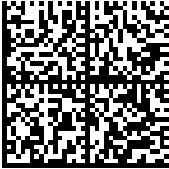
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